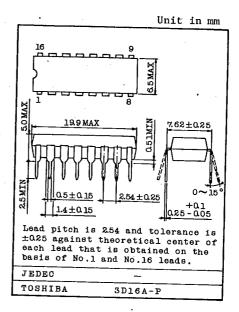
TC9135P 6CH MUTUAL RESET TYPE TOUCH SWITCH

TC9135P is a 6 circuit flip-flop IC having mutual reset action. When a "L" level signal is given to one input, corresponding output only is ON and all other outputs become OFF.

This IC is effective for electronization of function selector switch of audio system.

- (6-circuit mutual reset type) and (3-circuit mutual reset type) are available for use.
- Nch open drain large current buffer is employed and small type LED relays can be directly driven.
- Input has high impedance, and by providing a touch sensor externally, a touch sensor can be easily configurated.
- · C·MOS construction provides a broad range of operating voltage, and current consumption is low.



## MAXIMUM RATINGS (Ta=25°C)

| SYMBOL            | RATING   | UNIT   |
|-------------------|--|--|
| $v_{\mathrm{DD}}$ | 16   | v  |
| VIN               | -0.3 ∿ V <sub>DD</sub> +0.3  | v  |
| Vout              | -0.3 ∿ V <sub>DD</sub> +0.3  | v  |
| Iout              | 30   | <br>   |
| PD                | 600  | ńW   |
| Topr              | -30 ∿ 75   | °C   |
| Tstg              | -55∿ 125   | °C   |
|                   | V <sub>DD</sub> V <sub>IN</sub> V <sub>out</sub> I <sub>out</sub> P <sub>D</sub> | VDD     16       VIN     -0.3 ∿ VDD+0.3       Vout     -0.3 ∿ VDD+0.3       Iout     30       PD     600       Topr     -30 ∿ 75 |

### PIN CONNECTION

| GND    | 1 | $-\sqrt{\frac{16}{16}}$ | $v_{DD}$ |
|--------|---|-------------------------|----------|
| Out-1  | 2 | 15                      | IN-1     |
| Out-2  | 3 | 14                      | IN-2     |
| Out-3  | 4 | 13                      | IN-3     |
| Out-4  | 5 | 12                      | IN-4     |
| 0ut-5■ | 6 | 11                      | IN-5     |
| Out-6  | 7 | 10                      | IN-6     |
| . 6/3■ | 8 | 9                       | KS       |
|        |   |                         |          |

TOSHIBA

## TC9135P

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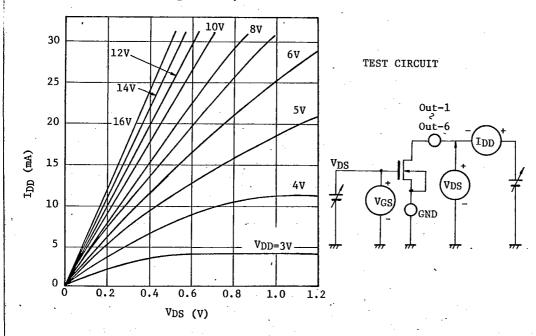
## ELECTRICAL CHARACTERISTICS (Unless otherwise specified, $V_{DD}$ =12V, Ta=25°C)

| CHARACTERISTIC                     |           | SYMBOL            | TEST<br>CIRCUIT | TEST CONDITION                                | MIN. | TYP. | MAX. | UNIT |
|------------------------------------|-----------|-------------------|-----------------|---|------|------|------|------|
| Operating Supply Voltage           |           | $v_{\mathrm{DD}}$ | ·-              |   | 3    | -    | 16   | V    |
| Quiescent Supply Current           |           | IDD               | _               | V <sub>DD</sub> =16.0V                        |      | -    | 20   | μA   |
| Input Current                      | "H" Level | IIH               |                 | V <sub>IH</sub> =12.0V,<br>All input terminal | -    | -    | 0.1  | μA   |
|                                    | "L" Level | IIL               |                 | VIL=0V,<br>All input terminal                 | -0.1 | -    | -    | μA   |
| Input Current<br>Threshold Voltage |           | VI-th             |                 | All input terminal                            |      | -    | 3.0  | V    |
| Output Current                     | "L" Level | IOL               |                 | $V_{DS}=1.0V$ , Out= $1 \sim 6$               | 20   | -    | -    | mA   |
|                                    | "H" Level | IOH .             |                 | V <sub>DS</sub> =1.0V,<br>KS terminal         | -    | -    | -1.0 | mA   |
|                                    | "L" Level | IOL               |                 | V <sub>DS</sub> =1.0V,<br>KS terminal         | 1.0  | _    | -    | mA   |
|                                    | "H" Level | IOH               |                 | Vps=16V,<br>Out=1∿6                           | -    | _    | 1.0  | μA   |

Characteristic of Output Buffer Transistor (Reference)

Characteristic of  $\mbox{\ensuremath{VDS}}$  to  $\mbox{\ensuremath{IDD}}$  at the time when  $\mbox{\ensuremath{VGS}}$  is taken as parameter (Reference value).

\* This IC is equivalent to  $V_{\rm GS}{=}V_{\rm DD},$  and  $V_{\rm DS}$  is the remaining voltage of output.



# TC9135P



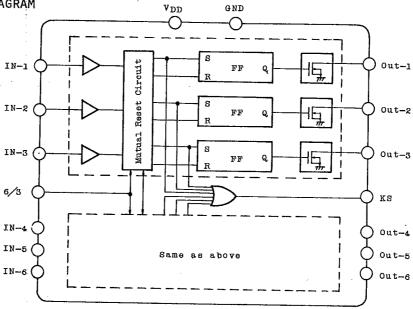
## T-65-05

## FUNCTIONAL EXPLANATION OF TERMINALS

| PIN   | SYMBOL            | TERMINAL                                      |  |
|-------|-------------------|---|--|
| NO.   | BINDOL            | NAME  | FUNCTION   |
| 15 ?  | IN-1<br>₹<br>IN-6 | Input signal<br>Terminals                     | When volatage applied to either one of 1 \( \infty \) 6 terminal is changed from "H" to "L" level, Nch FET of corresponding output is turned ON and other outputs are turned OFF. It is thus possible to correspond to both the feather-touch system by measures of a mechanical key and the touch switch by means of a touch sensor.  |
| 2 ? 7 | Out-1             | Output<br>Terminals                           | When corresponding input terminals are changed to "L" level, output Nch FET is turned ON and output level becomes "L" level. Even when input signal is returned to "H" level, output is kept at "L" level, but it is released if input is applied to other channels, and Nch FET is turned OFF and output is turned to an open state. As max 30mA of current can be applied to output Nch FET, it is possible to directly drive LED's and small size relays. |
| 9     | · KS              | Input<br>Detection<br>signal                  | KS output becomes "H" level only during the period when "L" level signal is given to input to either IN-1 $\sim$ IN-6.   |
| . 8   | 6/3               | 6ch/3ch × 2<br>switching<br>input<br>terminal | When this terminal is at "H" level, the internal 6-circuit flip-flops are all connected as mutual reset type. When it is at "L" level, the flip-flops are separated to 3ch mutual reset types; IN-1 $\sim$ IN-3 and IN-4 $\sim$ IN-6.  |
| 16    | V <sub>DD</sub>   |   |  |
| -1    | GND               | ·   |  |

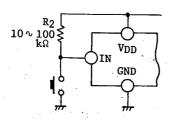
T-65-05

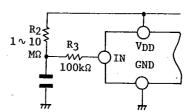
BLOCK DIAGRAM



## EXAMPLES OF APPLICATION CIRCUIT

- 1. INPUT CIRCUIT
  - a. Feather-touch key system
- b. Touch sensor system





- \* If an erroneous operation is a problem in a noisy equipment, a condenser of small capacity should be connected between IN and GND
- \*  $R_2$  should be decided with specification of sensor sensitivity,  $V_{DD}$  voltage, et.
- \* R3 is a static voltage breakdown protective resistor.

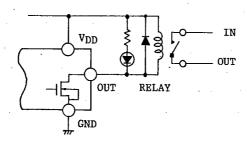
ESSENIO DIGITAL IC

TC9135P

T-65-05

#### 2. OUTPUT CIRCUIT

The output circuit is of Nch FET open drain type, capable of leading in current up to  $30\,\mathrm{mA}$ , and is able to directly drive LED's and relays.



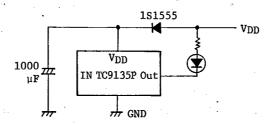
- \* Drive current should be limited to 30mA per output terminal.
- \* In the event using supply voltage is low, current capacity of output FET also drops and current up to 30mA may not be applied.

  (See the graph.)

When signal level only is required without driving LED's and relays, a load resistnace must be connected between  $\text{Out-V}_{\text{DD}}$ .

#### 3. STATUS BACK-UP

Current consumption of TC9135P itself is as very small as  $10\mu A$ , max., and  $V_{\rm DD}$  is operable up to 3V. Therefore, by providing a condenser to power supply , status back-up for an extended period of time is possible.



**TOSHIRA** 

T-65-05

### 4. INITIALIZATION AT TIME POWER IS TURNED ON

If the initialization of a specific channel is desirable at time power is turned ON, the initialization is possible by connecting a condenser of small capacity to the input to a desired channel.

