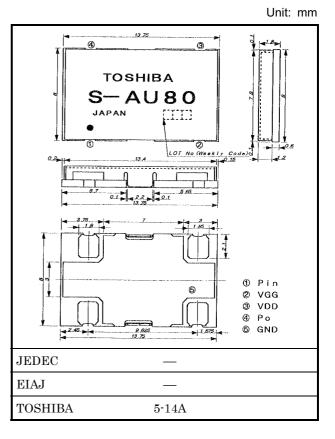
TOSHIBA RF Power Amplifire Module

S - A U 8 0

900 MHz Band Amplifier Applications (GSM)

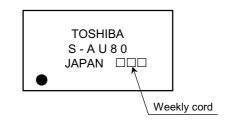
- Output Power: Po = 35.0 dBmW (typ.)
- Power Gain: $G_p = 35.0 \text{ dB}$ (typ.)
- Total Efficiency: $\eta T = 43\%$ (typ.)



Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit	
DC Supply Voltage	V _{DD}	8 (Note1)	V	
DC Supply Voltage	V _{GG}	5 (Note2)	V	
DC Current	I _{DD}	5	А	
Input Power	Pi	6	dBmW	
Output Power	Po	36 (Note3)	dBmW	
Operating Case Temperature Range	T _{c (opr)}	-30~85	°C	
Storage Temperature Range	T _{stg}	-40~110	°C	

Type Name



Note1: This value is specified at no operation ($V_{GG} = 0 V$, $P_i = none$)

Note2: This value is specified at no operation ($V_{DD} = 0 V$, $P_i = none$)

Note3: This value is specified at no 50 Ω load operation

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others

The information contained herein is subject to change without notice.

Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition		Min	Тур.	Max	Unit
Frequency range	f _{range}	_		880		915	MHz
Leakage current	I _{leak}	$V_{DD} = 6.0, V_{GG} = 0 V$		_	500		μA
Output power	Po	P_i = 0 dBmW, V_{DD} = 3.6 V, V_{GG} \leq 2.5 V Z_G = Z_L = 50 Ω		34.5	35.0		dBmW
Power gain	Gp			34.5	35.0		dB
Total efficiency (Note4)	Eff	$\begin{array}{l} P_{i}=0 \text{ dBmW}, V_{DD}=3.6 V, V_{GG} \leq 2.5 V \\ P_{o}=34.5 \text{ dBmW}, Z_{G}=Z_{L}=50 \Omega \end{array}$		37	43	_	%
Input VSWR	VSWR	P _i = 0 dBmW, V _{DD} = 3.6 V, V _{GG} \leq 2.5 V P _o = 34.5 dBmW, Z _G = Z _L = 50 Ω		_	_	3.0	_
Control current	I _{cont}			_	0.5	1.0	mA
2nd harmonics	2 nd HRM			_	-45	-35	dB
3rd harmonics	3 rd HRM			_	-50	-40	dB
Low voltage power	P _o -L	$\begin{array}{l} {\sf P}_i = 0 \ d{\sf B}m{\sf W}, \ {\sf V}_{DD} = 3.2 \ {\sf V}, \ {\sf V}_{GG} \leqq 2.5 \ {\sf V} \\ {\sf Z}_G = {\sf Z}_L = 50 \ \Omega, \ {\sf T}_C = 85^\circ {\sf C} \end{array}$		33.5	34.0	_	dBmW
Isolation	P _o -iso	$\label{eq:pi_basis} \begin{array}{l} P_i = 0 \text{ dBmW}, \ V_{DD} = 3.6 \ V, \ V_{GG} = 0.3 \ V \\ Z_G = Z_L = 50 \ \Omega \end{array}$		_	-40	-37	dBmW
AM-AM conversion AM _{con} P _{in} P _o		P _{i1} = 0 dBmW, P _{i2} = -40 dBmW P _{in2} = P _{i1} + 200 kHz, V _{DD} = 3.6 V	f0 – 200 kHz		-30	_	dB
	$P_0 = 7 - 34.5 \text{ dBmW} (V_{GG} = \text{adjust})$ $Z_G = Z_L = 50 \Omega$	f0 + 200 kHz		-30	_	dB	
Switching time (Note5)	t _r /t _f				1.0		μs
Noise Power NRB	$f0 = 915 \text{ MHz}, P_{in} = 0 \text{ dBmW}$	f0 + 20 MHz	—	-82	—	dBmW	
	NKD	$P_0 = 34.5 \text{ dBmW}$, RBW = 100 kHz V _{DD} = 3.6 V, V _{GG} = adjust	f0 + 10 MHz		-78	_	dBmW
Load Mismatch		$\begin{array}{l} P_{i}=0 \text{ dBmW}, \text{ V}_{DD}=3.24.3 \text{ V} \\ P_{o} \leq 34.5 \text{ dBmW} (\text{V}_{GG}=\text{adjust}), \text{ Z}_{G}=50 \ \Omega \\ \text{VSWR LOAD 6:1 ALL PHASE} \end{array}$		No degradation			_
Stability		$\begin{array}{l} P_{i} = 0 \text{ dBmW}, \text{V}_{DD} = 3.24.3 \text{ V} \\ V_{GG} = 02.5 \text{ V}, \text{Z}_{G} = 50 \ \Omega \\ P_{o} \leq 34.5 \text{ dBmW} \ (@\text{Z}_{L} = 50 \ \Omega) \\ \text{VSWR LOAD 6:1 ALL PHASE} \end{array}$		All spurious output than 60 dB below desired signal			_

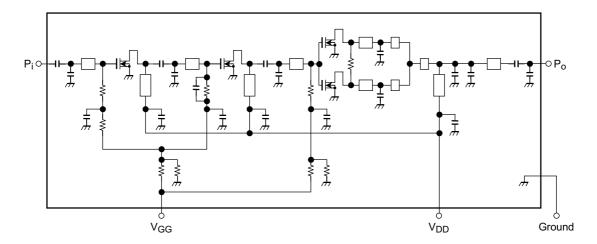
Note4: Output power P₀ is defined at the root point of the module output pin P₀. The coefficient of output power loss in the P.C.B. output is showed as follows: $1/(S21)^{2} = 1/(0.9809)^{2} = 1.04$

Note5: GSM pulse is applied to V_{GG} (1/8 duty 575 μ s)

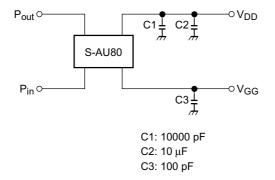
*: This transistor is the electrostatic sensitive device. Please handle with caution.

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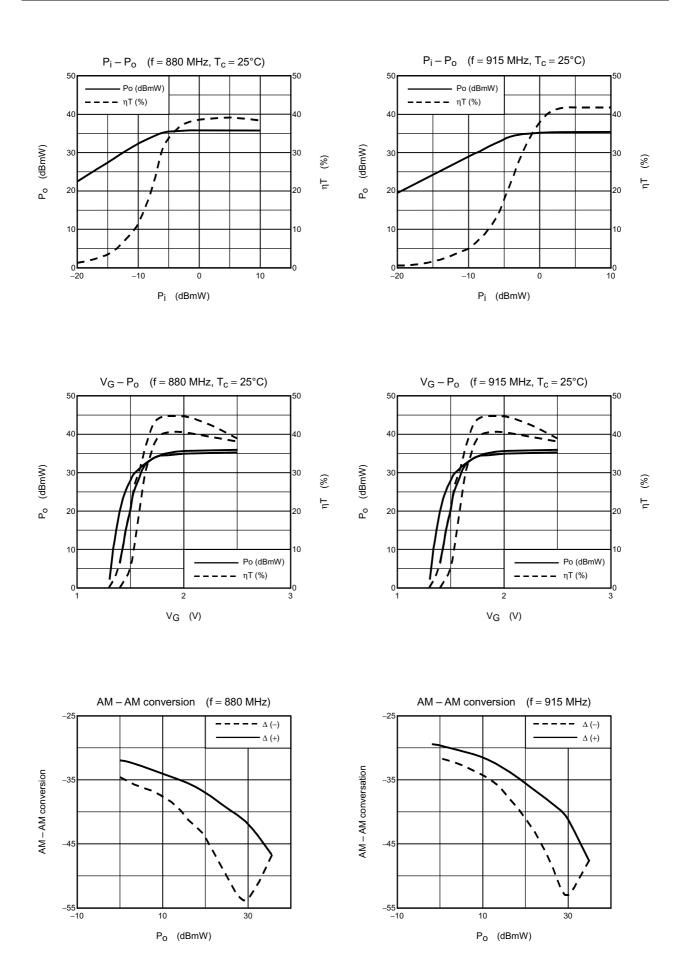
Schematic



Test Circuit



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*: These are only typical curves and devices are not necessarily guaranteed at these curves.