TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7PA14FU

Dual Schmitt Inverter with 3.6 V Tolerant Input and Output

Features

- Operating voltage range: $V_{CC} = 1.8 \sim 3.6 \text{ V}$
- High-speed operation: $t_{pd} = 4.0 \text{ ns (max)}$ at $V_{CC} = 3.0 \sim 3.6 \text{ V}$

 $t_{pd} = 4.3 \text{ ns (max)}$ at $V_{CC} = 2.3 \sim 2.7 \text{ V}$

 t_{pd} = 8.6 ns (max) at V_{CC} = 1.8 V

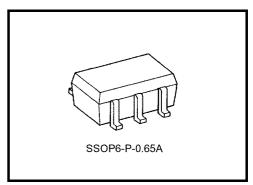
• High-level output current:

 $I_{OH}/I_{OL} = \pm 24 \text{ mA (min)}$ at $V_{CC} = 3.0 \text{ V}$

 $IOH/IOL = \pm 18 \text{ mA (min)}$ at VCC = 2.3 V

 $I_{OH}/I_{OL} = \pm 6 \text{ mA (min)}$ at $V_{CC} = 1.8 \text{ V}$

- 3.6-V tolerant inputs.
- Power down protection is provided on all outputs.



Weight: 0.0068 g (typ.)

Maximum Ratings (Ta = 25°C)

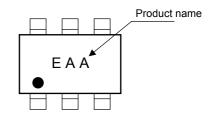
| Characteristics | Symbol | Symbol Value | | |
|------------------------------------|------------------|--|----|--|
| Power supply voltage | V _{CC} | -0.5~4.6 | V | |
| DC input voltage | V _{IN} | -0.5~4.6 | V | |
| | | -0.5~4.6 (Note 1) | | |
| DC output voltage | V _{OUT} | -0.5~V _{CC} + 0.5 (Note 2) | V | |
| Input diode current | I _{IK} | -50 | mA | |
| Output diode current | lok | ±50 (Note 3) | mA | |
| DC output current | lout | ±50 | mA | |
| Power dissipation | PD | 200 | mW | |
| DC V _{CC} /ground current | Icc | ±100 | mA | |
| Storage temperature | T _{stg} | -65~150 | °C | |

Note 1: $V_{CC} = 0 V$

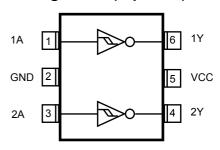
Note 2: High or Low state. IOUT absolute maximum rating must be observed.

Note 3: V_{OUT} < GND, V_{OUT} > V_{CC}

Marking



Pin Assignment (top view)



Truth Table

| Α | Υ |
|---|---|
| L | Н |
| Н | L |

IEC Logic Symbol



Recommended Operating Conditions

| Characteristics | Symbol | Value | Unit | | |
|-----------------------|----------------------------------|----------------------------|------|--|--|
| Dowar cupply voltage | V | 1.8~3.6 | | | |
| Power supply voltage | V _{CC} | 1.2~3.6 (Note 4) | V | | |
| Input voltage | V _{IN} | -0.3~3.6 | V | | |
| Output voltage | V _{OUT} | 0~3.6 (Note 5) | V | | |
| Output voltage | | 0~V _{CC} (Note 6) | V | | |
| | I _{OH} /I _{OL} | ±24 (Note 7) | | | |
| Output Current | | ±18 (Note 8) | mA | | |
| | | ±6 (Note 9) | | | |
| Operating temperature | T _{opr} | -40~85 | °C | | |

Note 4: Data retention only

Note 5: $V_{CC} = 0 V$

Note 6: High or Low state

Note 7: $V_{CC} = 3.0 \sim 3.6 \text{ V}$

Note 8: $V_{CC} = 2.3 \sim 2.7 \text{ V}$

Note 9: $V_{CC} = 1.8 \text{ V}$

DC Electrical Characteristics (Ta = $-40\sim85^{\circ}$ C, 2.7 V < V_{CC} \leq 3.6 V)

| Characteristics | Symbol | Too | Test Condition | | Min | | Unit |
|---------------------------------------|--|--|---------------------------|---------|--------------------------|-------|------|
| Characteristics | Symbol | 163 | | | IVIIII | Max | 5 |
| Positive Threshold Voltage | VP | | | | _ | 2.2 | V |
| Positive Tilleshold Voltage | VP | | _ | 3.0 | _ | 2.0 | V |
| Negative Threehold Voltage | V _i . | | | 3.6 | 0.8 | _ | V |
| Negative Threshold Voltage | ٧N | V _N | 3.0 | 0.7 | _ | V | |
| Llustereaus Valtage | V _i . | | | | 0.3 | 1.2 | V |
| Hysteresys Voltage | V _H | | _ | 3.0 | 0.3 | 1.2 | V |
| | _evel Output Voltage V _{OH} V _{IN} = V _{IL} | | I _{OH} = -100 μA | 2.7~3.6 | V _{CC} - 0.2 | _ | |
| High-Level Output Voltage | | $V_{IN} = V_{IL}$ | I _{OH} = -12 mA | 2.7 | 2.2 | _ | |
| | | | I _{OH} = -18 mA | 3.0 | 2.4 | _ | |
| | | | $I_{OH} = -24 \text{ mA}$ | 3.0 | 2.2 | _ | V |
| | | | I _{OL} = 100 μA | 2.7~3.6 | _ | 0.2 | |
| Low-Level Output Voltage | V _{OL} | V _{IN} = V _{IH} | I _{OL} = 12 mA | 2.7 | _ | 0.4 | |
| Low-Level Output voltage | VOL | VIN = VIH | I _{OL} = 18 mA | 3.0 | _ | 0.4 | |
| | | | I _{OL} = 24 mA | 3.0 | _ | 0.55 | |
| Input Leakage Current | I _{IN} | V _{IN} = 0~3.6 V | V _{IN} = 0~3.6 V | | _ | ±5.0 | μА |
| Power-off Leakage Current | loff | V _{IN} , V _{OUT} = 0~3.6 V | | 0 | _ | 10.0 | μА |
| Quiescent Supply Current | I _{CC} | V _{IN} = V _{CC} or GND | | 2.7~3.6 | _ | 20.0 | |
| Quiescent Supply Current | | $V_{CC} \leq (V_{IN}, V_{CC})$ | _{OUT}) ≦ 3.6 V | 2.7~3.6 | _ | ±20.0 | μΑ |
| Increase in I _{CC} per Input | Δl _{CC} | $V_{IH} = V_{CC} - 0.$ | 6 V | 2.7~3.6 | _ | 750 | |

DC Characteristics (Ta = $-40\sim85^{\circ}$ C, 2.3 V \leq V_{CC} \leq 2.7 V)

| Characteristics | Symbol | Too | Test Condition | | Min | Max | Unit |
|---|------------------|---|--|---------------------|--------------------------|-------|-------|
| Characteristics | Symbol | Test solidition | | V _{CC} (V) | IVIIII | IVIAX | Offic |
| Positive Threshold Voltage | V_{P} | | _ | 2.3 | _ | 1.8 | V |
| Negative Threshold Voltage | V _N | | _ | 2.3 | 0.5 | _ | v |
| Hysteresys Voltage | V _H | | _ | | 0.3 | 1.0 | V |
| | | | I _{OH} = -100 μA | 2.3~2.7 | V _{CC} - 0.2 | _ | |
| High-Level Output Voltage V _{OH} | V _{OH} | $V_{IN} = V_{IL}$ | I _{OH} = -6 mA | 2.3 | 2.0 | _ | |
| | | | I _{OH} = -12 mA | 2.3 | 1.8 | _ | |
| | | | I _{OH} = -18 mA | 2.3 | 1.7 | _ | V |
| | | | I _{OL} = 100 μA | 2.3~2.7 | _ | 0.2 | |
| Low-Level Output Voltage | V _{OL} | $V_{IN} = V_{IH}$ | I _{OL} = 12 mA | 2.3 | | 0.4 | |
| | | | I _{OL} = 18 mA | 2.3 | | 0.6 | |
| Input Leakage Current | I _{IN} | V _{IN} = 0~3.6 V | | 2.3~2.7 | | ±5.0 | μА |
| Power-off Leakage Current | l _{OFF} | V _{IN} , V _{OUT} = 0~ | V _{IN} , V _{OUT} = 0~3.6 V | | _ | 10.0 | μА |
| Quiescent Supply Current | laa | $V_{IN} = V_{CC}$ or G | V _{IN} = V _{CC} or GND | | | 20.0 | |
| | Icc | $V_{CC} \leq (V_{IN}, V_{CC})$ | $V_{CC} \le (V_{IN}, V_{OUT}) \le 3.6 \text{ V}$ | | _ | ±20.0 | μΑ |

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DC Characteristics (Ta = $-40\sim85^{\circ}$ C, 1.8 V \leq V_{CC} < 2.3 V)

| Characteristics | Symbol | Test Condition | | V _{CC} (V) | Min | Max | Unit | |
|----------------------------|------------------|--|---------------------------|---------------------|--------------------------|-------|------|---|
| Positeve Threshold Voltage | V _P | | _ | 1.8 | _ | 1.4 | V | |
| Negative Threshold Voltage | V _N | | _ | 1.8 | 0.25 | _ | V | |
| Hysteresys Voltage | V _H | | _ | | 0.2 | 0.95 | V | |
| High-Level Output Voltage | VoH | V _{IN} = V _{IL} | I _{OH} = -100 μA | 1.8 | V _{CC} - 0.2 | _ | | |
| | | | - | | $I_{OH} = -6 \text{ mA}$ | 1.8 | 1.4 | _ |
| Low-Level Output Voltage | Va | Var. Var. | $I_{OL} = 100 \mu A$ | 1.8 | _ | 0.2 | | |
| Low-Level Output Voltage | V _{OL} | $V_{IN} = V_{IH}$ | $I_{OL} = 6 \text{ mA}$ | 1.8 | _ | 0.3 | | |
| Input Leakage Current | I _{IN} | V _{IN} = 0~3.6 V | V _{IN} = 0~3.6 V | | | ±5.0 | μА | |
| Power-off Leakage Current | l _{OFF} | V _{IN} , V _{OUT} = 0~3.6 V | | 0 | | 10.0 | μА | |
| Quiescent Supply Current | Icc | V _{IN} = V _{CC} or GND | | 1.8 | _ | 20.0 | μА | |
| Quiescent Supply Current | 100 | V _{CC} ≤ (V _{IN} , V _{OU} | T) ≦ 3.6 V | 1.8 | _ | ±20.0 | μΑ | |

AC Electrical Characteristics (Ta = $-40 \sim 85$ °C, input $t_r = t_f = 2.0$ ns, $C_L = 30$ pF, $R_L = 500$ Ω)

| Characteristics | Symbol | Test Condition | V _{CC} (V) | Min | Max | Unit |
|----------------------------------|----------|------------------|---------------------|-----|-----|------|
| Propagation delay time tpHL tpHL | 4 | | 1.8 | 1.0 | 8.6 | |
| | · · | (Figure 1 and 2) | 2.5 ± 0.2 | 8.0 | 4.3 | ns |
| | фнг | | 3.3 ± 0.3 | 0.6 | 4.0 | |

For $C_L = 50 \ pF$, add approximately 300 ps to the AC maximum specification.

Dynamic Switching Characteristics (Ta = 25°C, input $t_r = t_f = 2.0$ ns, $C_L = 30$ pF)

| Characteristics | Symbol | Test Condition | TYP. | Unit | |
|------------------------------|------------------------------|--|---------------------|-------|-------|
| Characteristics | Symbol | rest Condition | V _{CC} (V) | | Offic |
| Quiet Output Maximum Dynamic | Quiet Output Maximum Dynamic | $V_{IN} = 1.8 \text{ V}, V_{IL} = 0 \text{ V}$ (Note | 11) 1.8 | 0.25 | |
| V _{OL} | | $V_{IN} = 2.5 \text{ V}, V_{IL} = 0 \text{ V} \tag{Note}$ | 11) 2.5 | 0.6 | ns |
| V OL | | $V_{IN} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$ (Note | 11) 3.3 | 0.8 | |
| Quiet Output Minimum Dynamic | | $V_{IN} = 1.8 \text{ V}, V_{IL} = 0 \text{ V}$ (Note | 11) 1.8 | -0.25 | |
| Voi | V_{OLV} | $V_{IN} = 2.5 \text{ V}, V_{IL} = 0 \text{ V} \tag{Note}$ | 11) 2.5 | -0.6 | ns |
| V OL | | $V_{IN} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$ (Note | 11) 3.3 | -0.8 | |
| Quiet Output Minimum Dynamic | | $V_{IN} = 1.8 \text{ V}, V_{IL} = 0 \text{ V}$ (Note | 11) 1.8 | 1.5 | |
| | V_{OLP} | $V_{IN} = 2.5 \text{ V}, V_{IL} = 0 \text{ V}$ (Note | 11) 2.5 | 1.9 | ns |
| V _{OH} | | $V_{IN} = 3.3 \ V, \ V_{IL} = 0 \ V \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \$ | 11) 3.3 | 2.2 | |

Note 11: Characteristics guaranteed by design.

Capacitive Characteristics (Ta = 25°C)

| Characteristics | Symbol | Test Condition | | V _{CC} (V) | TYP. | Unit | |
|-------------------------------|-----------------|--------------------------|---|---------------------|---------------|------|----|
| Input Capacitance | C _{IN} | | _ | | 1.8, 2.5, 3.3 | 4 | pF |
| Power Dissipation Capacitance | C _{PD} | f _{IN} = 10 MHz | | (Note 12) | 1.8, 2.5, 3.3 | 27 | pF |

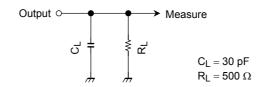
Note 12: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

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Average operating current can be obtained by the equation:

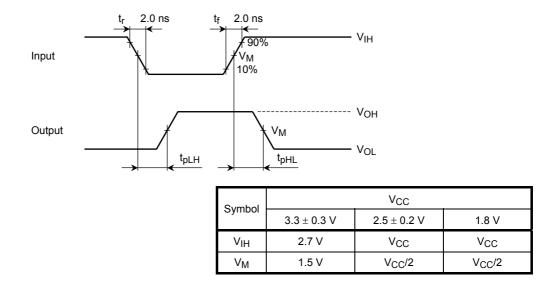
 $I_{CC\ (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2$

Figure 1 Test Circuit



AC Waveforms

Figure 2 t_{pLH}, t_{pHL}

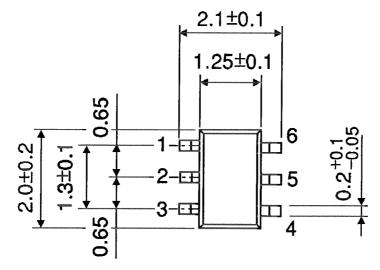


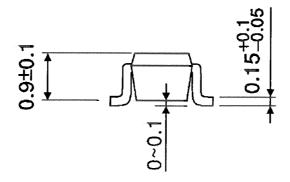
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Package Dimensions

SSOP6-P-0.65A

Unit: mm





Weight: 0.0068 g (typ.)

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Handbook" etc..

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