TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SG02AFS

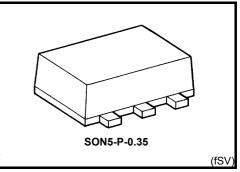
2 Input NOR Gate

Features

- High-level output current: I_{OH}/I_{OL} = ±8 mA (min)
- $\label{eq:VCC} \mbox{at V}_{CC} = 3.0 \mbox{ V}$ $\mbox{High-speed operation: } t_{pd} = 2.4 \mbox{ ns (typ.)}$

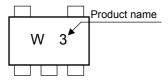
at V_{CC} = 3.3 V,15pF

- Operating voltage range: V_{CC} = 0.9~3.6 V
- 5.0-V tolerant inputs.

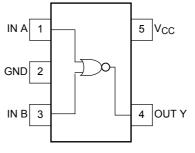


Weight: 0.001 g (typ.)

Marking







Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Value	Unit
Power supply voltage	V _{CC}	-0.5~4.6	V
DC input voltage	VIN	-0.5~7.0	V
DC output voltage	Vout	-0.5~ V _{CC} + 0.5	V
Input diode current	I _{IK}	-20	mA
Output diode current	I _{OK}	±20 (Note 1)	mA
DC output current	IOUT	±25	mA
DC V _{CC} /ground current	ICC	±50	mA
Power dissipation	PD	50	mW
Storage temperature	T _{stg}	-65~150	°C

Note 1: $V_{OUT} < GND, V_{OUT} > V_{CC}$

<u>TOSHIBA</u>

Truth Table

А	В	Y
L	L	Н
L	Н	L
Н	L	L
Н	Н	L

IEC Logic Symbol



Recommended Operating Conditions

Characteristics	Symbol	Value	Unit
Power supply voltage	V _{CC}	0.9~3.6	V
Input voltage	V _{IN}	0~5.5	V
Output voltage	V _{OUT}	0~V _{CC}	V
Output Current		±8.0 (Note 2)	
	IOH/IOL	±4.0 (Note 3)	
		±3.0 (Note 4)	mA
		±1.7 (Note 5)	ma
		±0.3 (Note 6)	
		±0.02 (Note 7)	
Operating temperature	T _{opr}	-40~85	°C
Input rise and fall time	dt/dV	0~10 (Note 8)	ns/V

Note 2: V_{CC} = 3.0~3.6 V

Note 3: V_{CC} = 2.3~2.7 V

Note 4: V_{CC} = 1.65~1.95 V

Note 5: V_{CC} = 1.4~1.6 V

Note 6: $V_{CC} = 1.1 \sim 1.3 V$

Note 7: $V_{CC} = 0.9 V$

Note 8: $V_{IN} = 0.8 \sim 2.0 \text{ V}, V_{CC} = 3.0 \text{ V}$

DC Electrical Characteristics

Characteristics	Sumbol	Test	Test Condition			Ta = 25°C			Ta = −40~85°C		Unit
Characteristics	Symbol	Circuit	Test			Min	Тур.	Max	Min	Max	Onit
				0.9	V _{CC}	_		V _{CC}	—		
					1.1~1.3	$V_{CC} \times 0.7$		_	V _{CC} × 0.7	_	V
High-level input	V _{IH}		_		1.4~1.6	V _{CC} × 0.65		_	V _{CC} × 0.65	_	
voltage	voltage		-		V _{CC} × 0.65			V _{CC} × 0.65	_		
					2.3~2.7	1.7	_		1.7	—	
					3.0~3.6	2.0		_	2.0	_	
					0.9		_	GND	_	GND	
					1.1~1.3	—		$\begin{array}{c} V_{CC} \\ \times \ 0.3 \end{array}$	_	$\begin{array}{c} V_{CC} \\ \times \ 0.3 \end{array}$	V
Low-level input voltage	V _{IL}	_		_	1.4~1.6	—		V _{CC} × 0.35	_	$\begin{array}{c} V_{CC} \\ \times \ 0.35 \end{array}$	
vollage					1.65~1.95	_		V _{CC} × 0.35		V _{CC} × 0.35	
					2.3~2.7		_	.0.7		0.7	
					3.0~3.6		_	0.8		0.8	
				I _{OH} =-0.02 mA	0.9	0.75		_	0.75	_	- - -
		V _{OH} —		I _{OH} = -0.3 mA	1.1~1.3	V _{CC} × 0.75	_	_	V _{CC} × 0.75	_	
igh-level output	V _{OH}		V _{IN} = V _{IL}	I _{OH} = -1.7 mA	1.4~1.6	V _{CC} × 0.75		_	$\begin{array}{c} V_{CC} \\ \times \ 0.75 \end{array}$		
voltage			I _{OH} = -3.0 mA	1.65~ 1.95	V _{CC} -0.45		_	V _{CC} -0.45	_		
				I _{OH} = -4.0 mA	2.3~2.7	2.0	_	—	2.0	_	
				I _{OH} = -8.0 mA	3.0~3.6	2.48	_		2.48	_	
		Voi Vii		$I_{OL} = 0.02 \text{ mA}$	0.9		_	0.1		0.1	
				I _{OL} = 0.3 mA	1.1~1.3	_		V _{CC} × 0.25	_	V _{CC} × 0.25	
Low-level output	V _{OL}		V _{IN} = V _{IH}	I _{OL} = 1.7 mA	1.4~1.6	_		V _{CC} × 0.25		$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$	V
voltage		or V _{IL}	V _{IL} I _{OL} = 3.0 mA	1.65~ 1.95	_		0.45	_	0.45		
				I _{OL} = 4.0 mA	2.3~2.7			0.4		0.4	
				I _{OL} = 8.0 mA	3.0~3.6	_	_	0.4	—	0.4	
Input leakage current	I _{IN}	_	V _{IN} = 0~5.5V		0~3.6	_	_	±0.1	_	±1.0	μA
Quiescent supply current	Icc	_	$V_{IN} = V_{CC}$	$V_{IN} = V_{CC}$ or GND		—		1.0		10.0	μA

AC Electrical Characteristics (input $t_r = t_f = 3 \text{ ns}$,)

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40~85°C		Unit
		Test Condition	C _{L (} pF)	Min	Тур.	Max	Min	Max	Unit
		$C_L = 10 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9		17.0		_	_	
			1.1~1.3	_	8.8	18.4	1.0	34.2	
			1.4~1.6		5.0	8.5	1.0	10.0	
			1.65~ 1.95	_	3.8	6.2	1.0	6.7	
			2.3~2.7	_	2.7	3.9	1.0	4.4	
			3.0~3.6	_	2.1	3.1	1.0	3.7	
			0.9	_	20.7	_	_	_	
	tplh tphl		1.1~1.3		10.6	21.5	1.0	37.2	ns
		$C_L = 15 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	1.4~1.6		5.9	9.3	1.0	11.2	
Propagation delay time			1.65~ 1.95		4.5	6.9	1.0	7.1	
			2.3~2.7		3.0	4.4	1.0	5.0	
			3.0~3.6	_	2.4	3.4	1.0	3.9	
		C _L = 30 pF, R _L = 1 MΩ	0.9	_	29.6	_	_	_	
			1.1~1.3	_	14.8	29.6	1.0	56.0	
			1.4~1.6		8.0	13.1	1.0	15.9	
			1.65~ 1.95		6.0	9.2	1.0	9.6	
			2.3~2.7	_	3.9	5.7	1.0	6.1	
			3.0~3.6		3.0	4.4	1.0	4.8	
Input capacitance	C _{IN}		3.6	_	3			—	pF
Power dissipation capacitance	C _{PD}	(Note9)	0.9~3.6		6	_		_	pF

Note 9: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation:

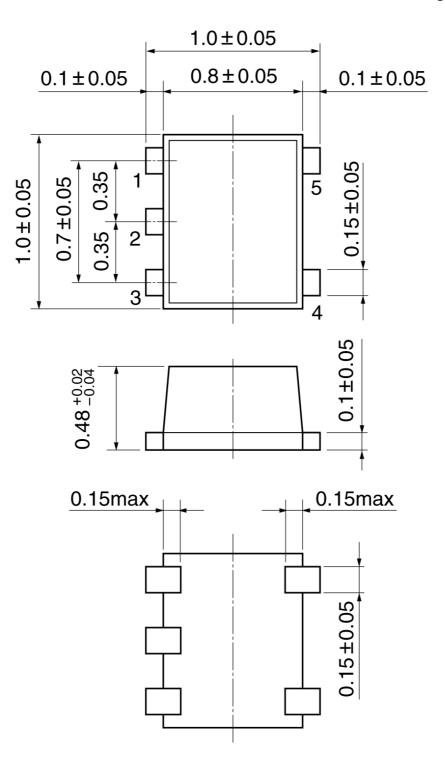
 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

TOSHIBA

Package Dimensions

SON5-P-0.35

Unit:mm



Weight: 0.001 g (typ.)

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