TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SG08AFS

2 Input AND Gate

Features

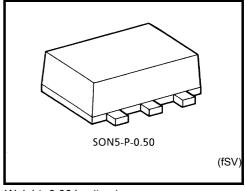
• High-level output current: $I_{OH}/I_{OL} = \pm 8 \text{ mA (min)}$ at $V_{CC} = 3.0 \text{ V}$

• High-speed operation: $t_{pd} = 2.5 \text{ ns (typ.)}$

at $V_{CC} = 3.3 \text{ V}, 15 \text{pF}$

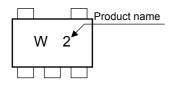
• Operating voltage range: V_{CC} = 0.9~3.6 V

• 5.0-V tolerant inputs.

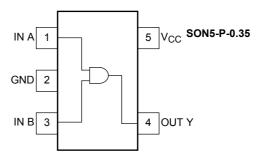


Weight: 0.001 g (typ.)

Marking



Pin Assignment (top view)



Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Value	Unit	
Power supply voltage	V _{CC}	-0.5~4.6	V	
DC input voltage	V _{IN}	-0.5~7.0	V	
DC output voltage	V _{OUT}	-0.5~ V _{CC} + 0.5	V	
Input diode current	I _{IK}	-20	mA	
Output diode current	lok	±20 (Note 1)	mA	
DC output current	lout	±25	mA	
DC V _{CC} /ground current	Icc	±50	mA	
Power dissipation	PD	50	mW	
Storage temperature	T _{stg}	-65~150	°C	

Note 1: $V_{OUT} < GND, V_{OUT} > V_{CC}$

Truth Table

Α	В	Υ
L	L	L
L	Н	L
Н	L	L
Н	Н	Н

IEC Logic Symbol



Recommended Operating Conditions

Characteristics	Symbol	Value	Unit
Power supply voltage	V_{CC}	0.9~3.6	V
Input voltage	V _{IN}	0~5.5	V
Output voltage	V _{OUT}	0~V _{CC}	V
Output Current		±8.0 (Note 2)	
		±4.0 (Note 3)	
	la/la.	±3.0 (Note 4)	mA
	I _{OH} /I _{OL}	±1.7 (Note 5)	ША
		±0.3 (Note 6)	
		±0.02 (Note 7)	
Operating temperature	T _{opr}	−40~85	°C
Input rise and fall time	dt/dV	0~10 (Note 8)	ns/V

Note 2: $V_{CC} = 3.0 \sim 3.6 \text{ V}$

Note 3: $V_{CC} = 2.3 \sim 2.7 \text{ V}$

Note 4: $V_{CC} = 1.65 \sim 1.95 \text{ V}$

Note 5: V_{CC} = 1.4~1.6 V

Note 6: $V_{CC} = 1.1 \sim 1.3 \text{ V}$

Note 7: $V_{CC} = 0.9 \text{ V}$

Note 8: $V_{IN} = 0.8 \sim 2.0 \text{ V}, V_{CC} = 3.0 \text{ V}$

DC Electrical Characteristics

Characteristics	Symbol	Test	Test Condition			Ta = 25°C			Ta = -40~85°C		Unit
Characteristics	Symbol	Circuit			V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic
				0.9	V _{CC}	_	_	V _{CC}	_		
					1.1~1.3	V _{CC} × 0.7		_	V _{CC} × 0.7	_	V
High-level input voltage	V_{IH}	_			1.4~1.6	V _{CC} × 0.65	_	_	V _{CC} × 0.65	_	
voltage					1.65~1.95	V _{CC} × 0.65		_	V _{CC} × 0.65	_	
					2.3~2.7	1.7		_	1.7	_	
					3.0~3.6	2.0		_	2.0	_	
					0.9	_		GND	_	GND	
					1.1~1.3	_		V _{CC} × 0.3	_	V _{CC} × 0.3	V
Low-level input	V_{IL}	_		_	1.4~1.6	_	_	V _{CC} × 0.35	_	V _{CC} × 0.35	
voltage			1.65~1.95	_	_	V _{CC} × 0.35	_	V _{CC} × 0.35			
					2.3~2.7	_	_	.0.7		0.7	
					3.0~3.6	_	_	0.8		0.8	
			V _{IN} = V _{IH}	I _{OH} =-0.02 mA	0.9	0.75	_	_	0.75	_	
				$I_{OH} = -0.3 \text{ mA}$	1.1~1.3	V _{CC} × 0.75		_	V _{CC} × 0.75	_	
igh-level output	V _{OH}			I _{OH} = -1.7 mA	1.4~1.6	V _{CC} × 0.75	_	_	V _{CC} × 0.75	_	
voltage VOH —			I _{OH} = -3.0 mA	1.65~ 1.95	V _{CC} -0.45		_	V _{CC} -0.45	_		
				I _{OH} = -4.0 mA	2.3~2.7	2.0	_	_	2.0	_	
				$I_{OH} = -8.0 \text{ mA}$	3.0~3.6	2.48	_	_	2.48	_	
				I _{OL} = 0.02 mA	0.9	_		0.1	_	0.1	
Low-level output voltage Vol — VIN = or VII			I _{OL} = 0.3 mA	1.1~1.3	_	_	V _{CC} × 0.25	_	V _{CC} × 0.25		
	V _{IN} = V _{IH}	$\begin{array}{c} V_{IN} = V_{IH} \\ \text{or } V_{IL} \\ \hline \\ I_{OL} = 3.0 \text{ mA} \end{array}$	1.4~1.6	_	_	V _{CC} × 0.25	_	V _{CC} × 0.25	V		
	or V _{IL}		1.65~ 1.95	_	_	0.45	_	0.45			
				I _{OL} = 4.0 mA	2.3~2.7	_	_	0.4	_	0.4	-
				I _{OL} = 8.0 mA	3.0~3.6	_	_	0.4	_	0.4	
Input leakage current	I _{IN}	_	V _{IN} = 0~5.5V		0~3.6	_	_	±0.1	_	±1.0	μА
Quiescent supply current	I _{CC}	_	V _{IN} = V _{CC} or GND		3.6	_	_	1.0	_	10.0	μΑ

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AC Electrical Characteristics (input $t_r = t_f = 3 \text{ ns,}$)

Characteristics	Symbol	Test Condition		-	Ta = 25° C Ta = -40° 85°C		Unit		
Characteristics		rest Condition	C _{L (} pF)	Min	Тур.	Max	Min	Max	Offic
		$\begin{aligned} C_L &= 10 \text{ pF}, \\ R_L &= 1 \text{ M}\Omega \end{aligned}$	0.9	_	26.9	_	_	_	
			1.1~1.3	_	10.9	18.4	1.0	34.2	
			1.4~1.6		5.9	8.5	1.0	10.0	
			1.65~ 1.95		4.5	6.2	1.0	6.7	
			2.3~2.7	_	2.9	3.9	1.0	4.4	
			3.0~3.6		2.2	3.1	1.0	3.7	
			0.9		30.0	_	_	_	
	tplн tpнL		1.1~1.3		12.0	21.5	1.0	37.2	ns
		$C_L = 15 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	1.4~1.6		6.5	9.3	1.0	11.2	
Propagation delay time			1.65~ 1.95	_	5.0	6.9	1.0	7.1	
			2.3~2.7		3.2	4.4	1.0	5.0	
			3.0~3.6		2.5	3.4	1.0	3.9	
		$C_L = 30 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9		45.0	_	_	_	
			1.1~1.3	_	18.0	29.6	1.0	56.0	
			1.4~1.6	_	8.9	13.1	1.0	15.9	
			1.65~ 1.95	_	6.9	9.2	1.0	9.6	
			2.3~2.7	_	4.4	5.7	1.0	6.1	
			3.0~3.6	_	3.5	4.4	1.0	4.8	
Input capacitance	C _{IN}		3.6	_	3	_	_	_	pF
Power dissipation capacitance	C_{PD}	(Note9)	0.9~3.6	_	6	—	_	_	pF

Note 9: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

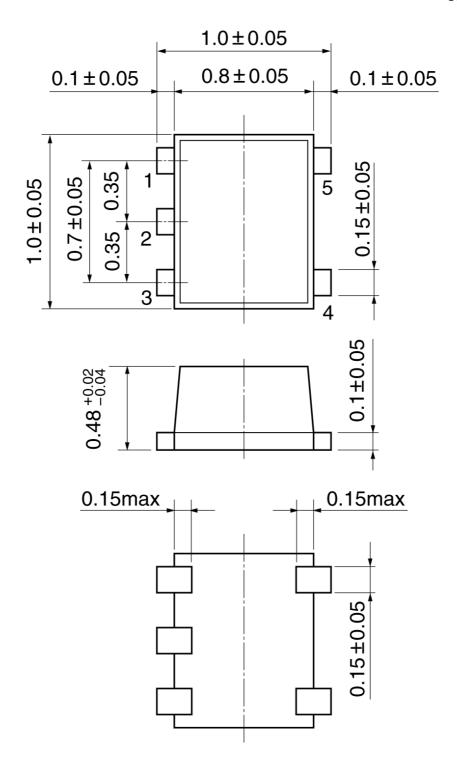
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Average operating current can be obtained by the equation:

 $I_{CC \ (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

Package Dimensions

SON5-P-0.35 Unit:mm



Weight: 0.001 g (typ.)

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