### TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

TD62591APG, TD62592APG, TD62593APG, TD62594APG TD62595APG, TD62595AFG, TD62596APG, TD62596AFG TD62597APG, TD62597AFG, TD62598APG, TD62598AFG

### 8CH SINGLE DRIVER

The TD62591APG Series are comprised of eight NPN Transistor Arrays.

Applications include relay, hammer, lamp and display (LED) drivers

This devices are a product for the Pb free(Sn-Ag).

### **FEATURES**

- Output current (single output) 200 mA (Max)
- High sustaining voltage output 50V (Min)
- Low saturation voltage VCE (sat) = 0.8 V

@Iout = 150mA inputs compatible with various type logic.

• Include Input Resistor

TD62591A, TD62595APG/FG: external.

general purpose

TD62592A, TD62596APG/FG:  $10.5 \text{ k}\Omega + 7\text{V}$ 

zener diode 14~25 V

**PMOS** 

TD62593A, TD62597APG/FG:  $2.7 \text{ k}\Omega$ 

TTL, 5 V CMOS

TD62594A, TD62598APG/FG:  $10.5 \text{ k}\Omega$ 

6~15 V PMOS, CMOS

• Include Clamp Diode

TD62595APG, TD62595AFG, TD62596APG, TD62596AFG TD62597APG, TD62597AFG, TD62598APG, TD62598AFG

Package type-APG : DIP-18pinPackage type-AFG : SOP-18pin

# TD62591APG, TD62592APG, TD62593APG TD62594APG, TD62595APG, TD62596APG TD62597APG, TD62598APG DIP18-P-300-2.54D TD62595AFG TD62596AFG TD62597AFG TD62597AFG TD62598AFG

Weight

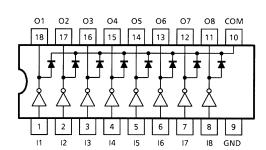
DIP18-P-300-2.54D : 1.47 g (Typ.) SOP18-P-375-1.27 : 0.5 g (Typ.)

### PIN CONNECTION (TOP VIEW)

TD62591APG, TD62592APG, TD62593APG TD62594APG

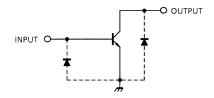
01 О3 04 05 06 07 08 NC 18 17 16 15 14 13 12 11 10 2 4 6 7 8 3 5 16

TD62595APG, TD62595AFG, TD62596APG, TD62596AFG TD62597APG, TD62597AFG, TD62598APG, TD62598AFG

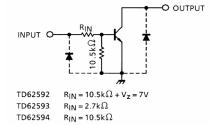


# **SCHEMATICS (EACH DRIVER)**

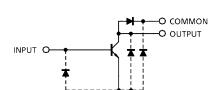
### TD62591APG



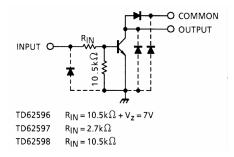
TD62592APG, TD62593APG, TD62594APG



TD62595APG, TD62595AFG



TD62596APG, TD62596AFG, TD62597APG, TD62597AFG, TD62598APG, TD62598AFG



Note: The input and output parasitic diodes cannot be used as clamp diodes.

# MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Collector-Emitter Voltage	V <sub>CEO</sub>	50	V
Collector-Base Voltage	V <sub>CBO</sub>	50	V
Clamp Diode Reverse Voltage	V <sub>R</sub> (Note 1)	50	V
Collector Current	I <sub>C</sub>	200	mA / ch
Input Voltage	V <sub>IN</sub> (Note 2)	-0.5~30	V
Input Current	I <sub>IN</sub> (Note 3)	25	mA
Power Dissipation	P <sub>D</sub> (Note 4)	0.96 (Note 5) / 1.47	W
Operating Temperature	T <sub>opr</sub>	-40~85	°C
Storage Temperature	T <sub>stg</sub>	-55~150	°C

Note 1: Except TD62591~TD62594APG

Note 2: Except TD62591APG, TD62595APG, TD62595AFG

Note 3: Only TD62591APG, TD62595APG, TD62595AFG

Note 4: Delated above 25°C in the proportion of 11.7mW / °C (APG-Type), 7.7mW / °C (AFG-Type)

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Note 5: SOP-18pin



# RECOMMENDED OPERATING CONDITIONS (Ta = $-40\sim85^{\circ}$ C)

CHARAC	CHARACTERISTIC SYMBOL CONDITION		CONDITION	MIN	TYP.	MAX	UNIT
Collector-Emitter \	/oltage	V <sub>CEO</sub>	_	0	_	50	٧
Collector-Base Vo	Itage	V <sub>CBO</sub>	_	0	_	50	V
Collector Current		Ic	-	0	_	150	mA / ch
Clamp Diode Reve	rse Voltage	V <sub>R</sub>	(Note1)	7	_	50	V
Input Voltage		V <sub>IN</sub>	(Note2)	0	_	25	V
Input Current		I <sub>IN</sub>	(Note3)	0	_	10	mA
Input Voltage (Output On)	TD62592 TD62596	Vin (on)	_	14.0	_	25	
	TD62593 TD62597			2.4	_	25	V
	TD62594 TD62598			7.0	_	25	
Power Dissipation	APG	P <sub>D</sub>	_	_	_	0.52	W
	AFG		_	_	_	0.355	VV

# ELECTRICAL CHARACTERISTICS (Ta = 25°C unless otherwise noted)

CHARA	ACTERISTIC	SYMBOL	TEST CIR- CUIT	TEST CONDITION		MIN	TYP.	MAX	UNIT
Output Leakage	Current	I <sub>CEX</sub>	1	V <sub>CE</sub> = 50 V, V <sub>IN</sub> = 0		_	_	10	μΑ
Collector-Emitter Saturation Voltage		V	2	I <sub>C</sub> = 10 mA, I <sub>IN</sub> = 0.4 mA		_	_	0.2	V
Collector-Enlitte	i Saturation voltage	V <sub>CE (sat)</sub>	(sat) $I_{C} = 150 \text{ mA}, I_{IN} = 3.0 \text{ mA}$		= 3.0 mA	_	_	0.8	v
DC Current Transfer Ratio		h <sub>FE</sub>	2	V <sub>CE</sub> = 10 V I <sub>C</sub> = 10 mA	(Note 3)	70	_	_	_
					(Note 2)	50	_	_	
Input Current -	TD62591 TD62595	- In (on)	3	I <sub>C</sub> = 50 mA		_	_	0.65	
	TD62592 TD62596			V <sub>IN</sub> = 14V, I <sub>C</sub> = 50 mA		_	_	0.9	mA
	TD62593 TD62597			V <sub>IN</sub> = 2.4 V, I <sub>C</sub> = 50 mA		_	_	0.9	IIIA
	TD62594 TD62598			V <sub>IN</sub> = 7.0 V, I <sub>C</sub> = 50 mA		_	_	0.9	
Turn-On Delay		t <sub>ON</sub>	4	V <sub>OUT</sub> = 50 V, R <sub>L</sub> = 330 Ω		_	0.1	_	μs
Turn-Off Delay		t <sub>OFF</sub>	7				0.3	_	μs

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Note 1: Except TD62591~TD62594APG

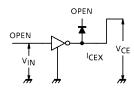
Note 2: Except TD62591APG, TD62595APG, TD62595AFG

Note 3: Only TD62591APG, TD62595APG, TD62595AFG

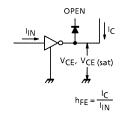


### **TEST CIRCUIT**

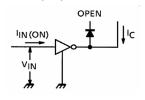
# 1. I<sub>CEX</sub>



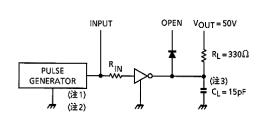
# 2. h<sub>FE</sub>, V<sub>CE (sat)</sub>

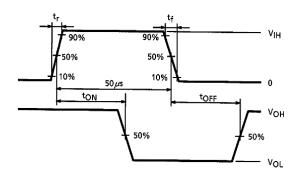


# 3. V<sub>IN (ON)</sub>



## 4. ton, toff





Note 1: Pulse width 50 µs, duty cycle 10%

Output impedance 50  $\Omega$ ,  $t_{\Gamma} \le 5$  ns,  $t_{f} \le 10$  ns

Note 2: See below

Input Condition

TYPE NUMBER	R <sub>IN</sub>	V <sub>IH</sub>
TD62591APG, TD62595APG, TD62595AFG	2.7 kΩ	3 V
TD62592APG, TD62596APG, TD62596AFG	0 Ω	15 V
TD62593APG, TD62597APG, TD62597AFG	0 Ω	3 V
TD62594APG, TD62598APG, TD62598AFG	0 Ω	10 V

Note 3: C<sub>L</sub> includes probe and jig capacitance

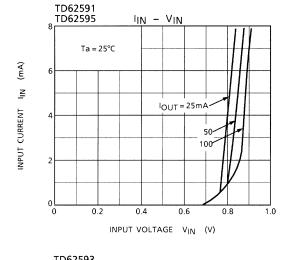
# **PRECAUTIONS for USING**

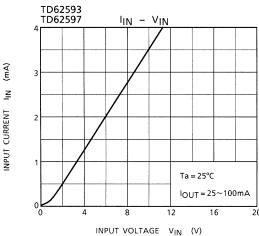
This IC does not integrate protection circuits such as overcurrent and overvoltage protectors.

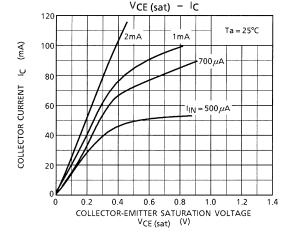
Thus, if excess current or voltage is applied to the IC, the IC may be damaged. Please design the IC so that excess current or voltage will not be applied to the IC.

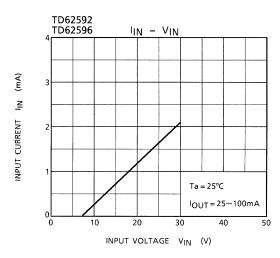
Utmost care is necessary in the design of the output line,  $V_{CC}$  and GND line since IC may be destroyed due to short–circuit between outputs, air contamination fault, or fault by improper grounding.

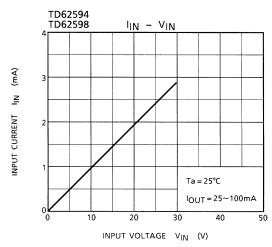
4

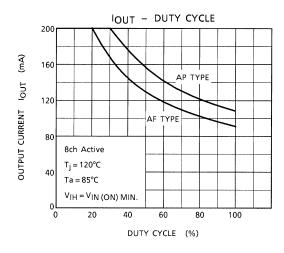


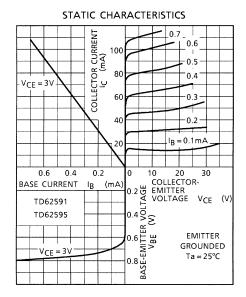


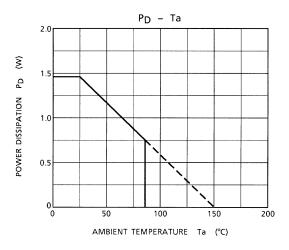


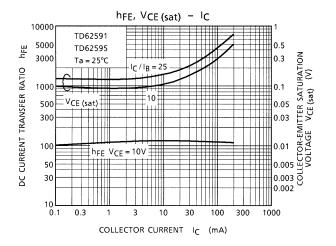






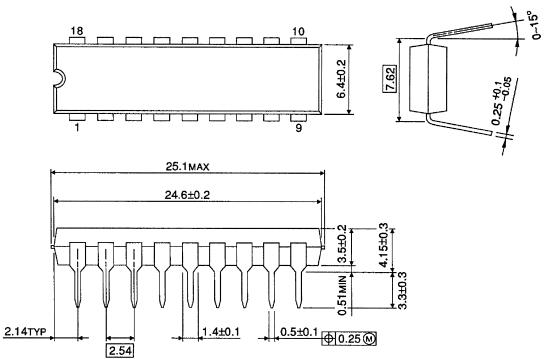






# **PACKAGE DIMENSIONS**

DIP18-P-300-2.54D Unit: mm



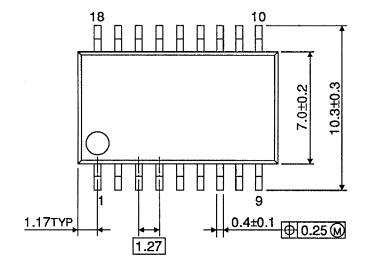
7

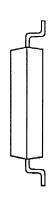
Weight: 1.47 g (Typ.)

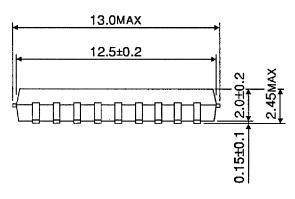
# **PACKAGE DIMENSIONS**

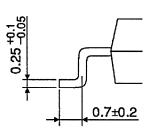
SOP18-P-375-1.27

Unit: mm









Weight: 0.50 g (Typ.)

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About solderability, following conditions were confirmed

- Solderability
  - (1) Use of Sn-63Pb solder Bath
    - · solder bath temperature = 230°C
    - · dipping time = 5 seconds
    - · the number of times = once
    - · use of R-type flux
  - (2) Use of Sn-3.0Ag-0.5Cu solder Bath
    - · solder bath temperature = 245°C
    - · dipping time = 5 seconds
    - · the number of times = once
    - · use of R-type flux

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