#### TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

# TD62593AFNG, TD62594AFNG, TD62597AFNG, TD62598AFNG

8CH SINGLE DRIVER: COMMON EMITTER

The TD62593, 4, 7, 8AFNG are comprised of eight NPN Transistor Arrays.

Applications include relay, hammer, lamp and display (LED) drivers.

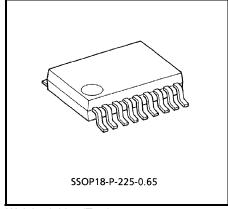
This devices are a product for the Pb free(Sn-Ag).

#### **FEATURES**

• Package Type: SSOP18pin (0.65 mm pitch)

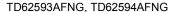
• High Sustaining Voltage Output : 50 V (MIN)

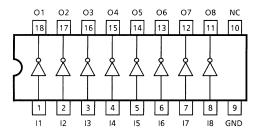
• Low Saturation Voltage :  $V_{CE}$  (sat) = 0.8 V @ $I_{OUT}$  = 150 mA Inputs Compatible with Various type Logic. TD62593, TD62597AFNG :  $R_{IN}$  = 2.7 k $\Omega$  TTL, 5 V CMOS TD62594, TD62598AFNG :  $R_{IN}$  = 10.5 k $\Omega$  6~15 V PMOS, CMOS



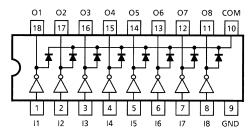
Weight: 0.09 g (Typ.)

## **PIN CONNECTION (TOP VIEW)**



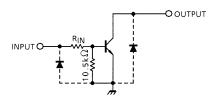


#### TD62597AFNG, TD62598AFNG



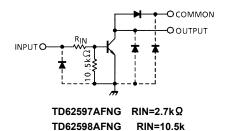
### **SCHEMATICS (EACH DRIVER)**

TD62593AFNG, TD62594AFNG



TD62593AFNG RIN=2.7kΩ TD62594AFNG RIN=10.5k

#### TD62597AFNG, TD62598AFNG



Note: The input and output parasitic diodes cannot be used as clamp diodes.



# MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Collector-Emitter Voltage	V <sub>CEO</sub>	50	V
Collector-Base Voltage	V <sub>CBO</sub>	50	V
Clamp Diode Reverse Voltage	V <sub>R</sub> (Note 1)	50	V
Collector Current	IC	200	mA / ch
Input Voltage	V <sub>IN</sub>	-0.5~30	V
Power Dissipation	P <sub>D</sub> (Note 2)	0.96	W
Operating Temperature	T <sub>opr</sub>	-40~85	°C
Storage Temperature	T <sub>stg</sub>	-55~150	°C

Note 1: Except TD62593AFNG, TD62594AFNG

Note 2: On Glass Epoxy PCB (50  $\times$  50  $\times$ 1.6 mm Cu 40%)

# **RECOMMENDED OPERATING CONDITIONS (Ta = -40-85°C)**

CHARA	CTERISTIC	SYMBOL	CONDITION	MIN	TYP.	MAX	UNIT
Collector-Emitter \	oltage/	V <sub>CEO</sub>		0	_	50	V
Collector-Base Vo	ltage	V <sub>CBO</sub>		0	_	50	V
Collector Current		IC		0	_	150	mA / ch
Clamp Diode Reverse Voltage		V <sub>R</sub> (Note 1)		7	_	50	V
Input Voltage		V <sub>IN</sub>		0	_	25	V
Input Current		I <sub>IN</sub>		0	_	10	mA
Input Voltage (Output On)	TD62593AFNG TD62597AFNG	Vin (on)		2.4	_	25	- V
	TD62594AFNG TD62598AFNG			7.0	_	25	
Power Dissipation		P <sub>D</sub> (Note 2)		_	_	0.4	W

Note 1: Except TD62593AFNG, TD62594AFNG

Note 2: On Glass Epoxy PCB (50 × 50 × 1.6 mm Cu 40%)

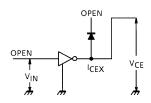
# **ELECTRICAL CHARACTERISTICS (Ta = 25°C)**

CHARACTERISTIC		SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN	TYP.	MAX	UNIT	
Output Leakage (	Current	I <sub>CEX</sub>	1	V <sub>CE</sub> = 50 V, V <sub>IN</sub> = 0	_	_	10	μΑ	
Output Saturation Voltage		V <sub>CE (sat)</sub>	2	I <sub>C</sub> = 10 mA, I <sub>IN</sub> = 0.4 mA	_	_	0.2	V	
				I <sub>C</sub> = 150 mA, I <sub>IN</sub> = 3.0 mA	_	_	0.8		
DC Current Trans	fer Ratio	h <sub>FE</sub>	2	$V_{CE}$ = 10 V, $I_{C}$ = 10 mA	50	_	_		
Input Current	TD62593AFNG TD62597AFNG	I	3	V <sub>IN</sub> = 2.4 V, I <sub>C</sub> = 50 mA	_	_	0.9	mA	
	TD62594AFNG TD62598AFNG	IN (ON)		V <sub>IN</sub> = 7.0 V, I <sub>C</sub> = 50 mA	_	_	0.9	IIIA	
Turn-On Delay		t <sub>ON</sub>	4	V <sub>OUT</sub> = 50 V, R <sub>L</sub> = 330 Ω	_	0.1	_	II.e	
Turn-Off Delay		t <sub>OFF</sub>		VOUT - 30 V, IV - 330 12	_	3.0	_	μs	

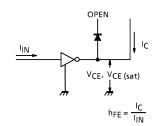
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### **TEST CIRCUIT**

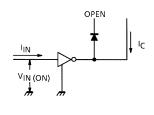
# 1. I<sub>CEX</sub>



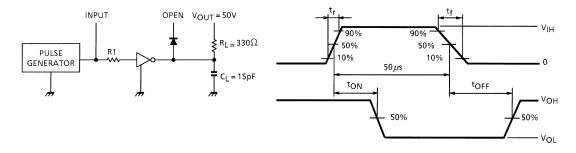
# 2. h<sub>FE</sub>, V<sub>CE (sat)</sub>



## 3. I<sub>IN</sub> (ON)



### 4. ton, toff



Note 1: Pulse Width 50 µs, Duty Cycle 10%

Output Impedance 50  $\Omega$ ,  $t_f \le 5$  ns,  $t_f \le 10$  ns

Note 2: See below

#### Input Condition

TYPE NUMBER	R <sub>IN</sub>	V <sub>IH</sub>
TD62593AFNG, TD62597AFNG	0 Ω	3 V
TD62594AFNG, TD62598AFNG	0 Ω	10 V

Note 3: C<sub>L</sub> includes probe and jig capacitance

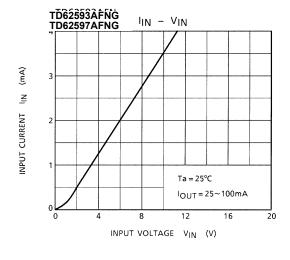
### **PRECAUTIONS for USING**

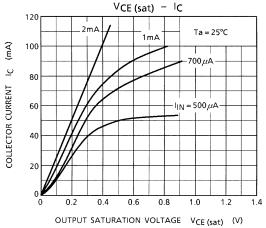
This IC does not integrate protection circuits such as overcurrent and overvoltage protectors.

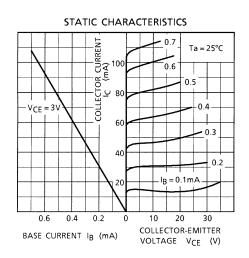
Thus, if excess current or voltage is applied to the IC, the IC may be damaged. Please design the IC so that excess current or voltage will not be applied to the IC.

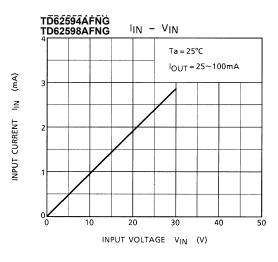
Utmost care is necessary in the design of the output line, VCC and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.

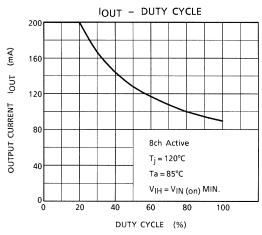
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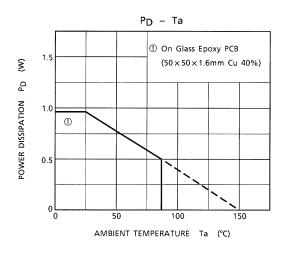








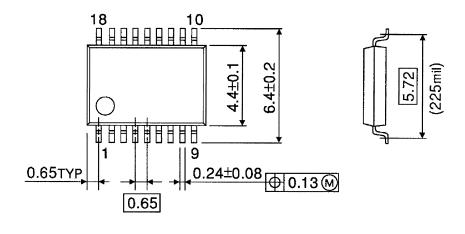


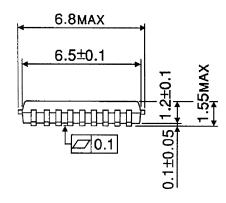


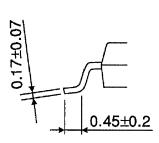
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## **PACKAGE DIMENSIONS**

SSOP18-P-225-0.65 Unit: mm







Weight: 0.09 g (Typ.)

About solderability, following conditions were confirmed

- Solderability
  - (1) Use of Sn-63Pb solder Bath
    - · solder bath temperature = 230°C
    - · dipping time = 5 seconds
    - · the number of times = once
    - · use of R-type flux
  - (2) Use of Sn-3.0Ag-0.5Cu solder Bath
    - · solder bath temperature = 245°C
    - · dipping time = 5 seconds
    - · the number of times = once
    - · use of R-type flux

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