TOSHIBA Field Effect Transistor Silicon P Channel MOS Type (U-MOSIV)

TPC6107

Notebook PC Applications Portable Equipment Applications

- · Small footprint due to small and thin package
- Low drain-source ON resistance: RDS (ON) = $40 \text{ m}\Omega$ (typ.)
- High forward transfer admittance: $|Y_{fs}| = 9.6 \text{ S (typ.)}$
- Low leakage current: $IDSS = -10 \mu A (max) (VDS = -20 V)$
- Enhancement model: V_{th} = -0.5 to -1.2 V (V_{DS} = -10 V, I_{D} = -200 μA)

Maximum Ratings (Ta = 25°C)

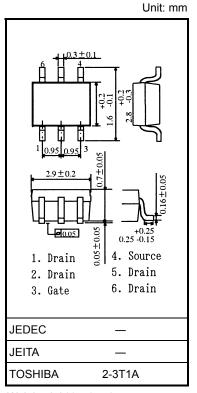
Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	-20	V	
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)		V_{DGR}	-20	V	
Gate-source voltage	Gate-source voltage		±12	٧	
Drain current	DC (Note 1)	Ι _D	-4.5	A	
	Pulse (Note 1)	I _{DP}	-18		
Drain power dissipation	(t = 5 s) (Note 2a)	P _D	2.2	W	
Drain power dissipation	(t = 5 s) (Note 2b)	P _D	0.7	W	
Single pulse avalanche ene	E _{AS}	1.3	mJ		
Avalanche current	I _{AR}	-2.25	Α		
Repetitive avalanche energy (Note 4)		E _{AR}	0.22	mJ	
Channel temperature	T _{ch}	150	°C		
Storage temperature range		T _{stg}	-55 to 150	°C	

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient $(t=5\;s) \eqno(Note\;2a)$	R _{th (ch-a)}	56.8	°C/W
Thermal resistance, channel to ambient (t = 5 s) (Note 2b)	R _{th (ch-a)}	178.5	°C/W

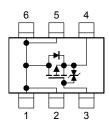
Note 1, Note 2, Note 3, Note 4 and Note 5: See the next page.

This transistor is an electrostatic- sensitive device. Please handle with caution.



Weight: 0.011 g (typ.)

Circuit Configuration



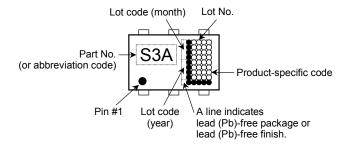
Electrical Characteristics (Ta = 25°C)

Ch	aracteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cui	rrent	I _{GSS}	$V_{GS} = \pm 10 \text{ V}, V_{DS} = 0 \text{ V}$		_	±10	μА
Drain cut-OFF cu	ırrent	I _{DSS}	$V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}$	_	_	-10	μА
Drain-source breakdown voltage		V (BR) DSS	$I_D = -10 \text{ mA}, V_{GS} = 0 \text{ V}$	-20	_	_	V
		V (BR) DSX	$I_D = -10 \text{ mA}, V_{GS} = 12 \text{ V}$	-8	_	_	v
Gate threshold v	oltage	V _{th}	$V_{DS} = -10 \text{ V}, \ I_D = -200 \ \mu\text{A}$	-0.5	_	-1.2	٧
			$V_{GS} = -2 \text{ V}, I_D = -2.2 \text{ A}$	_	110	180	
Drain-source ON	resistance	R _{DS} (ON)	$V_{GS} = -2.5 \text{ V}, I_D = -2.2 \text{ A}$	_	70	100	mΩ
		R _{DS} (ON)	$V_{GS} = -4.5 \text{ V}, I_D = -2.2 \text{ A}$	_	40	55	
Forward transfer	admittance	Y _{fs}	$V_{DS} = -10 \text{ V}, I_D = -2.2 \text{ A}$	4.8	9.6	_	S
Input capacitance		C _{iss}	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		680	_	pF
Reverse transfer capacitance		C _{rss}		_	130	_	
Output capacitance		C _{oss}			140	_	
Switching time	Rise time	t _r	V _{GS} 0 V	_	6	_	
	Turn-ON time	t _{on}		_	16	_	
	Fall time	t _f		_	38	_	ns
	Turn-OFF time	t _{off}	$V_{DD} \simeq -10 \text{ V}$ Duty $\leq 1\%$, $t_W = 10 \mu\text{s}$	_	85	_	
Total gate charge (gate-source plus gate-drain)		Qg	$V_{DD} \simeq -16 \text{ V}, V_{GS} = -5 \text{ V},$ $I_{D} = -4.5 \text{ A}$	_	9.8	_	
Gate-source charge		Q _{gs}		_	2	_	nC
Gate-drain ("mille	er") charge	Q _{gd}		_	3	_	

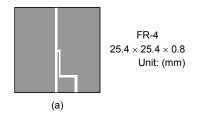
Source-Drain Ratings and Characteristics (Ta = 25°C)

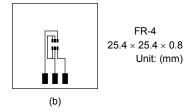
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Pulse drain reverse current	(Note 1)	I _{DRP}	_	_	_	-18	Α
Forward voltage (diode)		V_{DSF}	$I_{DR} = -4.5 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	1.2	V

Marking (Note 5)

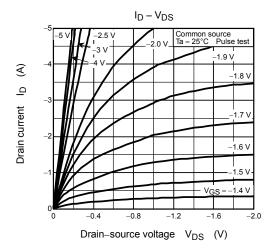


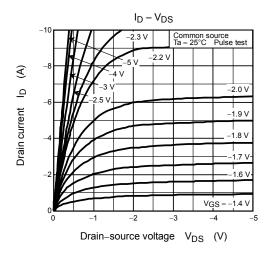
- Note 1: Ensure that the channel temperature does not exceed 150°C.
- Note 2: (a) Device mounted on a glass-epoxy board (a) (t = 5 s)
 - (b) Device mounted on a glass-epoxy board (b) (t = 5 s)

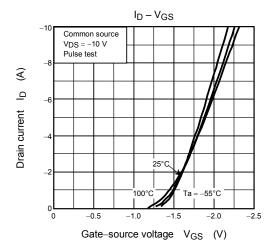


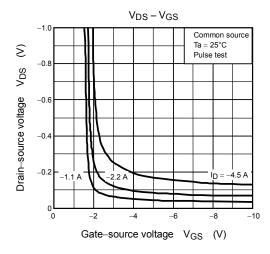


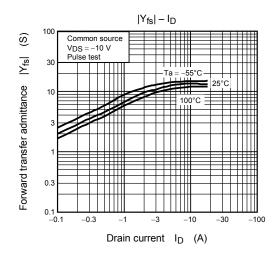
- Note 3: V_{DD} = 16 V, T_{Ch} = 25°C (initial), L = 0.2 mH, R_G = 25 Ω , I_{AR} = -2.25 A
- Note 4: Repetitive rating: pulse width limited by maximum channel temperature
- Note 5: on lower left of the marking indicates Pin 1.

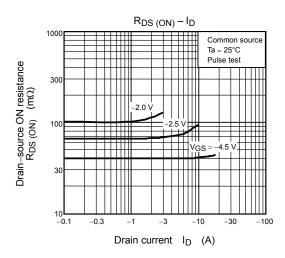




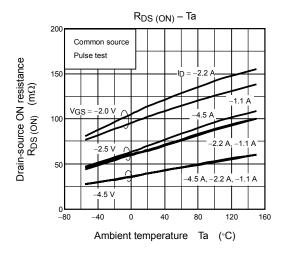


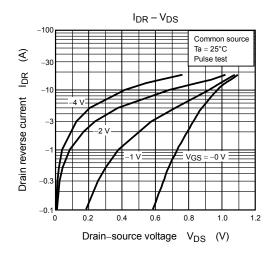


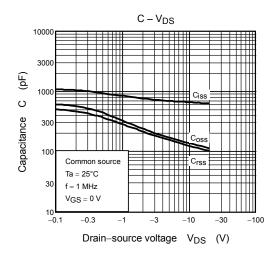


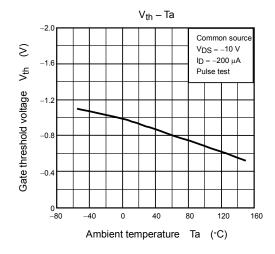


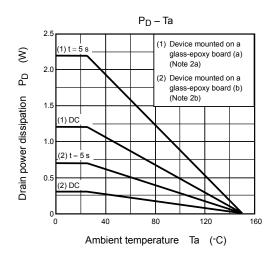
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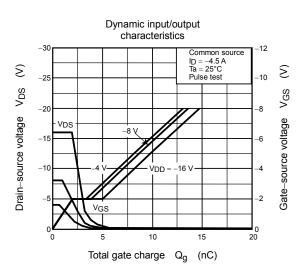


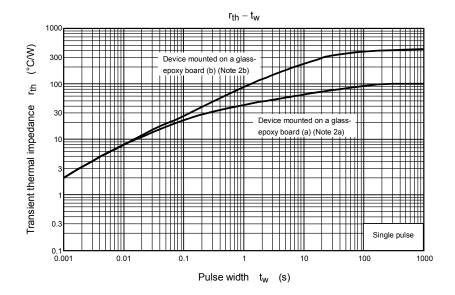


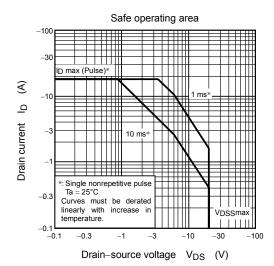












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