TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (U-MOSII)

# **TPC6002**

# Notebook PC Applications Portable Equipment Applications

- Low drain-source ON resistance: RDS (ON) = 25 m $\Omega$  (typ.)
- High forward transfer admittance:  $|Y_{fs}| = 10 \mathrm{S}$  (typ.)
- Low leakage current:  $IDSS = 10 \mu A (max) (VDS = 30 V)$
- Enhancement-model:  $V_{th}$  = 1.3 to 2.5 V ( $V_{DS}$  = 10 V,  $I_{D}$  = 1 mA)

#### **Maximum Ratings (Ta = 25°C)**

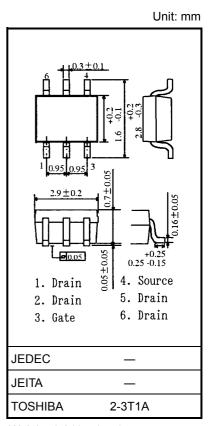
Characteristics		Symbol	Rating	Unit	
Drain-source voltage		$V_{DSS}$	30	V	
Drain-gate voltage ( $R_{GS} = 20 \text{ k}\Omega$ )		$V_{DGR}$	30	V	
Gate-source voltage		$V_{GSS}$	±20	V	
Drain current	DC (Note 1)	Ι <sub>D</sub>	6	A	
	Pulse (Note 1)	I <sub>DP</sub>	24		
Drain power dissipation	(t = 5 s) (Note 2a)	P <sub>D</sub>	2.2	W	
Drain power dissipation	(t = 5 s) (Note 2b)	P <sub>D</sub>	0.7	W	
Single pulse avalanche energy (Note 3)		E <sub>AS</sub>	5.8	mJ	
Avalanche current	I <sub>AR</sub>	3	Α		
Repetitive avalanche energy (Note 4)		E <sub>AR</sub>	0.22	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C	

#### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient $(t = 5 \text{ s})$ (Note 2a)	R <sub>th (ch-a)</sub>	56.8	°C/W
Thermal resistance, channel to ambient $(t=5\;s) \eqno(Note\;2b)$	R <sub>th (ch-a)</sub>	178.5	°C/W

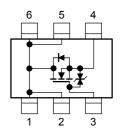
Note: (Note 1), (Note 2), (Note 3), (Note 4), (Note 5) Please see next page.

This transistor is an electrostatically sensitive device. Please handle it with caution.

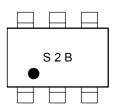


Weight: 0.011 g (typ.)

### **Circuit Configuration**



#### Marking (Note 5)





### **Electrical Characteristics (Ta = 25°C)**

Cha	racteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage curi	rent	I <sub>GSS</sub>	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μΑ
Drain cut-OFF cu	rrent	I <sub>DSS</sub>	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V	_	_	10	μΑ
Drain-source breakdown voltage		V <sub>(BR) DSS</sub>	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	30	_	_	V
		V <sub>(BR) DSX</sub>	$I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$	15	_	_	V
Gate threshold vo	oltage	V <sub>th</sub>	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$	1.3	_	2.5	V
Drain-source ON resistance		R <sub>DS (ON)</sub>	$V_{GS} = 4.5 \text{ V}, I_D = 3 \text{ A}$	_	36	50	mΩ
		R <sub>DS (ON)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 3 A	_	25	30	
Forward transfer	admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 3 A	3.5	10	_	S
Input capacitance		C <sub>iss</sub>		_	610	_	
Reverse transfer capacitance		C <sub>rss</sub>	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	105	_	pF
Output capacitance		C <sub>oss</sub>		_	151		
Switching time Fall	Rise time	t <sub>r</sub>	$V_{GS} = \begin{cases} 0 & V \\ 0 & V \\ 0 & V \end{cases}$ $V_{D} = \begin{cases} 0 & V \\ 0 & V$	_	3	_	
	Turn-ON time	t <sub>on</sub>		_	9	_	- ns
	Fall time	t <sub>f</sub>		_	9	_	
	Turn-OFF time	t <sub>off</sub>	$V_{DD} \simeq 15 \text{ V}$ Duty $\leq$ 1%, $t_W = 10 \mu\text{s}$	_	27	_	
Total gate charge (gate-source plus gate-drain)		Qg		_	13	_	nC
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \simeq 24 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 6 \text{ A}$	_	10	_	
Gate-drain ("miller") charge		Q <sub>gd</sub>		_	3		

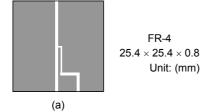
## Source-Drain Ratings and Characteristics (Ta = 25°C)

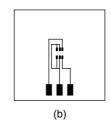
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Pulse drain reverse current	(Note 1)	I <sub>DRP</sub>	_	_	_	24	Α
Forward voltage (Diode)		$V_{DSF}$	I <sub>DR</sub> = 6 A, V <sub>GS</sub> = 0 V		_	-1.2	V

Note 1: Please use devices on condition that the channel temperature is below 150  $^{\circ}$ C.

Note 2: (a) Device mounted on a glass-epoxy board (a) (t = 5 s)

(b) Device mounted on a glass-epoxy board (b) (t = 5 s)





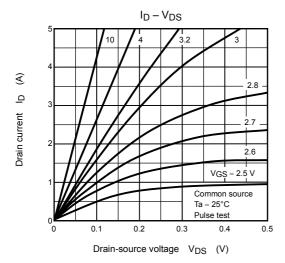
 $FR-4 \\ 25.4 \times 25.4 \times 0.8 \\ Unit: \text{(mm)}$ 

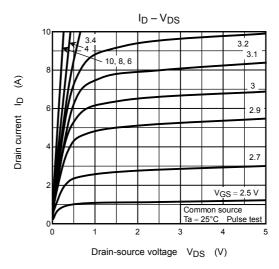
Note 3:  $V_{DD} = 24 \text{ V}$ ,  $T_{ch} = 25^{\circ}\text{C}$  (initial), L = 0.5 mH,  $R_G = 25 \Omega$ ,  $I_{AR} = 3.0 \text{ A}$ 

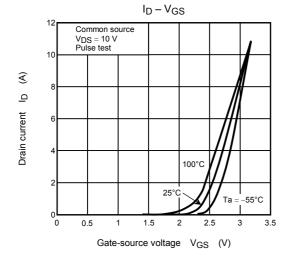
Note 4: Repetitive rating; pulse width limited by maximum channel temperature

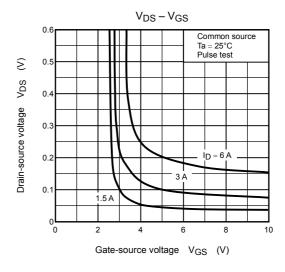
Note 5: Black round marking "•" locates on the left lower side of parts number marking "S2B" indicates terminal No.1.

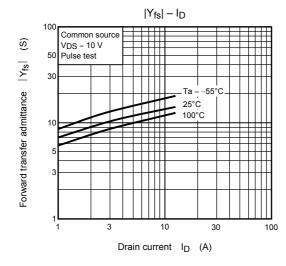
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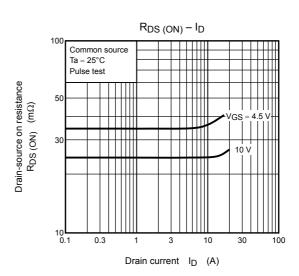


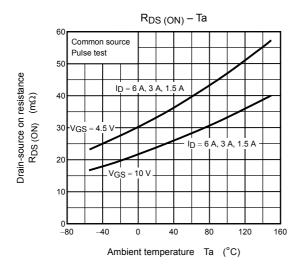


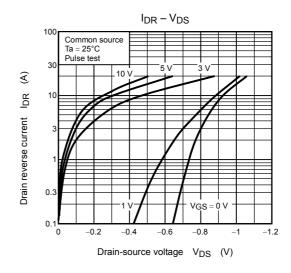


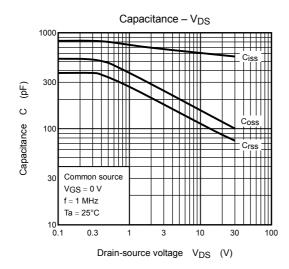


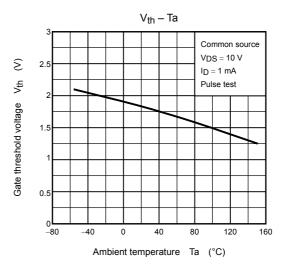


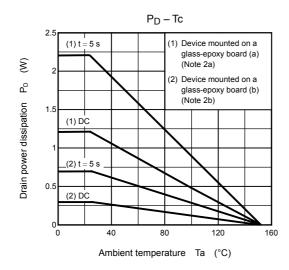


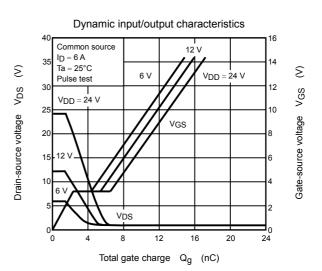




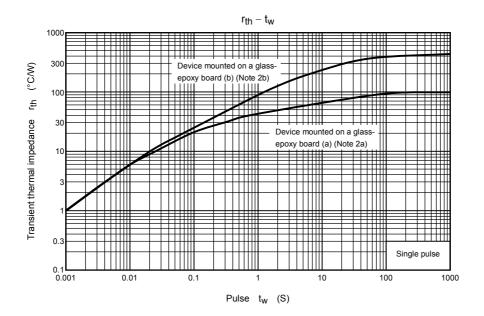


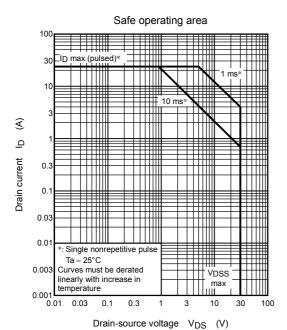






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