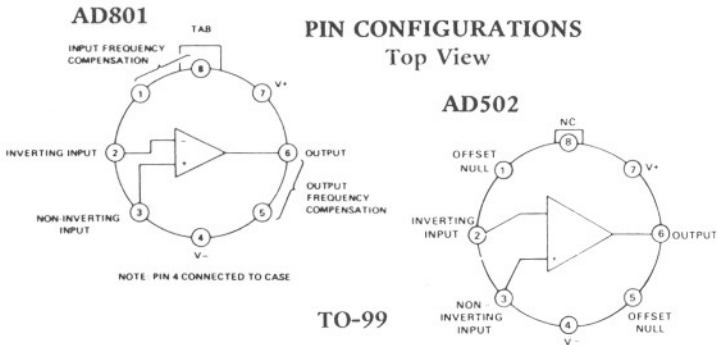


# LOW INPUT CURRENT 709, 741 REPLACEMENTS

## AD801, AD502



### GENERAL DESCRIPTION

The AD801 and AD502 are low input current replacements for the popular 709 and 741 operational amplifiers. Levels of  $I_b$  below 4nA and  $I_{OS}$  below 1nA are achieved by utilizing a Darlington input modification of the basic 709 and 741 designs, with no significant change in other operating parameters. Thus the user is afforded the opportunity of upgrading performance in his 709 and 741 sockets without resorting to a new amplifier design. The AD801 offers the 709's flexibility of external compensation; the AD502, like the 741, is internally compensated. Both devices are supplied in the TO-99 metal can package.

### SPECIFICATION SUMMARY (Typical @ $T_A = +25^\circ\text{C}$ and $V_S = \pm 15\text{V}$ unless otherwise noted)

	AD801			AD502		
	A	B	S	J	K	L
Open Loop Gain $R_L \geq 2\text{k}\Omega$ , $E_O = \pm 10\text{V}$ , min	15,000	*	*	20,000	**	**
Rated Output Voltage $R_L \geq 2\text{k}\Omega$ , min	$\pm 10\text{V}$	*	*	$\pm 10\text{V}$	**	**
Frequency Response						
Unity Gain, Small Signal	200kHz (note 1)	*	*	1MHz	**	**
Full Power Response	200kHz (note 2)	*	*	10kHz	**	**
Slew Rate	10V/ $\mu\text{sec}$ (note 2)	*	*	0.5V/ $\mu\text{sec}$	**	**
Input Offset Voltage						
@ $+25^\circ\text{C}$ max	$\pm 5\text{mV}$	*	*	$\pm 6\text{mV}$	$\pm 5\text{mV}$	$\pm 5\text{mV}$
Over Temp Range ( $T_l$ to $T_h$ ), max	$\pm 7.4\text{mV}$	$\pm 5.6\text{mV}$	$\pm 7.0\text{mV}$	$\pm 7.5\text{mV}$	$\pm 6\text{mV}$	$\pm 6\text{mV}$
Avg. vs Temp ( $T_l$ to $+25^\circ\text{C}$ to $T_h$ ), max	$\pm 40\mu\text{V}/^\circ\text{C}$	$\pm 10\mu\text{V}/^\circ\text{C}$	$\pm 20\mu\text{V}/^\circ\text{C}$	$\pm 40\mu\text{V}/^\circ\text{C}$	$\pm 10\mu\text{V}/^\circ\text{C}$	$\pm 10\mu\text{V}/^\circ\text{C}$
vs Supply Voltage, max	$\pm 200\mu\text{V}/\text{V}$	*	*	$\pm 150\mu\text{V}/\text{V}$	**	**
Input Bias Current						
@ $+25^\circ\text{C}$ , max	4nA	*	*	25nA	7nA	4nA
Over Temp Range ( $T_l$ to $T_h$ ), max	11nA	*	16nA	50nA	15nA	10nA
Input Difference Current						
@ $+25^\circ\text{C}$ , max	$\pm 2\text{nA}$	$\pm 1\text{nA}$	$\pm 2\text{nA}$	$\pm 12\text{nA}$	$\pm 4\text{nA}$	$\pm 1\text{nA}$
Over Temp Range ( $T_l$ to $T_h$ ), max	$\pm 8\text{nA}$	$\pm 2\text{nA}$	$\pm 5\text{nA}$	$\pm 24\text{nA}$	$\pm 8\text{nA}$	$\pm 2\text{nA}$
Input Impedance						
Differential, min	25M $\Omega$	*	*	25M $\Omega$	**	**
Common Mode	500M $\Omega$	*	*	500M $\Omega$	**	**
Input Voltage Noise <sup>1</sup>						
0.01 to 10Hz, p-p	100 $\mu\text{V}$	*	*	100 $\mu\text{V}$	**	**
10Hz to 5kHz, rms	6 $\mu\text{V}$	*	*	6 $\mu\text{V}$	**	**
Input Voltage Range						
Common Mode Voltage, Min	$\pm 8\text{V}$	*	*	$\pm 10\text{V}$	**	**
Common Mode Rejection, Min	65dB	*	*	70dB	**	**
Max Safe Differential Voltage	$\pm 10\text{V}$	*	*	$\pm V_S$	**	**
Power Supply						
Voltage, Rated Specification	$\pm(15 \text{ to } 16)\text{V}$	*	*	$\pm(15 \text{ to } 16)\text{V}$	**	**
Voltage, Derated Specification	$\pm(5 \text{ to } 18)\text{V}$	*	*	$\pm(5 \text{ to } 18)\text{V}$	**	**
Current, Quiescent, max	$\pm 6\text{mA}$	*	*	$\pm 2.8\text{mA}$	**	**
Temperature Range						
Operating, Rated Specifications	$T_l = -25^\circ\text{C}$ , $T_h = +85^\circ\text{C}$	$T_l = -25^\circ\text{C}$ , $T_h = +85^\circ\text{C}$	$T_l = -55^\circ\text{C}$ , $T_h = +125^\circ\text{C}$	$T_l = 0$ , $T_h = +70^\circ\text{C}$	**	**
Operating, Derated Specifications	$-55^\circ\text{C}$ to $+125^\circ\text{C}$	*	*	$-55^\circ\text{C}$ to $+125^\circ\text{C}$	**	**
Storage	$-65^\circ\text{C}$ to $+150^\circ\text{C}$	*	*	$-65^\circ\text{C}$ to $+150^\circ\text{C}$	**	**
Mechanical						
Case Style - Pin Configuration	TO-99	*	*	TO-99	**	**
Price						
1-24	\$14.00	\$19.00	\$23.00	\$4.50	\$9.00	\$19.00
25-99	\$12.00	\$16.00	\$19.00	\$3.60	\$7.20	\$15.00
100-999	\$9.75	\$13.00	\$15.00	\$3.00	\$6.00	\$12.50

#### NOTES:

1.  $C_1 = 5000\text{pF}$ ,  $R_1 = 1.5\text{k}\Omega$ ,  $C_2 = 200\text{pF}$  ( $A_{CL} = 1$ ).

2.  $C_1 = 10\text{pF}$ ,  $R_1 = 0\Omega$ ,  $C_2 = 3\text{pF}$  ( $A_{CL} = 1000$ ).

\*Specifications same as for AD801A.

\*\*Specifications same as for AD502J.