



N-Channel 60-V (D-S) MOSFET

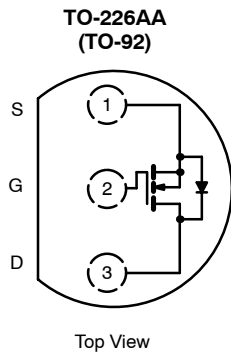
PRODUCT SUMMARY			
V _{DS} (V)	r _{DS(on)} (Ω)	V _{GS(th)} (V)	I _D (A)
60	2 @ V _{GS} = 10 V	1.0 to 2.5	0.47
	4 @ V _{GS} = 4.5 V		0.33

FEATURES

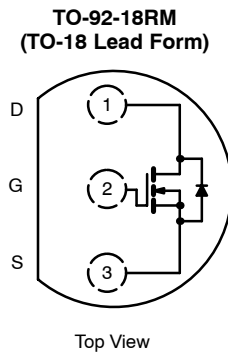
- TrenchFET® Power MOSFET
- ESD Protected: 2000 V

APPLICATIONS

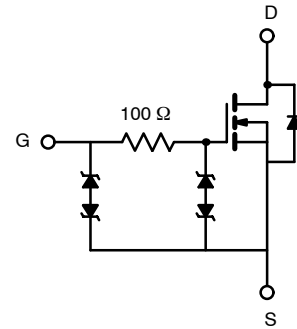
- Direct Logic-Level Interface: TTL/CMOS
- Solid State Relays
- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories, Transistors, etc.
- Battery Operated Systems



Ordering Information: 2N7000KL-TR1



Ordering Information: BS170KL-TR1



ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C UNLESS OTHERWISE NOTED)			
Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	60	V
Gate-Source Voltage	V _{GS}	± 20	
Continuous Drain Current (T _J = 150 °C)	I _D	T _A = 25 °C	0.47
		T _A = 70 °C	0.37
Pulsed Drain Current ^a	I _{DM}	1.0	A
Power Dissipation	P _D	T _A = 25 °C	0.8
		T _A = 70 °C	0.51
Maximum Junction-to-Ambient	R _{thJA}	156	°C/W
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55 to 150	°C

Notes

a. Pulse width limited by maximum junction temperature.



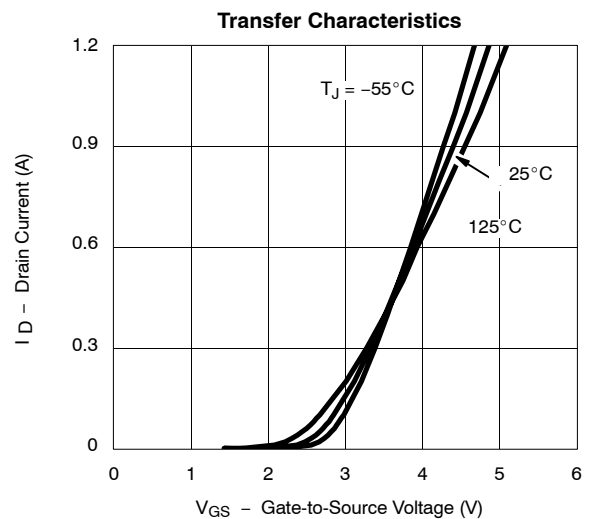
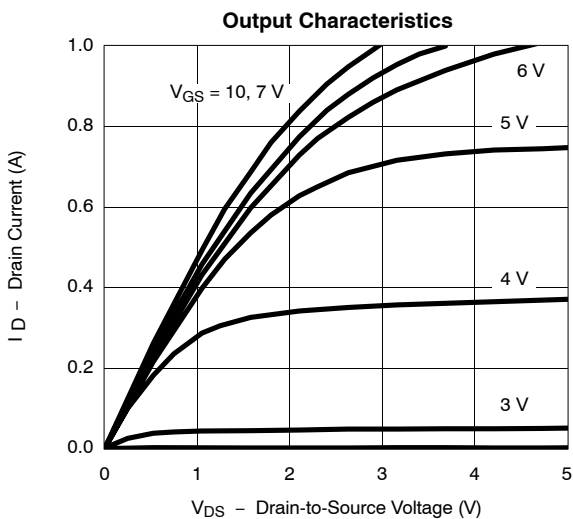
SPECIFICATIONS^a (T_A = 25 °C UNLESS OTHERWISE NOTED)

Parameter	Symbol	Test Conditions	Limits			Unit
			Min	Typ	Max	
Static						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0 V, I _D = 10 μA	60			V
Gate-Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	1	2.0	2.5	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ±10 V			±1	μA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 60 V, V _{GS} = 0 V			1	μA
		V _{DS} = 60 V, V _{GS} = 0 V, T _J = 55 °C			10	
On-State Drain Current ^b	I _{D(on)}	V _{GS} = 10 V, V _{DS} = 7.5 V	0.8			A
		V _{GS} = 4.5 V, V _{DS} = 10 V	0.5			
Drain-Source On-Resistance ^b	r _{DS(on)}	V _{GS} = 10 V, I _D = 0.5 A		1.1	2	Ω
		V _{GS} = 4.5 V, I _D = 0.2 A		1.6	4	
Forward Transconductance ^b	g _{fs}	V _{DS} = 10 V, I _D = 0.5 A		550		mS
Diode Forward Voltage	V _{SD}	I _S = 0.3 A, V _{GS} = 0 V		0.87	1.3	V
Dynamic^b						
Total Gate Charge	Q _g	V _{DS} = 10 V, V _{GS} = 4.5 V I _D ≅ 0.25 A		0.4	0.6	nC
Gate-Source Charge	Q _{gs}			0.11		
Gate-Drain Charge	Q _{gd}			0.15		
Gate Resistance	R _g			173		Ω
Turn-On Time	t _{d(on)}	V _{DD} = 30 V, R _L = 150 Ω I _D ≅ 0.2 A, V _{GEN} = 10 V R _g = 10 Ω		3.8	10	ns
	t _r			4.8	15	
Turn-Off Time	t _{d(off)}			12.8	20	
	t _f			9.6	15	

Notes

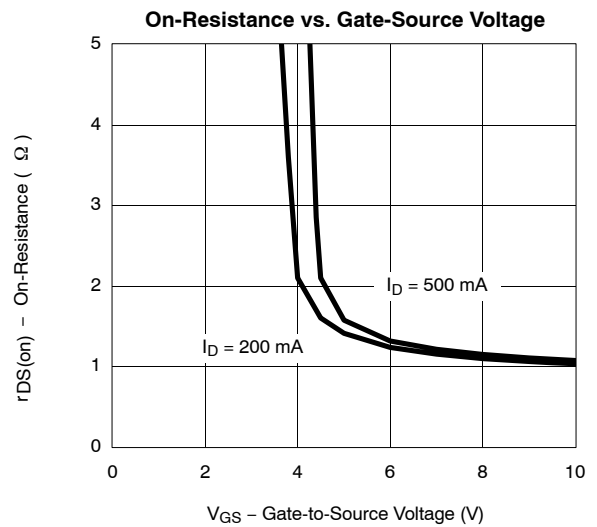
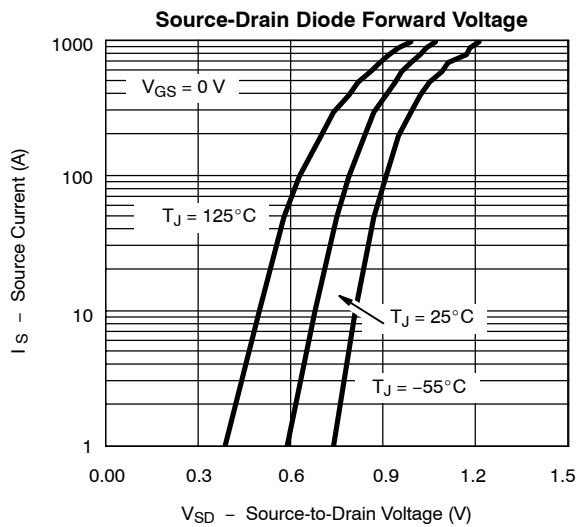
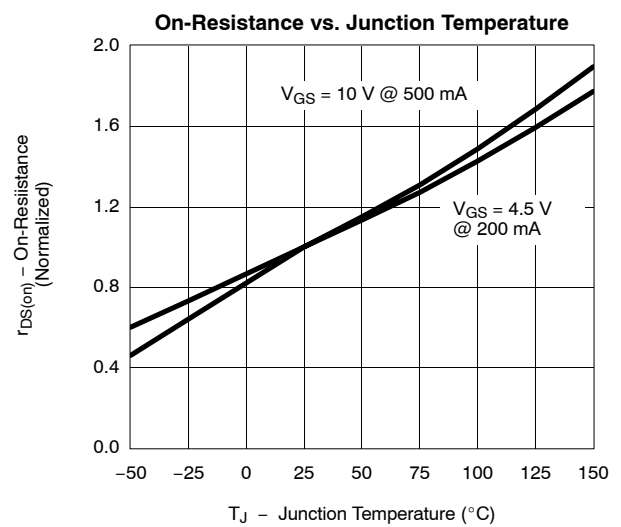
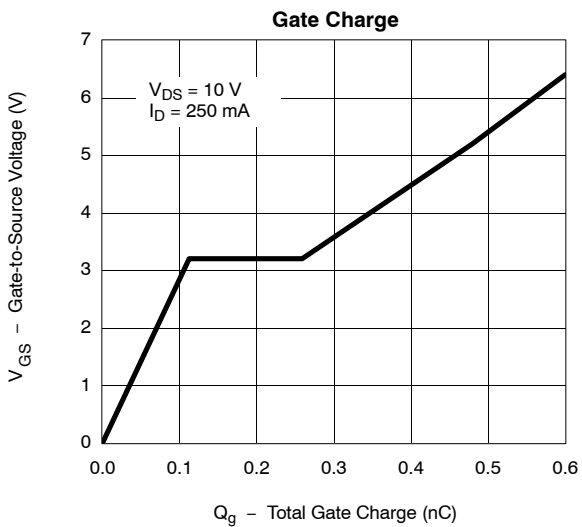
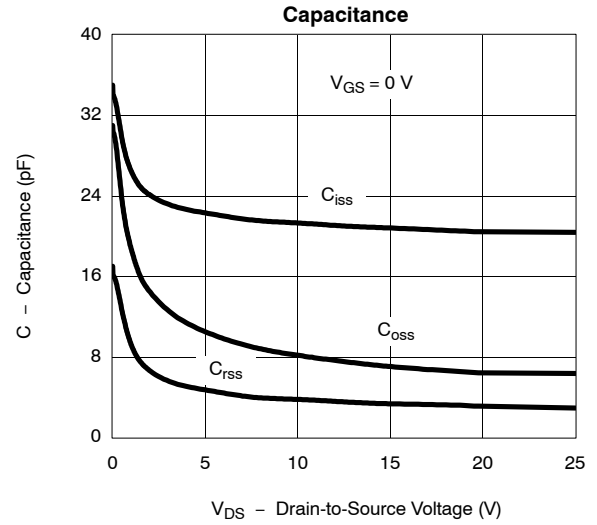
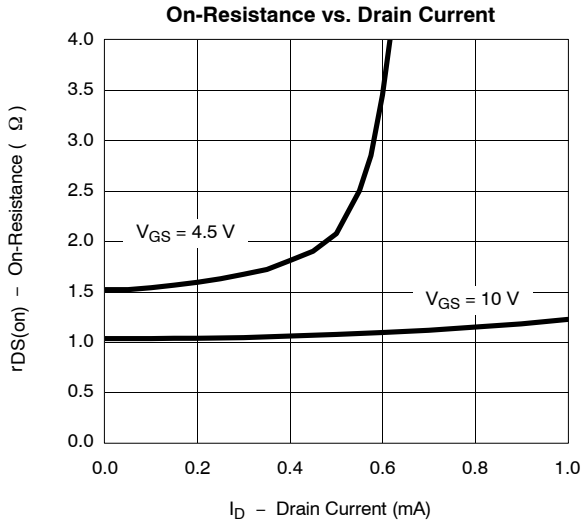
- a. Pulse test: PW ≤ 300 μs duty cycle ≤ 2%.
- b. Guaranteed by design, not subject to production testing.

TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)



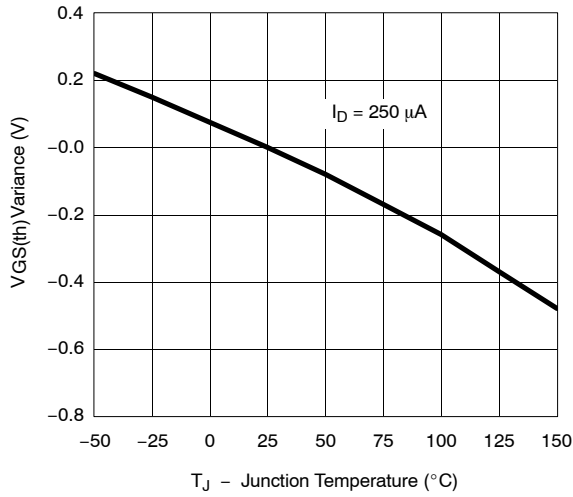


TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

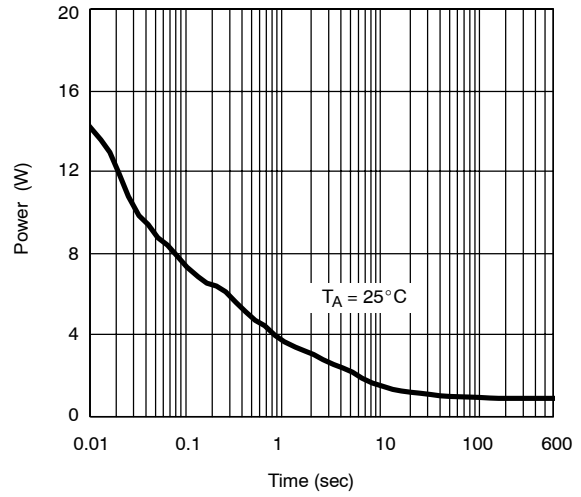


TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

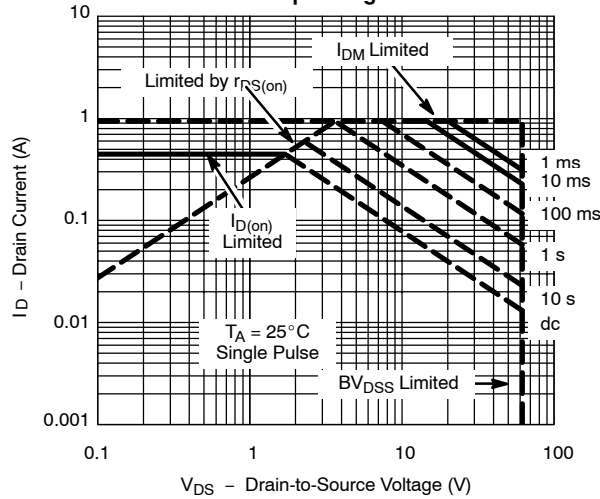
Threshold Voltage Variance Over Temperature



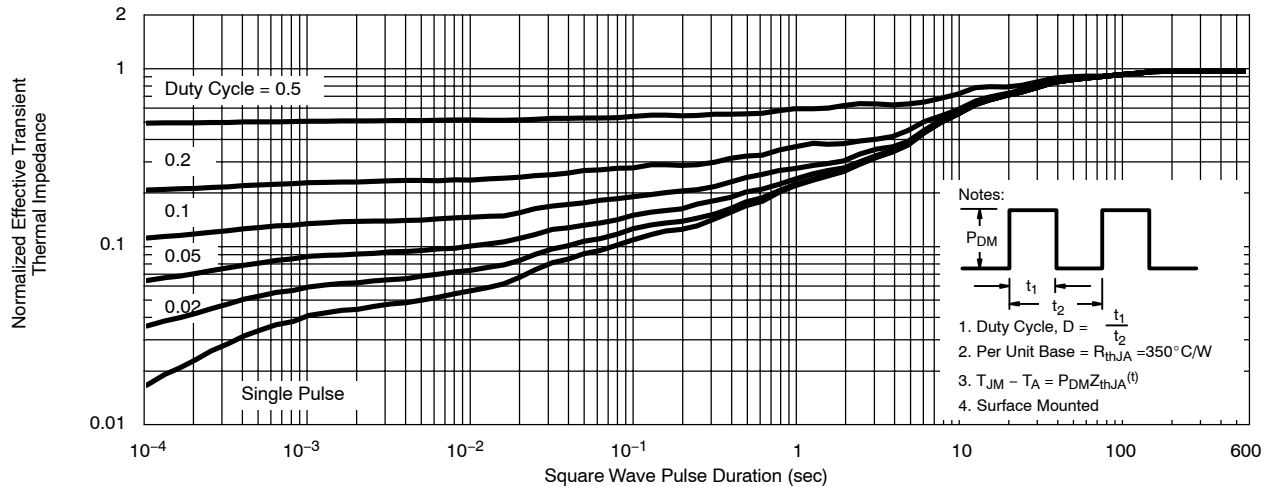
Single Pulse Power, Junction-to-Ambient



Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient





Notice

Specifications of the products displayed herein are subject to change without notice. Vishay Intertechnology, Inc., or anyone on its behalf, assumes no responsibility or liability for any errors or inaccuracies.

Information contained herein is intended to provide a product description only. No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document. Except as provided in Vishay's terms and conditions of sale for such products, Vishay assumes no liability whatsoever, and disclaims any express or implied warranty, relating to sale and/or use of Vishay products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright, or other intellectual property right.

The products shown herein are not designed for use in medical, life-saving, or life-sustaining applications. Customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Vishay for any damages resulting from such improper use or sale.