

Marketing Bulletin

DATE: August 25, 2005

TO: All Sales Personnel

FROM: Mark Stoner

RE: Product Termination

To all concerned parties,

This bulletin is to notify all customers of the discontinuation of the following Ecliptek series effective August 25th, 2005:

Series	Description	Recommended Replacement
E31J2	5V 6 pad SMD LVPECL VCXO	E32D1
E32J2	3.3V 6 pad SMD LVPECL VCXO	E32D1

In compliance with our End of Life (EOL) policy, this will serve as advanced notice of product termination. New orders will not be accepted after November 25th, 2005, with delivery to conclude by February 25th 2006.

If there are any questions pertaining to this bulletin, please fell free to contact me. Thank you again for your cooperation.

Best Regards,

Mark W. Stoner

Director of Marketing Ecliptek Corporation

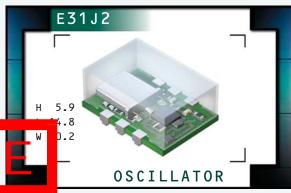
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E31J2 Series

- PECL Output VCXO
- 5.0V supply voltage
- 6 pad PCB SMD package with J-leads
- Stability to 20ppm
- Output Ena
- Available o Tape and Rec







ELECTRICAL SPECIFICATIONS

Frequency Range				19.440MHz to 212.500MH	lz	
Operating Temperature Range				0°C to 70°C or -40°C to 8		
Storage Temperature Range				-55°C to 125°C		
Supply Voltage (V _{cc})				5.0V _{DC} ±5%		
Input Current				100mA Maximum		
Logic Type				100KH		
Frequency Tolerance / Stability	Inclusive of	f Operating Temp Range	e, Supply Voltage,	+50ppm, +25ppm, or		
.,,		Aging @25°C over 10 ye		±20ppm Maximum		
Output Voltage Logic High (V _{OH})	·	3 3 3 3		V _{CC} -1.025V _{DC} Minimum		
Output Voltage Logic Low (V _{OL})				V _{cc} -1.620V _{pc} Maximum		
Rise Time / Fall Time	20% to 80%	% of waveform		2 nSeconds Maximum		
Duty Cycle	at 50% of w	vaveform		50 ±10(%)		
				50 ±5(%)		
Load Drive Capability				50 Ohms into V _{cc} -2.0V _{DC}		
Additional Output / Logic Control				No Connect and Single Outpu	ıt	
				Enable/Disable and Single Output No Connect and Complementary Output		
				Enable/Disable and Complen	nentary Outpu	
Enable/Disable Input Voltage	V _{IL} of V _{CC} -1.4	475V _{DC} Maximum		Enables Output		
	No Connect	ion		Enables Output		
	V_{IH} of V_{CC} -1.	165V _{DC} Minimum		Disables Output: Logic Logic	W	
				Disables Complementary Out	put: Logic Hig	
Start Up Time				10 mSeconds Maximum		
RMS Phase Jitter	FJ = 12kHz	to 20MHz		1 pSec Maximum		
Absolute Pull Range (APR)	Inclusive of	Operating Temp Range	e, Supply Voltage,	±50ppm Minimum		
	Load, and A	Aging @25°C over 10 ye	ears			
Linearity				20%, 15%, or 10% Maxim	um	
Control Voltage (V _c): Test Conditions for	r APR			2.5V _{DC} ±2.0V _{DC}		
Control Voltage Range (V _{CR})		$0.0V_{DC}$ to V_{CC}				
Center Control Voltage				2.5V _{DC}		
Transfer Function				Positive Transfer Characte	ristic	
Input Impedance				50k0hms Typical		
Modulation Bandwidth	at 2dD wit	h Control Voltage of +2	5V	10kHz Minimum		
riodatation bandwidth	at-3ub wit	ii control vollage of +2	· J v DC	101112 1 11111111111111		

PART NUMBERING GUIDE

E31J2 F 3 A 2 C - 155.520M TR

FREQUENCY TOLERANCE & STABILITY/ **OPERATING TEMPERATURE RANGE**

D=±50ppm Maximum over 0°C to +70°C E=±25ppm Maximum over 0°C to +70°C F=±20ppm Maximum over 0°C to +70°C H=±50ppm Maximum over -40°C to +85°C

APR

3=±50ppm Minimum

LINEARITY

A=20% B=15% C=10%

AVAILABLE OPTIONS

Blank=Tubes

TR = Tape and Reel (Standard)

FREQUENCY

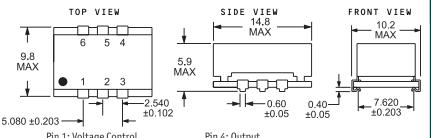
ADDITIONAL OUTPUT/LOGIC CONTROL

A=No Connect and Single Output B=Enable/Disable and Single Output C=No Connect and Complementary Output D=Enable/Disable and Complementary Output

DUTY CYCLE

 $1=50\pm10(\%), 2=50\pm5(\%)$

MECHANICAL DIMENSIONS ALL DIMENSIONS IN MILLIMETERS



Pin 1: Voltage Control

Pin 2: Enable/Disable or No Connect

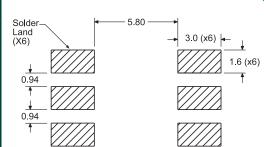
Pin 3: Case Ground

Pin 4: Output

Pin 5: Complementary Output or No Connect

Pin 6: Supply Voltage

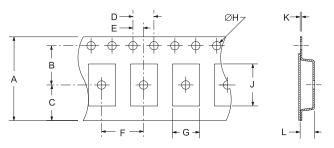
SUGGESTED SOLDER PAD LAYOUT ALL DIMENSIONS IN MILLIMETERS



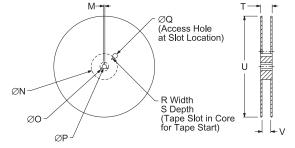
Tolerances = ± 0.1

TAPE AND REEL DIMENSIONS

ALL DIMENSIONS IN MILLIMETERS



TAPE	А	В	С	D	Е
	24 ±.3	11.5 ±.1	10.75 ±.1	4 ±.2	2±.1
F	G	Н	J	K	L
12 ±.1	B0*	1.5 +.1-0	A0*	.4 ±.05	K0*



REEL	М	N	0	Р	Q
	1.5 MIN	50 MIN	20.2 MIN	13±.2	40 MIN
R	S	T	U	V	QTY/REEL
2.5 MIN	10 MIN	30.4 MAX	360 MAX	24.4+2-0	1000

*Compliant to EIA 481A

ENVIRONMENTAL/MECHANICAL SPECIFICATIONS

Characteristic **Specification**

Seal Integrity Bubble test in Perfluorocarbon at +125°C ±5°C for 60 seconds

minimum (internal crystal only).

Solderability Sn63 Solder dip at +230°C ±5°C for 5 seconds/95% coverage.

10 Strokes with brush after 1 minute soak in solvent, 3 times. Marking Permanency

Shock Random drop on hard wooden plate 3 times from a height

of 20cm.

Frequency with an amplitude of 1.5mm sweeping between 10Hz Vibration

to 55Hz within 1 minute (approximately) for 2 hours minimum on

each axis (X, Y and Z) for a total of 6 hours.

MARKING SPECIFICATIONS

Line 1: ECLIPTEK

Line 2: XX.XXX M

Frequency in MHz (5 Digits Maximum + Decimal)

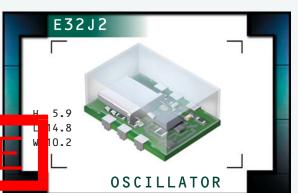
Line 3: XX Y ZZ Week of Year Last Digit of Year Ecliptek Manufacturing Identifier

MANUFACTURER CATEGORY PACKAGE SERIES VOLTAGE CLASS REV - DATE ECLIPTEK CORP. OSCILLATOR 6-PCB-J 01/03 0S74 E31J2 5.0V

E32J2 Series



- PECL Output VCXO
- 3.3V supply voltage
- 6 pad PCB SMD package with J-leads
- Stability to 20nnm
- Output Enable/Disable available
- Compleme taly Quippt avoidable
- Available (Tano and Pool



ELECTRICAL SPECIFICATIONS

Frequency Range				19.440MHz to 212.500MI	Hz
Operating Temperature Range				0°C to 70°C or -40°C to 8	35°C
Storage Temperature Range				-55°C to 125°C	
Supply Voltage (V _{cc})				3.3V _{DC} ±5%	
Input Current				75mA Maximum	
Logic Type				100KH	
Frequency Tolerance / Stability	Inclusive o	of Operating Temp Range,	Supply Voltage,	±50ppm, ±25ppm, or	
	Load, and	Aging @25°C over 10 year	ars	±20ppm Maximum	
Output Voltage Logic High (V _{OH})				V _{CC} -1.025V _{DC} Minimum	
Output Voltage Logic Low (V _{OL})				V _{CC} -1.620V _{DC} Maximum	
Rise Time / Fall Time	20% to 80	% of waveform		2 nSeconds Maximum	
Duty Cycle	at 50% of	waveform		50 ±10(%)	
				50 ±5(%)	
Load Drive Capability				50 Ohms into V _{CC} -2.0V _{DC}	
Additional Output / Logic Control				No Connect and Single Outp	ut
				Enable/Disable and Single Output	
				No Connect and Complementary Outpu	
				Enable/Disable and Comple	mentary Outpu
Enable/Disable Input Voltage	$V_{\rm IL}$ of $V_{\rm CC}$ -1	.475V _{DC} Maximum		Enables Output	
	No Connec	ction		Enables Output	
	V_{IH} of V_{CC} -1	.165V _{DC} Minimum		Disables Output: Logic Lo)W
				Disables Complementary Ou	tput: Logic Hig
Start Up Time				10 mSeconds Maximum	
RMS Phase Jitter	FJ = 12kHz	z to 20MHz	1 pSec Maximum		
Absolute Pull Range (APR)	Inclusive of	of Operating Temp Range,	Supply Voltage,	±50ppm Minimum	
	Load, and	Aging @25°C over 10 year	ars		
Linearity				20%, 15%, or 10% Maxim	num
Control Voltage (V _c): Test Conditions i	or APR			$1.65V_{DC} \pm 1.35V_{DC}$	
Control Voltage Range (V _{CR})		$0.0V_{DC}$ to V_{CC}			
Center Control Voltage				1.65V _{DC}	
Transfer Function				Positive Transfer Characte	eristic
Input Impedance				50k0hms Typical	
Modulation Bandwidth	at -3dB wi	th Control Voltage of +1.0	55V _{DC}	10kHz Minimum	
MANUFACTURER CATEGORY	SERIES	PACKAGE	VOLTAGE	CLASS	REV - DATI
ECLIPTEK CORP. OSCILLATOR	E32J2	6-PCB-J	3.3V	0S73	01/03

OBSOLETE

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3=±50ppm Minimum

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Blank=Tubes

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ADDITIONAL OUTPUT/LOGIC CONTROL

A=No Connect and Single Output
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C=No Connect and Complementary Output
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DUTY CYCLE

 $1=50\pm10(\%), 2=50\pm5(\%)$

MECHANICAL DIMENSIONS ALL DIMENSIONS IN MILLIMETERS FRONT VIEW TOP VIEW SIDE VIEW 14.8 10.2 MAX MAX 5 4 9.8 5.9 MAX MAX 2 3 1 ЦЦ 2 540 -0.607.620 0.40 ±0.102 ±0.203 ±0.05 ±0.05

Pin 1: Voltage Control

Pin 2: Enable/Disable or No Connect

Pin 3: Case Ground

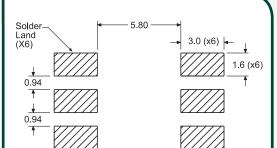
5.080 ±0.203

Pin 4: Output

Pin 5: Complementary Output or No Connect

Pin 6: Supply Voltage

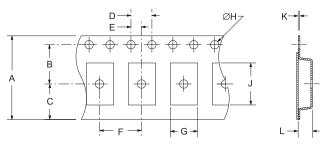
SUGGESTED SOLDER PAD LAYOUT ALL DIMENSIONS IN MILLIMETERS



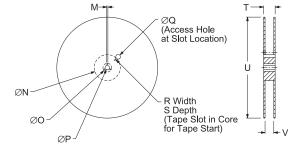
Tolerances = ± 0.1

TAPE AND REEL DIMENSIONS

ALL DIMENSIONS IN MILLIMETERS



	TAPE		A	В	С	D	Е
ı		24	±.3	11.5 ±.1	10.75 ±.1	4 ±.2	2±.1
ı	F		G	Н	J	K	L
ı	12 ±.1		B0*	1.5 +.1-0	O A0*	.4 ±.05	K0*



REEL	М	N	0	Р	Q
	1.5 MIN	50 MIN	20.2 MIN	13±.2	40 MIN
R	S	T	U	V	QTY/REEL
2.5 MIN	10 MIN	30.4 MAX	360 MAX	24.4+2-0	1000

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Line 2: XX.XXX M

Frequency in MHz (5 Digits Maximum + Decimal)

Line 3: XX Y ZZ

Week of Year

Last Digit of Year

Ecliptek Manufacturing Identifier

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