EB72F72 Series

- Oven Controlled Crystal Oscillator (OCXO)
- SC-Cut Crystal
- HCMOS output
- 3.3V supply voltage
- 5 pin DIP package
- External control voltage
- Stability to ±30ppb





ELECTRICAL SPECIFICATIONS

Frequency Range	10.000MHz, 12.288MH	z, 12.800MHz, 16.000MH					
Operating Temperature Range (OTR)			0°C to 50°C, 0°C to 70°C, or -20°C to 70°C				
Storage Temperature Range			-55°C to 125°C				
Supply Voltage (V _{DD})		3.3V _{DC} ±5%	3.3V _{DC} ±5%				
Frequency Tolerance / Stability							
vs. Initial Tolerance	at Nominal V_{DD} and V_{C} ,	at 25°C		±500ppb or ±300ppb Maximum			
vs. Temperature Stability	at Nominal V_{DD} and V_{C}			±30ppb, ±50ppb, ±80ppb, ±100ppb, ±200pp			
			or ±280ppb				
vs. Vdd	$V_{DD} \pm 5\%$			±20ppb Maximum			
vs. Load	Vload ±5%		±20ppb Ma	±20ppb Maximum			
vs. Aging (1 Day)	after 72 Hours of Opera		2.0ppb Max	2.0ppb Maximum			
vs. Aging (1 Year)	after 72 Hours of Opera		±100ppb M	±100ppb Maximum			
vs. Aging (10 Years)	after 72 Hours of Opera	ation	±500ppb M	±500ppb Maximum			
Crystal Cut			SC-Cut	SC-Cut			
Warm Up Time	to ±50ppb of Final Frequ		3 Minute M	3 Minute Maximum			
Power Consumption	at Steady State, at 25°	C	1.2 Watts M	1.2 Watts Maximum			
	During Warm Up, at 25	°C	3.6 Watts M	laximum			
Output Voltage Logic High (V _{OH})	$I_{OH} = -4mA$		2.6V _{DC} Minii	2.6V _{DC} Minimum			
Output Voltage Logic Low (V _{OL})	$I_{0L} = +4mA$		0.4V _{DC} Maxi	0.4V _{DC} Maximum			
Rise Time / Fall Time	Measured at 20% to 80		6nSec Maxi	6nSec Maximum			
Duty Cycle	Measured at 50% of Wa	veform	50 ±5(%)				
Load Drive Capability				15pF HCMOS Load Maximum			
Frequency Deviation	Referenced to F_0 at $V_c = 1$	$.65V_{DC}$; V_{DD} =5.0 V_{DC} over OTF		±0.5ppm Minimum			
Control Voltage Range			$0.0V_{DC}$ to V_{D}	$0.0V_{DC}$ to V_{DD}			
Control Voltage (V _c)				$1.65V_{DC} \pm 1.65V_{DC}$			
Transfer Function		Positive Tra	Positive Transfer Characteristic				
Reference Voltage Output			$2.8V_{DC} \pm 0.2$	2.8V _{DC} ±0.2V _{DC} (Pin 4)			
Linearity	±10% Maxi	±10% Maximum					
Input Impedance	10k0hms Ty	10k0hms Typical					
Typical Phase Noise (at 12.800MHz)	-90dBc/Hz	-90dBc/Hz					
	10Hz Offset		-100dBc/H	Z			
	100Hz Offset 1kHz Offset		-130dBc/H	Z			
	-145dBc/H	-145dBc/Hz					
	10kHz Offset		-150dBc/H	Z			

5 pin DIP

ECLIPTEK CORP.

PART NUMBERING GUIDE

EB72F72 D 10 B V 2 - 20.000M

INITIAL TOLERANCE
D=±500ppb
E=±300ppb

FREQUENCY STABILITY
2 Digit Code Per Table 1

OPERATING TEMPERATURE RANGE

FREQUENCY

FREQUENCY

VOLTAGE CONTROL OPTION
V=Voltage Control on Pin 3 and Reference
Voltage Output on Pin 4

OPERATING TEMPERATURE RANGE 1 Letter Code Per Table 1

	TABLE 1: PART NUMBERING CODES											
Range		FREQUENCY STABILITY X Denotes availability										
ature			±30ppb	±50ppb	±80ppb	±100ppb	±200ppb	±280ppb				
Operating Temperature		Code	03	05	08	10	20	28				
	0°C to +50°C	А	Х	Х	Х	Х	Х	Х				
	0°C to +70°C	В		Х	Х	Х	Х	Х				
do	-20°C to +70°C	С			Х	Х	Х	Х				

