

Silicon NPN Epitaxial

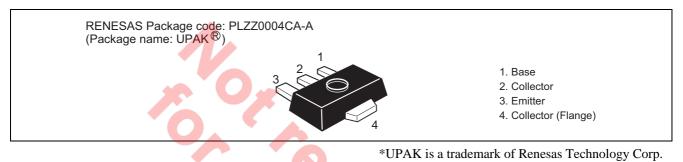
REJ03G0789-0200 (Previous ADE-208-1152) Rev.2.00 Aug.10.2005

2500

Application

Low frequency power amplifier

Outline



Absolute Maximum Ratings

			$(Ta = 25^{\circ}C)$
Item	Symbol	Ratings	Unit
Collector to base voltage	V _{CBO}	180	V
Collector to emitter voltage	V _{CEO}	160	V
Emitter to base voltage	V _{EBO}	5	V
Collector current	I _C	1.5	А
Collector peak current	i _{C(peak)} *1	3	А
Collector power dissipation	Pc* ²		W
Junction temperature	Tj	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. $PW \le 10 \text{ ms}$, Duty cycle $\le 20\%$

2. Value on the alumina ceramic board (12.5 x 20 x 0.7 mm)



Electrical Characteristics

						$(Ta = 25^{\circ}C)$
ltem	Symbol	Min	Тур	Max	Unit	Test conditions
Collector to base breakdown voltage	V _{(BR)CBO}	180	_		V	I _C = 1 mA, I _E = 0
Collector to emitter breakdown voltage	V _{(BR)CEO}	160			V	I_C = 10 mA, R_{BE} = ∞
Emitter to base breakdown voltage	V _{(BR)EBO}	5	_	_	V	$I_E = 1 \text{ mA}, I_C = 0$
Collector cutoff current	I _{CBO}	_	_	10	μA	V _{CB} = 160 V, I _E = 0
DC current transfer ratio	h _{FE1} * ¹	60		200		V _{CE} = 5 V, I _C = 0.15 A
	h _{FE2}	30	_	_		V _{CE} = 5 V, I _C = 0.5 A
Collector to emitter saturation voltage	V _{CE(sat)}	_		1.0	V	I_C = 0.5 A, I_B = 50 mA, Pulse
Base to emitter voltage	V _{BE}	_		0.9	V	V_{CE} = 5 V, I_C = 0.15 A, Pulse

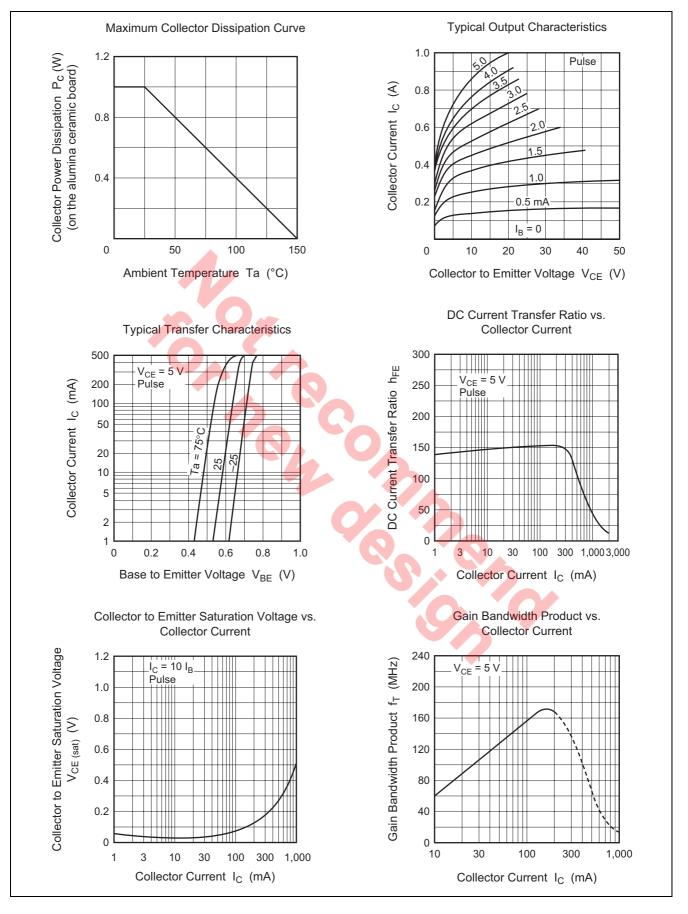
Note: 1. The 2SD1421 is grouped by h_{FE1} as follows.

Mark	ED	EE	
h _{FE1}	60 to 120	100 to 200	

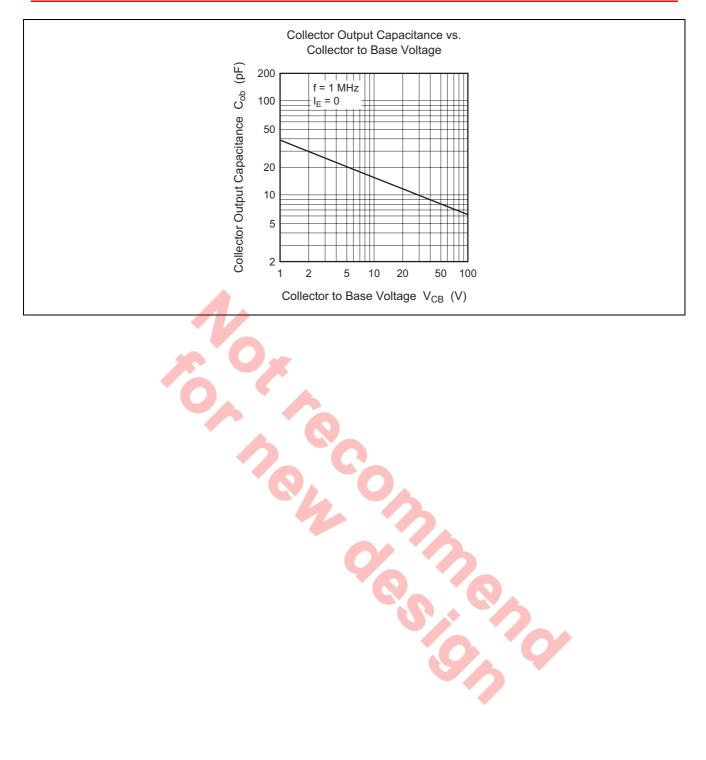
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Main Characteristics

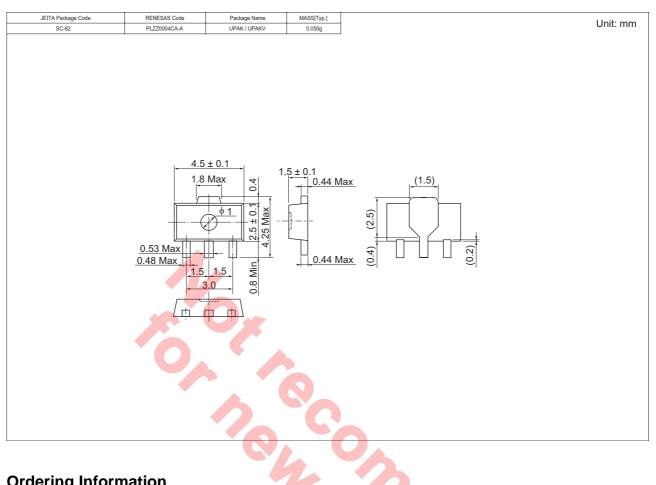








Package Dimensions



Ordering Information

Part Name	Quantity	Shipping Container
2SD1421EDTR-E	1000	the two sets of the two sets the two
2SD1421EETR-E		

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.



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