

# 2SJ517

# Silicon P Channel MOS FET

REJ03G0874-0400

(Previous: ADE-208-575B)

Rev.4.00 Sep 07, 2005

### **Description**

High speed power switching

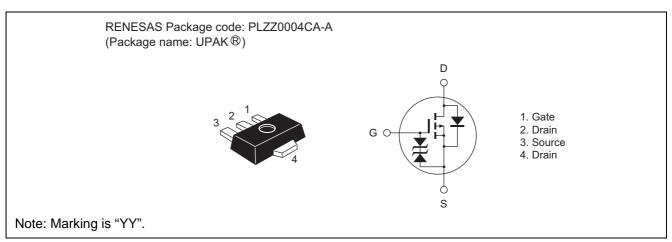
#### **Features**

• Low on-resistance

$$R_{DS\,(on)} = 0.18~\Omega$$
 typ. (at  $V_{GS} = -4~V,~I_D = -1~A)$ 

- Low drive current
- High speed switching
- 2.5 V gate drive devices.

#### **Outline**



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# **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Value	Unit
Drain to source voltage	V <sub>DSS</sub>	-20	V
Gate to source voltage	V <sub>GSS</sub>	±10	V
Drain current	I <sub>D</sub>	-2	Α
Drain peak current	I <sub>D (pulse)</sub> Note 1	-4	Α
Body to drain diode reverse drain current	I <sub>DR</sub>	-2	Α
Channel dissipation	Pch Note 2	1	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW  $\leq$  100  $\mu$ s, duty cycle  $\leq$  10%

2. When using the aluminium ceramic board (12.5  $\times$  20  $\times$  0.7 mm)

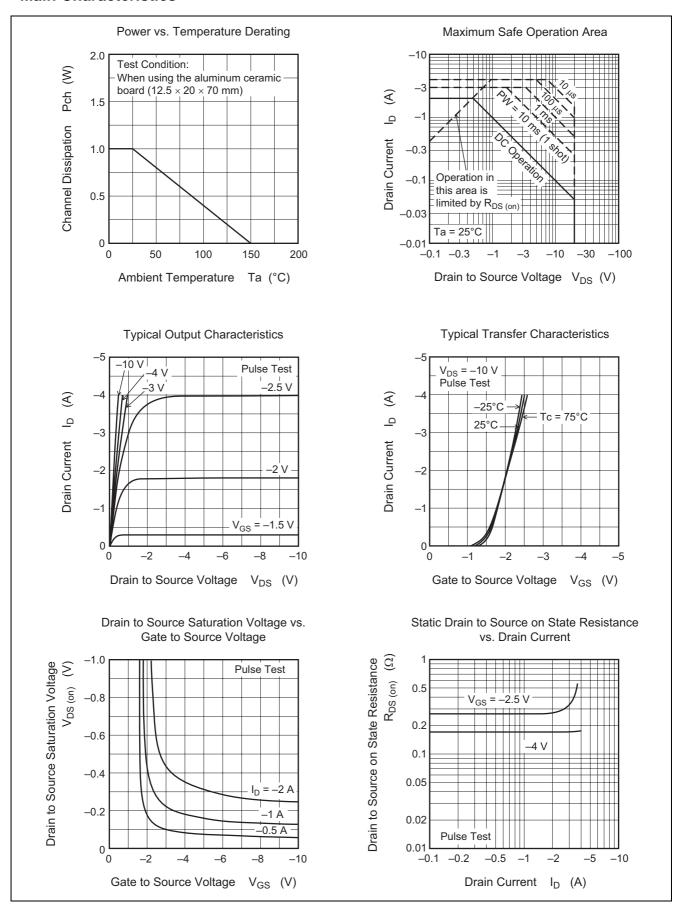
### **Electrical Characteristics**

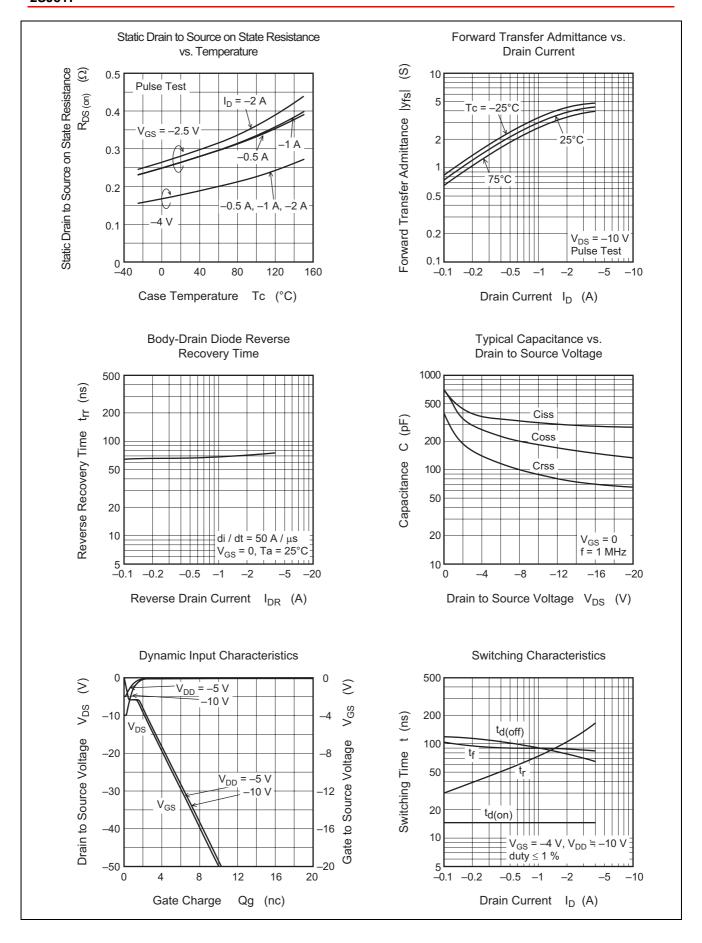
 $(Ta = 25^{\circ}C)$ 

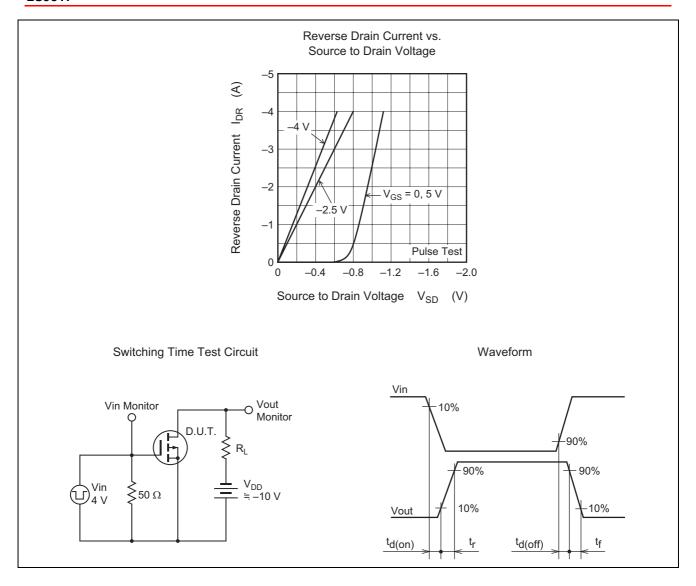
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V <sub>(BR) DSS</sub>	-20	_	_	V	$I_D = -10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	V <sub>(BR) GSS</sub>	±10	_	_	V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	_	_	-10	μΑ	$V_{DS} = -20 \text{ V}, V_{GS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_	_	±10	μΑ	$V_{GS} = \pm 8 \text{ V}, V_{DS} = 0$
Gate to source cutoff voltage	V <sub>GS (off)</sub>	-0.5	_	-1.5	V	$I_D = -1 \text{ mA}, V_{DS} = -10 \text{ V}$
Static drain to source on state resistance	R <sub>DS (on)</sub>	_	0.18	0.24	Ω	$I_D = -1 A, V_{GS} = -4 V^{\text{Note 3}}$
	R <sub>DS (on)</sub>	_	0.27	0.43	Ω	$I_D = -1 \text{ A}, V_{GS} = -2.5 \text{ V}^{\text{Note 3}}$
Forward transfer admittance	y <sub>fs</sub>	1.8	3.0	_	S	$I_D = -1 \text{ A}, V_{DS} = -10 \text{ V}^{\text{Note 3}}$
Input capacitance	Ciss	_	320	_	pF	V <sub>DS</sub> = -10 V
Output capacitance	Coss	_	190	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	90	_	pF	f = 1 MHz
Turn-on delay time	t <sub>d (on)</sub>	_	14	_	ns	$V_{GS} = -4 \text{ V}$
Rise time	t <sub>r</sub>	_	75	_	ns	$I_D = -1 A$
Turn-off delay time	t <sub>d (off)</sub>	_	90	_	ns	$R_L = 10 \Omega$
Fall time	t <sub>f</sub>	_	90	_	ns	
Body to drain diode forward voltage	$V_{DF}$	_	-0.95	_	V	$I_F = -2 \text{ A}, V_{GS} = 0$
Body to drain diode reverse recovery	t <sub>rr</sub>	_	70	_	ns	$I_F = -2 A, V_{GS} = 0$
time						$di_F/dt = 50 A/\mu s$

Note: 3. Pulse test

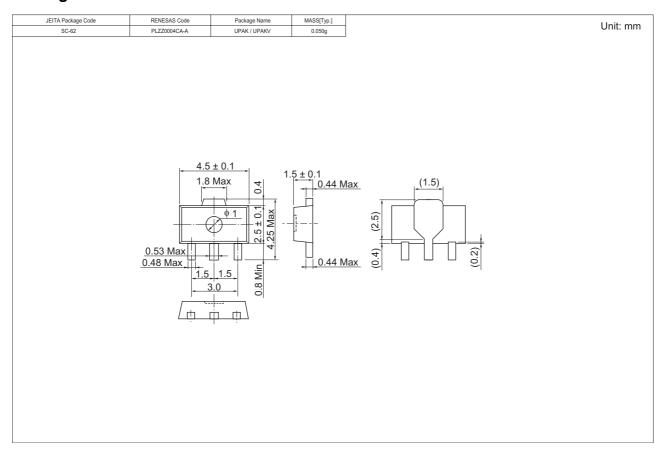
### **Main Characteristics**







# **Package Dimensions**



# **Ordering Information**

Part Name	Quantity	Shipping Container			
2SJ517YYTL-E	1000 pcs	Taping			
2SJ517YYTR-E	1000 pcs	Taping			

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.

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