

# **HD74HCT245**

## Octal Bus Transceivers (with 3-state outputs)

REJ03D0665-0200 (Previous ADE-205-554) Rev.2.00 Mar 30, 2006

### **Description**

This device has an active low enable input  $\overline{G}$  and a direction control input (DIR). When DIR is high, data flows from the A inputs to the B outputs. When DIR is low, data flows from the B inputs to the A outputs. The HD74HCT245 transfers true data from one bus to the other.

This device does not have schmitt trigger inputs.

#### **Features**

• LSTTL Output Logic Level Compatibility as well as CMOS Output Compatibility

• High Speed Operation:  $t_{pd}$  (A to Y) = 12 ns typ ( $C_L = 50 \text{ pF}$ )

• High Output Current: Fanout of 15 LSTTL Loads

• Wide Operating Voltage:  $V_{CC} = 4.5$  to 5.5 V

• Low Input Current: 1 µA max

• Low Quiescent Supply Current:  $I_{CC}$  (static) = 4  $\mu$ A max (Ta = 25°C)

• Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74HC245P	DILP-20 pin	PRDP0020AC-B (DP-20NEV)	Р	_
HD74HC245FPEL	SOP-20 pin (JEITA)	PRSP0020DD-B (FP-20DAV)	FP	EL (2,000 pcs/reel)
HD74HC245RPEL	SOP-20 pin (JEDEC)	PRSP0020DC-A (FP-20DBV)	RP	EL (1,000 pcs/reel)
HD74HC245TELL	TSSOP-20 pin	PTSP0020JB-A (TTP-20DAV)	Т	ELL (2,000 pcs/reel)

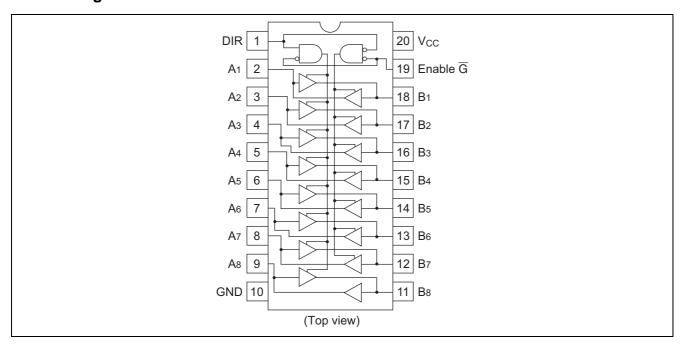
Note: Please consult the sales office for the above package availability.

#### **Function Table**

Enable $\overline{G}$	Direction Control DIR	Operation
L	L	B data to A bus
L	Н	A data to B bus
Н	X	Isolation

H: high levelL: low levelX: irrelevant

### **Pin Arrangement**



### **Absolute Maximum Ratings**

Symbol	Ratings	Unit
V <sub>CC</sub>	-0.5 to 7.0	V
V <sub>IN</sub> , V <sub>OUT</sub>	-0.5 to V <sub>CC</sub> +0.5	V
I <sub>IK</sub> , I <sub>OK</sub>	±20	mA
I <sub>O</sub>	±35	mA
I <sub>CC</sub> or I <sub>GND</sub>	±75	mA
P <sub>T</sub>	500	mW
Tstg	-65 to +150	°C
	V <sub>CC</sub> V <sub>IN</sub> , V <sub>OUT</sub> I <sub>IK</sub> , I <sub>OK</sub> I <sub>O</sub> I <sub>CC</sub> or I <sub>GND</sub> P <sub>T</sub>	V <sub>CC</sub> −0.5 to 7.0       V <sub>IN</sub> , V <sub>OUT</sub> −0.5 to V <sub>CC</sub> +0.5       I <sub>IK</sub> , I <sub>OK</sub> ±20       I <sub>O</sub> ±35       I <sub>CC</sub> or I <sub>GND</sub> ±75       P <sub>T</sub> 500

Note: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

### **Recommended Operating Conditions**

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	Vcc	4.5 to 5.5	V	
Input / Output voltage	V <sub>IN</sub> , V <sub>OUT</sub>	0 to V <sub>CC</sub>	V	
Operating temperature	Та	-40 to 85	°C	
Input rise / fall time*1	t <sub>r</sub> , t <sub>f</sub>	0 to 500	ns	V <sub>CC</sub> = 4.5 V

Notes: 1. This item guarantees maximum limit when one input switches.

Waveform: Refer to test circuit of switching characteristics.

### **Electrical Characteristics**

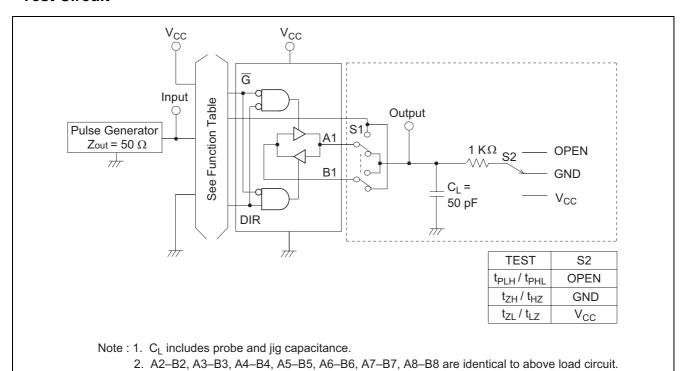
Item	Symbol	V <sub>cc</sub> (V)	Ta = 25°C			Ta = -40 to+85°C		Unit	Test Conditions		
item			Min	Тур	Max	Min	Max	Oilit	rest conditions		
Input voltage	$V_{IH}$	4.5 to 5.5	2.0	_	_	2.0	_	V			
	$V_{IL}$	4.5 to 5.5	_	_	0.8	_	0.8	V			
Output voltage	$V_{OH}$	4.5	4.4	_	_	4.4	_	V	$Vin = V_{IH} \text{ or } V_{IL}$	$I_{OH} = -20 \mu A$	
		4.5	4.18	_	_	4.13	_			$I_{OH} = -6 \text{ mA}$	
	$V_{OL}$	4.5	_	_	0.1	_	0.1	V	$Vin = V_{IH} \text{ or } V_{IL}$	$I_{OL} = 20 \mu A$	
		4.5	_	_	0.26	_	0.33			$I_{OL} = 6 \text{ mA}$	
Off-state output	l <sub>OZ</sub>	5.5	_	_	±0.5	_	±5.0	μΑ	Vin = V <sub>IH</sub> or V <sub>IL</sub> ,		
current									Vout = $V_{CC}$ or GND		
Input current	lin	5.5	_	_	±0.1	_	±1.0	μΑ	Vin = V <sub>CC</sub> or GND		
Quiescent current	Icc	5.5		_	4.0	_	40	μΑ	Vin = $V_{CC}$ or GND, lout = 0 $\mu$ A		

### **Switching Characteristics**

$$(C_L = 50 \text{ pF, Input } t_r = t_f = 6 \text{ ns)}$$

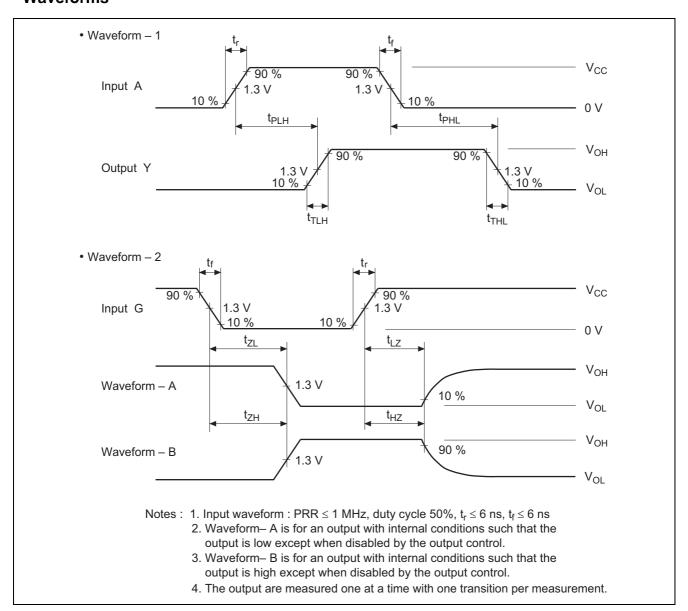
Item	Symbol	V <sub>cc</sub> (V)	Ta = 25°C			Ta = -40	to +85°C	Unit	Test Conditions
			Min	Тур	Max	Min	Max	Offic	rest conditions
Propagation delay time	t <sub>PLH</sub>	4.5	_	11	22	_	28	ns	
	t <sub>PHL</sub>	4.5	_	13	22	_	28		
Output enable time	$t_{ZL}$	4.5	_	17	30	_	38	ns	
	t <sub>zH</sub>	4.5	_	14	30	_	38		
Output disable time	$t_{LZ}$	4.5	_	20	30	_	38	ns	
	t <sub>HZ</sub>	4.5	_	22	30	_	38		
Output rise/fall time	t <sub>TLH</sub>	4.5	_	4	12	_	15	ns	
	$t_{THL}$								
Input capacitance	Cin	_		5	10	_	10	pF	

### **Test Circuit**

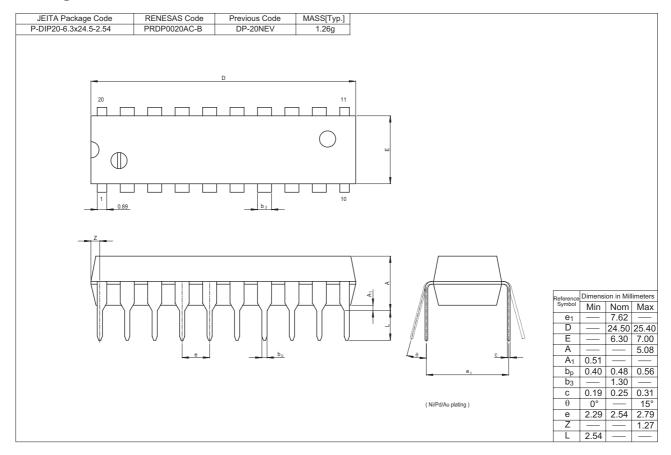


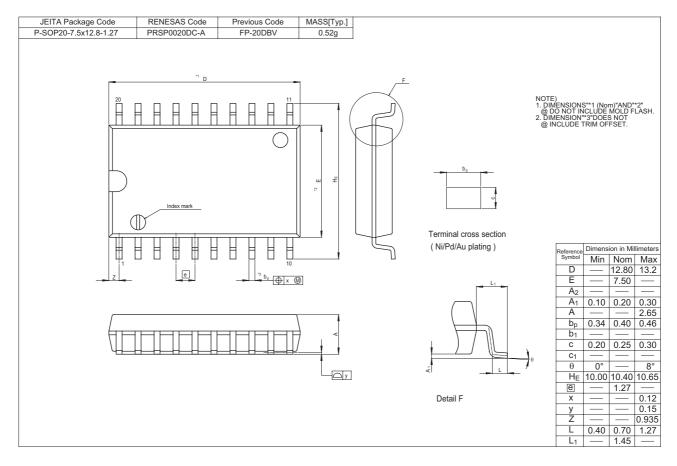
3. S1 is a input / output swich.

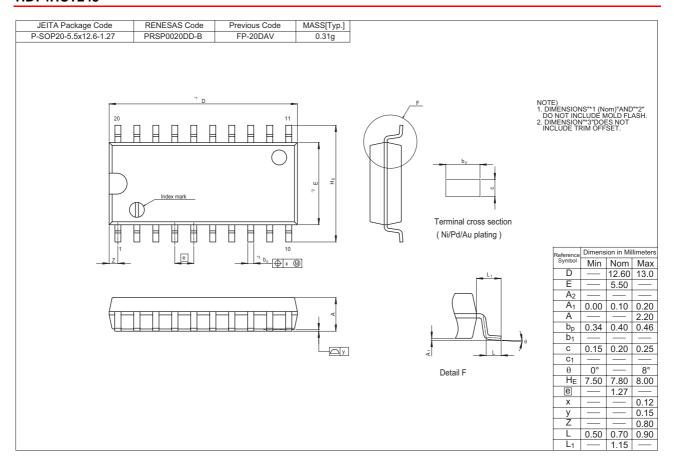
### **Waveforms**

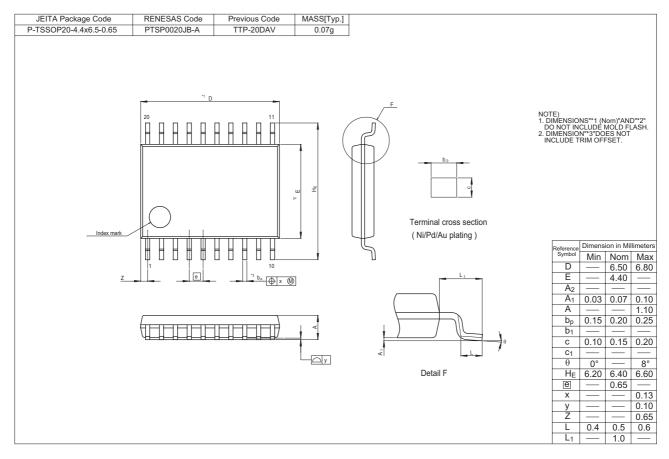


### **Package Dimensions**









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