

HD74LS27 Triple 3-input Positive NOR Gates

REJ03D0403-0200 Rev.2.00 Feb.18.2005

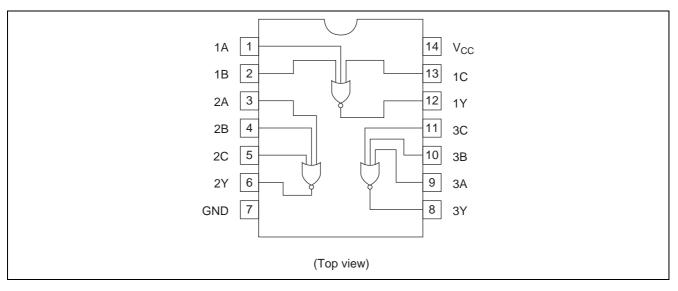
Features

• Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LS27P	DILP-14 pin	PRDP0014AB-B (DP-14AV)	Р	—
HD74LS27FPEL	SOP-14 pin (JEITA)	PRSP0014DF-B (FP-14DAV)	FP	EL (2,000 pcs/reel)

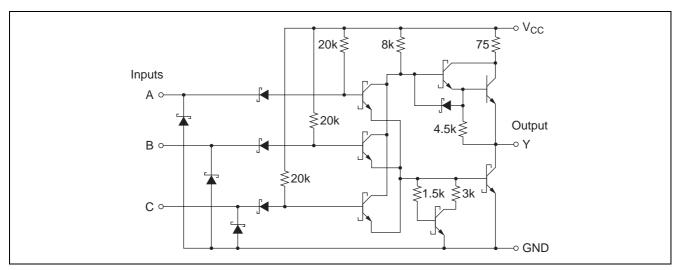
Note: Please consult the sales office for the above package availability.

Pin Arrangement





Circuit Schematic (1/3)



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit
Supply voltage	V _{CC}	7	V
Input voltage	V _{IN}	7	V
Power dissipation	PT	400	mW
Storage temperature	Tstg	-65 to +150	°C

Note: Voltage value, unless otherwise noted, are with respect to network ground terminal.

Recommended Operating Conditions

ltem	Symbol	Min	Тур	Max	Unit
Supply voltage	Vcc	4.75	5.00	5.25	V
Output current	I _{OH}	—	—	-400	μΑ
Oupur current	I _{OL}	—	—	8	mA
Operating temperature	Topr	-20	25	75	°C



Electrical Characteristics

 $(Ta = -20 \text{ to } +75 \text{ }^{\circ}\text{C})$

Item	Symbol	min.	typ.*	max.	Unit	Condition
Input voltage	VIH	2.0	—	—	V	
Input voltage	VIL	—	—	0.8	V	
Output voltage	V _{OH}	2.7	_	_	V	V_{CC} = 4.75 V, V_{IL} = 0.8 V, I_{OH} = –400 μA
	V _{OL}		—	0.4	V	$I_{OL} = 4 \text{ mA}$ $V_{CC} = 4.75 \text{ V}, \text{ V}_{IH} = 2 \text{ V}$
		_	_	0.5		$I_{OL} = 8 \text{ mA}$ $V_{CC} = 4.75 \text{ V}, \text{ V}_{IH} = 2 \text{ V}$
	I _{IH}		—	20	μΑ	$V_{CC} = 5.25 \text{ V}, \text{ V}_{I} = 2.7 \text{ V}$
Input current	I _{IL}		—	-0.4	mA	$V_{CC} = 5.25 \text{ V}, \text{ V}_{I} = 0.4 \text{ V}$
	I _I		—	0.1	mA	$V_{CC} = 5.25 \text{ V}, \text{ V}_{I} = 7 \text{ V}$
Short-circuit output current	los	-20	_	-100	mA	V _{CC} = 5.25 V
Supply current	I _{ССН}	—	2.0	4.0	mA	V _{CC} = 5.25 V
	I _{CCL}		3.4	6.8	mA	V _{CC} = 5.25 V
Input clamp voltage	VIK		_	-1.5	V	$V_{CC} = 4.75 \text{ V}, I_{IN} = -18 \text{ mA}$

Note: $* V_{CC} = 5 V$, Ta = $25^{\circ}C$

Switching Characteristics

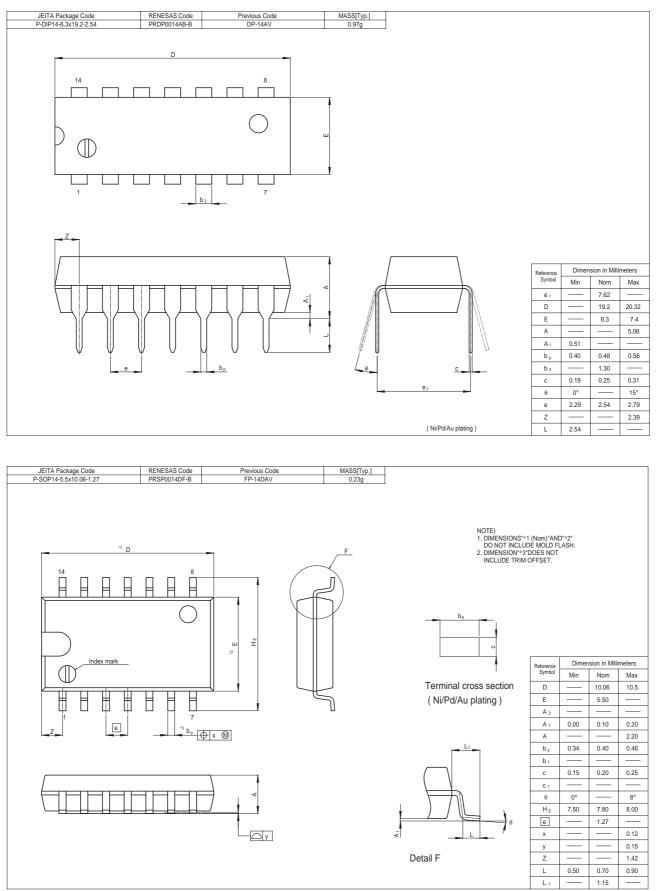
 $(V_{CC} = 5 V, Ta = 25^{\circ}C)$

Item	Symbol	min.	typ.	max.	Unit	Condition
Propagation delay time	t _{PLH}	—	10	15	ns	$C_{L} = 15 \text{ pF}, R_{L} = 2 \text{ k}\Omega$
	t _{PHL}	—	10	15	ns	$C_{L} = 13 \mu F, R_{L} = 2 R_{22}$

Note: Refer to Test Circuit and Waveform of the Common Item "TTL Common Matter (Document No.: REJ27D0005-0100)".



Package Dimensions





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