# RENESAS HD74ALVC2G08

**Dual 2-input AND Gates** 

REJ03D0164–0400Z (Previous ADE-205-612C (Z)) Rev.4.00 Dec.17.2003

### Description

The HD74ALVC2G08 has two-input AND gate in an 8 pin package. Low voltage and high-speed operation is suitable for the battery powered products (e.g., notebook computers), and the low power consumption extends the battery life.

### Features

- The basic gate function is lined up as Renesas uni logic series.
- Supplied on emboss taping for high-speed automatic mounting.
- Supply voltage range : 1.2 to 3.6 V Operating temperature range: -40 to +85°C
- All inputs V<sub>IH</sub> (Max.) = 3.6 V (@V<sub>CC</sub> = 0 V to 3.6 V) All outputs V<sub>0</sub> (Max.) = 3.6 V (@V<sub>CC</sub> = 0 V)
- Output current  $\pm 2 \text{ mA} (@V_{CC} = 1.2 \text{ V})$

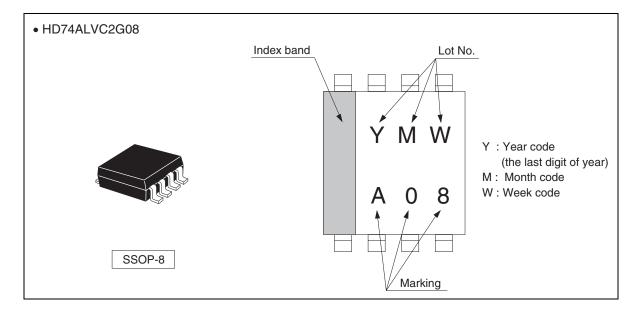
 $\pm 4 \text{ mA} (@V_{CC} = 1.4 \text{ V to } 1.6 \text{ V})$   $\pm 6 \text{ mA} (@V_{CC} = 1.65 \text{ V to } 1.95 \text{ V})$   $\pm 18 \text{ mA} (@V_{CC} = 2.3 \text{ V to } 2.7 \text{ V})$  $\pm 24 \text{ mA} (@V_{CC} = 3.0 \text{ V to } 3.6 \text{ V})$ 

Ordering Information

Part Name	Package Type	Package Code	Package Abbreviation	Taping Abbreviation (Quantity)	
HD74ALVC2G08USE	SSOP-8 pin	TTP-8DBV	US	E (3,000 pcs/reel)	



#### **Outline and Article Indication**



### **Function Table**

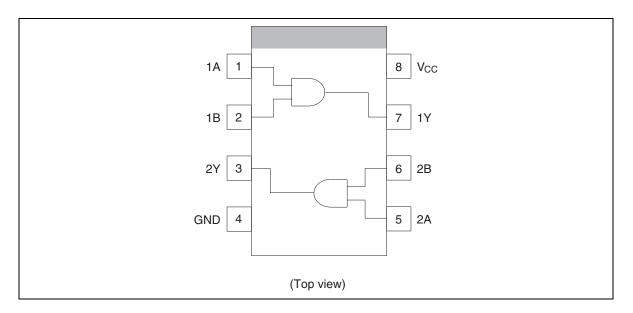
Input	ts			
Α		В	Output Y	
L		L	L	
Н		L	L	
L		Н	L	
Н		Н	н	
H:	High level			

High level

L: Low level



#### **Pin Arrangement**



### **Absolute Maximum Ratings**

Item	Symbol	Ratings	Unit	Conditions
Supply voltage range	V <sub>CC</sub>	-0.5 to 4.6	V	
Input voltage range *1	VI	-0.5 to 4.6	V	
Output voltage range *1, 2	Vo	–0.5 to V <sub>CC</sub> +0.5	V	Output : H or L
		-0.5 to 4.6		V <sub>CC</sub> : OFF
Input clamp current	I <sub>IK</sub>	-50	mA	V <sub>1</sub> < 0
Output clamp current	Ι <sub>ΟΚ</sub>	±50	mA	$V_{\rm O}$ < 0 or $V_{\rm O}$ > $V_{\rm CC}$
Continuous output current	lo	±50	mA	$V_{O} = 0$ to $V_{CC}$
Continuous current through V <sub>CC</sub> or GND	$I_{CC}$ or $I_{GND}$	±100	mA	
Maximum power dissipation at Ta = 25°C (in still air) $^{*3}$	PT	200	mW	
Storage temperature	Tstg	-65 to 150	°C	

Notes: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.

2. This value is limited to 4.6 V maximum.

3. The maximum package power dissipation was calculated using a junction temperature of 150°C.



Item	Symbol	Min	Max	Unit	Conditions
Supply voltage range	V <sub>CC</sub>	1.2	3.6	V	
Input voltage range	VI	0	3.6	V	
Output voltage range	Vo	0	V <sub>CC</sub>	V	
Output current	I <sub>OH</sub>		-2	mA	V <sub>CC</sub> = 1.2 V
			-4		V <sub>CC</sub> = 1.4 V
			-6		V <sub>CC</sub> = 1.65 V
			-18		V <sub>CC</sub> = 2.3 V
			-24		V <sub>CC</sub> = 3.0 V
	I <sub>OL</sub>		2		V <sub>CC</sub> = 1.2 V
			4		V <sub>CC</sub> = 1.4 V
			6		V <sub>CC</sub> = 1.65 V
			18		V <sub>CC</sub> = 2.3 V
			24		V <sub>CC</sub> = 3.0 V
Input transition rise or fall rate	$\Delta t$ / $\Delta v$	0	20	ns / V	$V_{CC}$ = 1.2 to 2.7 V
		0	10		V <sub>CC</sub> = 3.3±0.3 V
Operating free-air temperature	Та	-40	85	°C	

### **Recommended Operating Conditions**

Note: Unused or floating inputs must be held high or low.



#### **Electrical Characteristics**

#### $(Ta = -40 \text{ to } 85^{\circ}C)$

Item	Symbol	$V_{cc}$ (V) $^{*}$	Min	Тур	Max	Unit	Test conditions
Input voltage	VIH	1.2	V <sub>CC</sub> ×0.75	_	_	V	
		1.4 to 1.6	V <sub>CC</sub> ×0.7	_	_	_	
		1.65 to 1.95	V <sub>CC</sub> ×0.7	_	_	-	
		2.3 to 2.7	1.7	_	_	_	
		3.0 to 3.6	2.0	_	_	_	
	V <sub>IL</sub>	1.2	_	_	V <sub>CC</sub> ×0.25	-	
		1.4 to 1.6	_	_	V <sub>CC</sub> ×0.3	_	
		1.65 to 1.95	_	_	V <sub>CC</sub> ×0.3	_	
		2.3 to 2.7		_	0.7	-	
		3.0 to 3.6	_	_	0.8	_	
Output voltage	V <sub>OH</sub>	Min to Max	V <sub>CC</sub> -0.2	_	_	V	I <sub>OH</sub> = -100 μA
		1.2	0.9	_	_	-	$I_{OH} = -2 \text{ mA}$
		1.4	1.1	_	_	_	$I_{OH} = -4 \text{ mA}$
		1.65	1.2	_	_	_	$I_{OH} = -6 \text{ mA}$
		2.3	1.7	_	_	-	I <sub>OH</sub> = -18 mA
		3.0	2.2	_	_	-	I <sub>OH</sub> = -24 mA
	V <sub>OL</sub>	Min to Max		_	0.2	-	I <sub>OL</sub> = 100 μA
		1.2	_	_	0.3	-	$I_{OL} = 2 \text{ mA}$
		1.4		_	0.3	-	$I_{OL} = 4 \text{ mA}$
		1.65		_	0.3	-	$I_{OL} = 6 \text{ mA}$
		2.3		_	0.55	-	I <sub>OL</sub> = 18 mA
		3.0		_	0.55	-	I <sub>OL</sub> = 24 mA
Input current	I <sub>IN</sub>	3.6		_	±5	μΑ	$V_{IN}$ = 3.6 V or GND
Quiescent supply current	I <sub>CC</sub>	3.6	—	—	10	μA	$\label{eq:VIN} \begin{split} V_{\text{IN}} &= V_{\text{CC}} \text{ or } \text{GND}, \\ I_{\text{O}} &= 0 \end{split}$
Output leakage current	I <sub>OFF</sub>	0	—	—	5	μA	$V_{IN}$ or $V_O =$ 0 to 3.6 V
Input capacitance	CIN	3.3	_	4.5	_	pF	$V_{IN} = V_{CC} \text{ or } GND$

Note: For conditions shown as Min or Max, use the appropriate values under recommended operating conditions.



### **Switching Characteristics**

 $(Ta = -40 \text{ to } 85^{\circ}\text{C})$ 

 $V_{CC} = 1.2 V$ 

Item	Symbol	Min	Тур	Max	Unit	Test conditions	FROM (Input)	TO (Output)
Propagation delay time	t <sub>PLH</sub> t <sub>PHL</sub>		7.5	—	ns	C <sub>L</sub> = 15 pF	A or B	Y

 $V_{CC} = 1.5 \pm 0.1 \text{ V}$ 

Item	Symbol	Min	Тур	Max	Unit	Test conditions	FROM (Input)	TO (Output)
Propagation delay time	t <sub>PLH</sub> t <sub>PHL</sub>	2.0	—	7.0	ns	C <sub>L</sub> = 15 pF	A or B	Y

 $V_{CC} = 1.8 \pm 0.15 \text{ V}$ 

Item	Symbol	Min	Тур	Max	Unit	Test conditions	FROM (Input)	TO (Output)
Propagation delay time	t <sub>PLH</sub> t <sub>PHL</sub>	1.5	—	5.0	ns	C <sub>L</sub> = 30 pF	A or B	Y

 $V_{CC} = 2.5 \pm 0.2 \text{ V}$ 

Item	Symbol	Min	Тур	Max	Unit	Test conditions	FROM (Input)	TO (Output)
Propagation delay time	t <sub>PLH</sub> t <sub>PHL</sub>	1.0	_	3.7	ns	C <sub>L</sub> = 30 pF	A or B	Y

 $V_{CC} = 3.3 \pm 0.3 V$ 

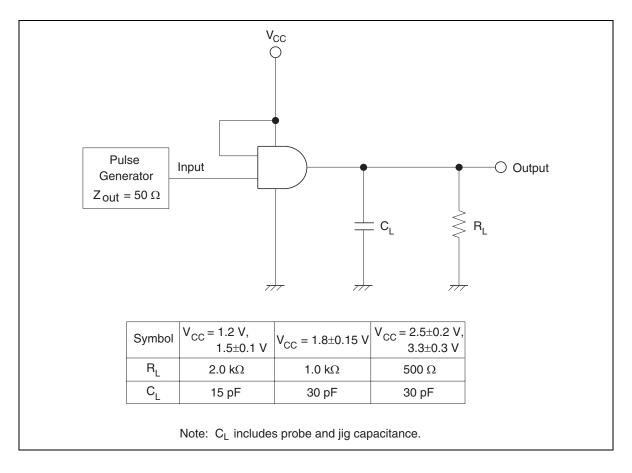
Item	Symbol	Min	Тур	Max	Unit	Test conditions	FROM (Input)	TO (Output)
Propagation delay time	t <sub>PLH</sub> t <sub>PHL</sub>	1.0		2.8	ns	C <sub>L</sub> = 30 pF	A or B	Y

### **Operating Characteristics**

#### $(Ta = 25^{\circ}C)$

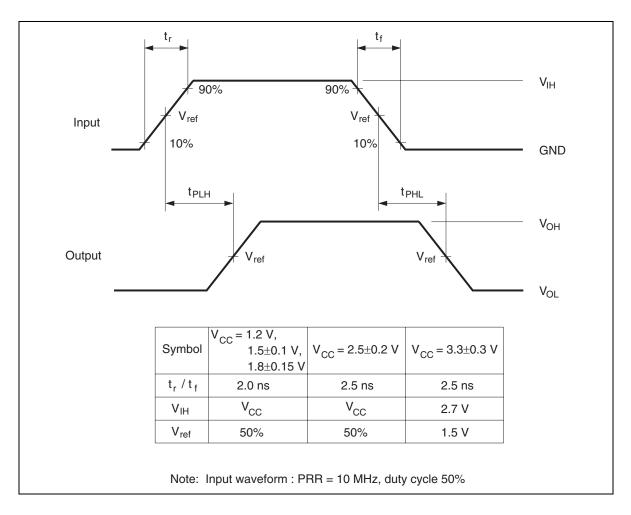
Item	Symbol	V <sub>cc</sub> (V)	Min	Тур	Max	Unit	Test conditions
Power dissipation	CPD	1.5	_	10.5	—	pF	f = 10 MHz
capacitance		1.8	_	10.5	_		
		2.5	_	10.5	_		
		3.3		11.5			

#### **Test Circuit**





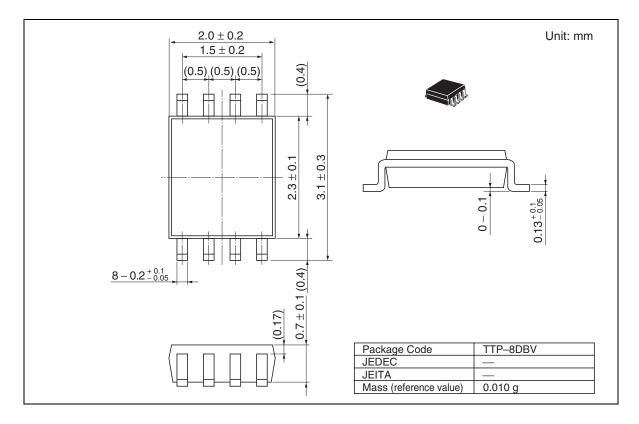
#### Waveforms



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### **Package Dimensions**





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