

# HD74HC01

Quad. 2-input NAND Gates (with open drain outputs)

REJ03D0532-0200 (Previous ADE-205-404) Rev.2.00 Oct 06, 2005

#### Features

- High Speed Operation:  $t_{pd} = 9$  ns typ ( $C_L = 50 \text{ pF}$ )
- High Output Current: Fanout of 10 LSTTL Loads
- Wide Operating Voltage:  $V_{CC} = 2 \text{ to } 6 \text{ V}$
- Low Input Current: 1 µA max
- Low Quiescent Supply Current:  $I_{CC}$  (static) = 1  $\mu$ A max (Ta = 25°C)
- Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74HC01P	DILP-14 pin	PRDP0014AB-B (DP-14AV)	Ρ	_
HD74HC01FPEL	SOP-14 pin (JEITA)	PRSP0014DF-B (FP-14DAV)	FP	EL (2,000 pcs/reel)
HD74HC01RPEL	SOP-14 pin (JEDEC)	PRSP0014DE-A (FP-14DNV)	RP	EL (2,500 pcs/reel)

Note: Please consult the sales office for the above package availability.

### **Function Table**

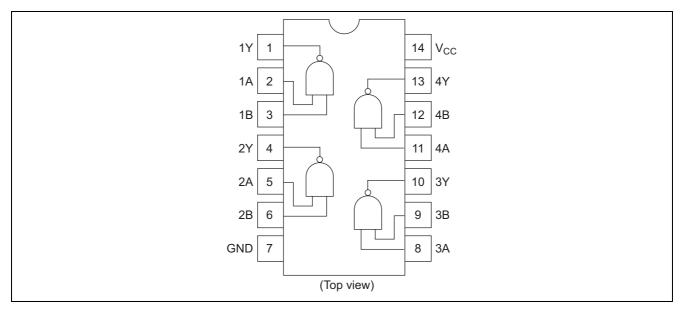
Inp	Output	
A	Y	
L	L	н
L	Н	Н
Н	L	н
Н	Н	L

H: High level

L: Low level



### **Pin Arrangement**



## **Absolute Maximum Ratings**

Item	Symbol	Ratings	Unit
Supply voltage range	V <sub>CC</sub>	-0.5 to 7.0	V
Input / Output voltage	Vin, Vout	–0.5 to V <sub>CC</sub> +0.5	V
Input / Output diode current	I <sub>IK</sub> , I <sub>OK</sub>	±20	mA
Output current	lo	±25	mA
V <sub>CC</sub> , GND current	I <sub>CC</sub> or I <sub>GND</sub>	±50	mA
Power dissipation	PT	500	mW
Storage temperature	Tstg	-65 to +150	°C

Note: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

### **Recommended Operating Conditions**

Item	Symbol	Ratings	Unit	Conditions	
Supply voltage	Vcc	2 to 6	V		
Input / Output voltage	Vin, Vout	0 to V <sub>CC</sub>	V		
Operating temperature	Та	-40 to 85	°C		
		0 to 1000		V <sub>CC</sub> = 2.0 V	
Input rise / fall time <sup>*1</sup>	t <sub>r</sub> , t <sub>f</sub>	0 to 500	ns	V <sub>CC</sub> = 4.5 V	
		0 to 400		$V_{CC} = 6.0 V$	

Note: 1. This item guarantees maximum limit when one input switches. Waveform: Refer to test circuit of switching characteristics.



			Т	a = 25°	С	Ta = -40	to+85°C		
Item	Symbol	V <sub>cc</sub> (V)	Min	Тур	Max	Min	Max	Unit	Test Conditions
Input voltage	VIH	2.0	1.5	_	—	1.5	_	V	
		4.5	3.15	_	—	3.15	_		
		6.0	4.2	_	—	4.2	_		
	VIL	2.0	_	_	0.5	—	0.5	V	
		4.5	_	_	1.35	—	1.35		
		6.0	_	_	1.8	—	1.8		
Output voltage	V <sub>OL</sub>	2.0	_	0.0	0.1	—	0.1	V	$Vin = V_{IH} \text{ or } V_{IL}   I_{OL} = 20 \ \mu A$
		4.5	_	0.0	0.1	—	0.1		
		6.0	_	0.0	0.1	—	0.1		
		4.5	_	_	0.26	—	0.33		$I_{OL} = 4 \text{ mA}$
		6.0	_	_	0.26	—	0.33		I <sub>OL</sub> = 5.2 mA
Off-state output	lo(off)	6.0	_	_	±0.5	—	±5.0	μA	$Vin = V_{IH} \text{ or } V_{IL},$
current									Vout = $V_{CC}$ or GND
Input current	lin	6.0			±0.1	_	±1.0	μΑ	$Vin = V_{CC} \text{ or } GND$
Quiescent supply current	Icc	6.0		—	1.0		10	μA	Vin = $V_{CC}$ or GND, lout = 0 $\mu$ A

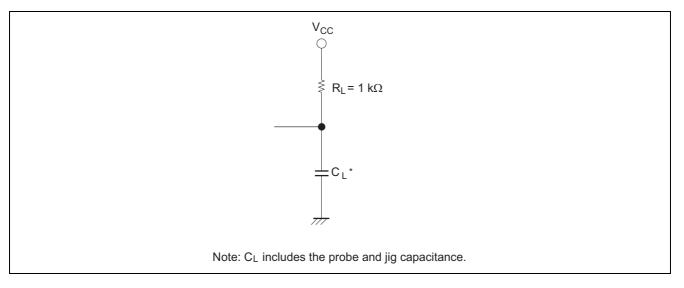
# **Electrical Characteristics**

# Switching Characteristics ( $C_L = 50 \text{ pF}$ , Input $t_r = t_f = 6 \text{ ns}$ )

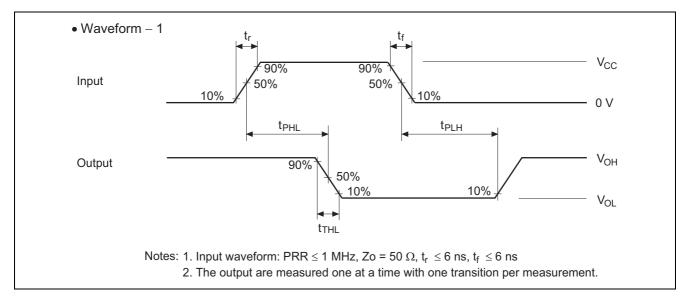
			Т	a = 25°	С	Ta = -40	to +85°C		
Item	Symbol	V <sub>cc</sub> (V)	Min	Тур	Max	Min	Max	Unit	Test Conditions
Propagation delay	t <sub>PLH</sub>	2.0	_	—	90	—	115	ns	
time		4.5		11	18	_	23		
		6.0			15	—	20		
	t <sub>PHL</sub>	2.0	_	_	90	—	112	ns	
		4.5	_	7	18	—	22		
		6.0	_	_	15	—	18		
Output fall time	t <sub>THL</sub>	2.0	_	_	75	—	95	ns	
		4.5	_	5	15	—	19		
		6.0			13	_	16		
Input capacitance	Cin	_		5	10	—	10	pF	



## **Test Circuit**

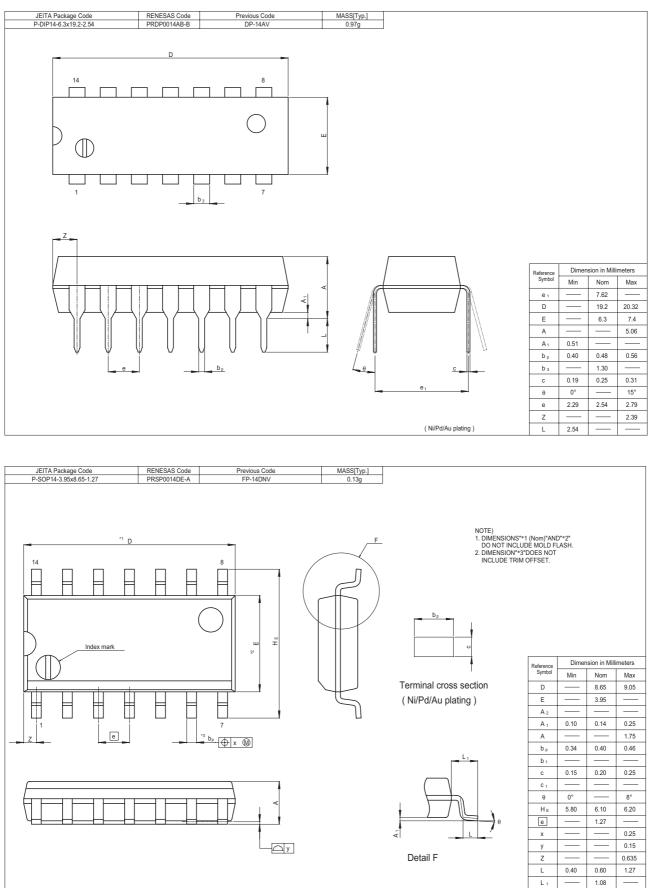


#### Waveforms



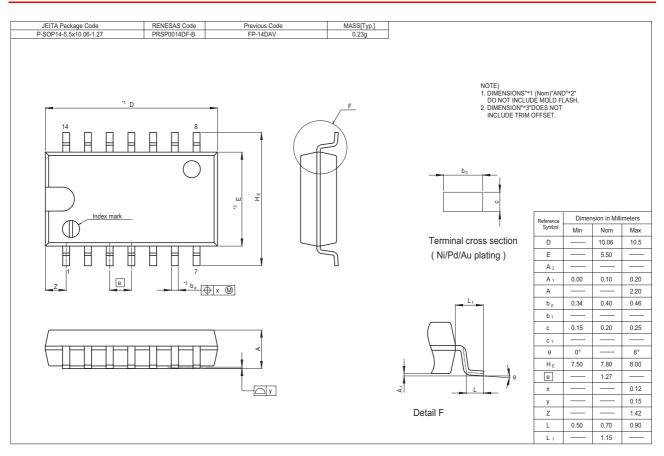


### **Package Dimensions**





#### HD74HC01





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