

HD74LS242

Quadruple Bus Transceivers (with three-state outputs)

REJ03D0461-0300 Rev.3.00 Jul.15.2005

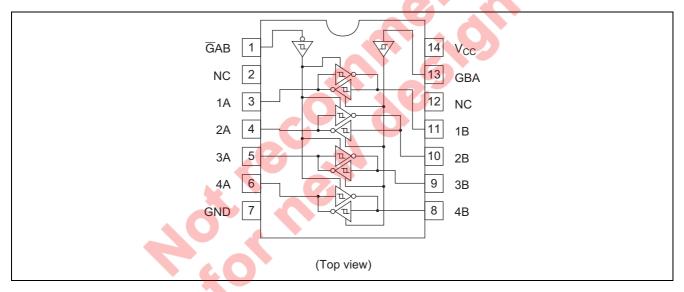
Features

• Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LS242P	DILP-14 pin	PRDP0014AB-B (DP-14AV)	Р	_
HD74LS242FPEL	SOP-14 pin (JEITA)	PRSP0014DF-B (FP-14DAV)	FP	EL (2,000 pcs/reel)

Note: Please consult the sales office for the above package availability.

Pin Arrangement



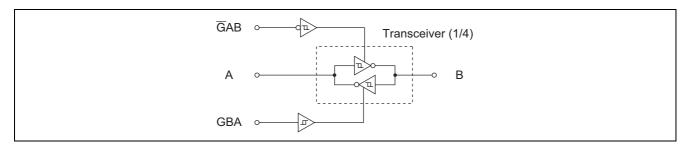
Function Table

Cont	rol input	Data port status		
GAB	GBA	A B		
Н	Н	Inverting output Input		
L	Н	,	*	
Н	L	Isolated	Isolated	
L	L	Input	Inverting output	

Notes: 1. H; high level, L; low level

2. *; Possibly destructive oscillation may occur if the transceivers are enabled in both directions at once.

Block Diagram



Absolute Maximum Ratings

Item		Symbol	Ratings	Unit
Supply voltage		V _{CC}	7	V
Input voltage	GAB, GBA	V _{IN}	7	V
	A, B	V _{IN}	5.5	V
Power dissipation		P _T	400	mW
Storage temperature		Tstg	-65 to +150	°C

Note: Voltage value, unless otherwise noted, are with respect to network ground terminal.

Recommended Operating Conditions

Item	Symbol	Min	Тур	Max	Unit
Supply voltage	V_{CC}	4.75	5.00	5.25	V
Output current	I _{OH}			– 15	mA
Output current	I _{OL}			24	mA
Operating temperature	Topr	-20	25	75	°C

Electrical Characteristics

 $(Ta = -20 \text{ to } +75 \text{ }^{\circ}\text{C})$

Item		Symbol	min.	typ.*	max.	Unit	С	ondition
Input voltage		V_{IH}	2.0		_	V		
input voit	age	V_{IL}	_	_	0.8	V		
Hysteresi	S	$V_T^+ - V_T^-$	0.2	0.4	_	V	V _{CC} = 4.75 V	
		V_{OH}	2.4	_	_	V	$V_{IL} = 0.8 \text{ V}, I_{OH} = -$	-3 mA $V_{CC} = 4.75 \text{ V},$
Output vo	oltago	VOH	2		_	V	$V_{IL} = 0.5 \text{ V}, I_{OH} = -$	- 15 mA V _{IH} = 2 V
Output vo	лауе	V _{OL}	1		0.4	V	I _{OL} = 12 mA	$V_{CC} = 4.75 \text{ V}, V_{IH} = 2 \text{ V},$
		V OL	1		0.5	V	I _{OL} = 24 mA	$V_{IL} = 0.8 V$
Off state	output current	l _{ozh}	1		40	^	$V_0 = 2.7 \text{ V}$	$V_{CC} = 5.25 \text{ V}, V_{IH} = 2 \text{ V},$
OII-State	output current	I _{OZL}	1		-200	μΑ	$V_0 = 0.4 \text{ V}$	$V_{IL} = 0.8 V$
		I _{IH}		_	20	μΑ	$V_{CC} = 5.25 \text{ V}, V_I = 2.7 \text{ V}$	
	A Input	I _{IL}		_	-0.2		$V_{CC} = 5.25 \text{ V}, V_I = 0.4 \text{ V},$	
	A Input						GAB or GBA at GND	
Input	B Input		_	_	-0.2	mA	$V_{CC} = 5.25 \text{ V}, V_{I} =$	
current					_		GAB or GBA at 4.5	
	GAB or GBA			_	-0.2		$V_{CC} = 5.25 \text{ V}, \text{ V}_{I} =$	0.4 V
	A or B	I _I		_	0.1	mA	$V_{CC} = 5.25 \text{ V}, \text{ V}_{I} =$	5.5 V
	GAB or GBA	''	_	_	0.1	117 ($V_{CC} = 5.25 \text{ V}, \text{ V}_{I} =$	7 V
Short-circuit output		los	-40		-225	mA	V _{CC} = 5.25 V	
current		105	70		-220	HIA	VCC = 3.23 V	
Supply current**		Іссн	_	22	38			
		I _{CCL}	_	29	50	mA	$V_{CC} = 5.25 \text{ V}$	
		Iccz		29	50			
Input clamp voltage		V _{IK}	_	_	-1.5	V	$V_{CC} = 4.75 \text{ V}, I_{IN} =$	–18 mA

Notes: $\overline{^* V_{CC} = 5 \text{ V, Ta} = 25^{\circ}\text{C}}$

Switching Characteristics

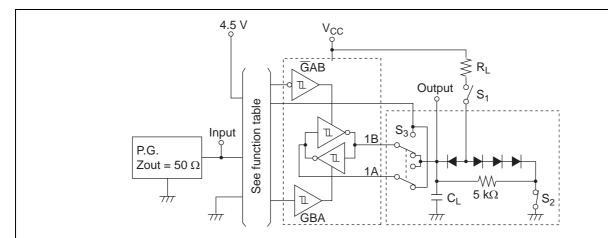
 $(V_{CC} = 5 \text{ V}, \text{Ta} = 25^{\circ}\text{C})$

Item	Symbol	min.	typ.	max.	Unit	Condition
Propagation delay time	t _{PLH}		9	14	ns	$C_L = 45 \text{ pF}, R_L = 667 \Omega$
Tropagation delay time	t _{PHL}	_	12	18		
Output anable time	t _{ZL}		20	30		
Output enable time	t _{zH}	_	15	23		
Output disable time	t_{LZ}	_	15	25		$C_L = 5 \text{ pF}, R_L = 667 \Omega$
Output disable tillle	t _{HZ}	_	10	18		Οι – 3 βι , Ιλι – 667 22

^{**} With all outputs open, I_{CC} is measured with transceivers enabled in one direction only, or with all transceivers disabled

Testing Method

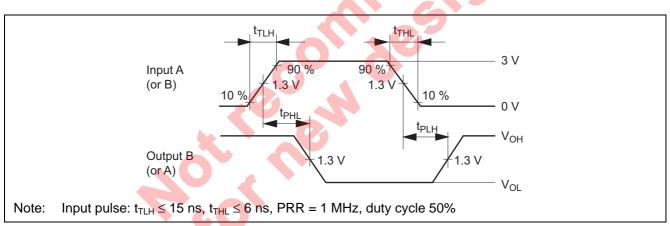
Test Circuit



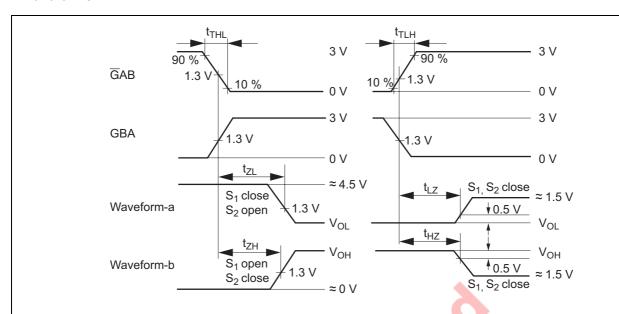
Notes:

- 1. 2A-2B, 3A-3B, 4A-4B are identical to abobe load circuit.
- 2. C_L includes probe and jig capacitance.
- 3. S_3 is a input-output switch.
- 4. All diodes are 1S2074(H).

Waveforms 1



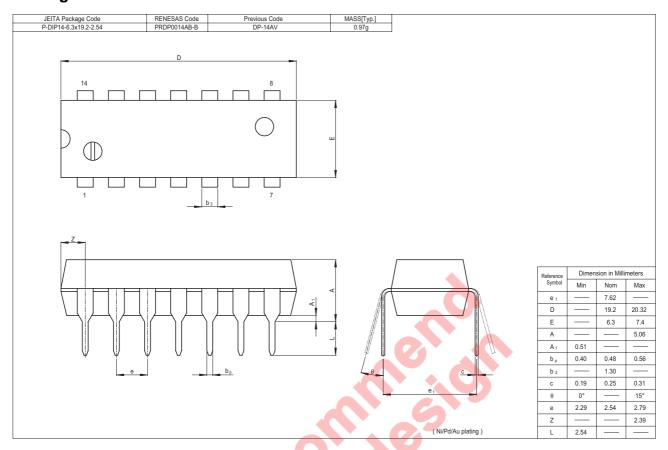
Waveforms 2

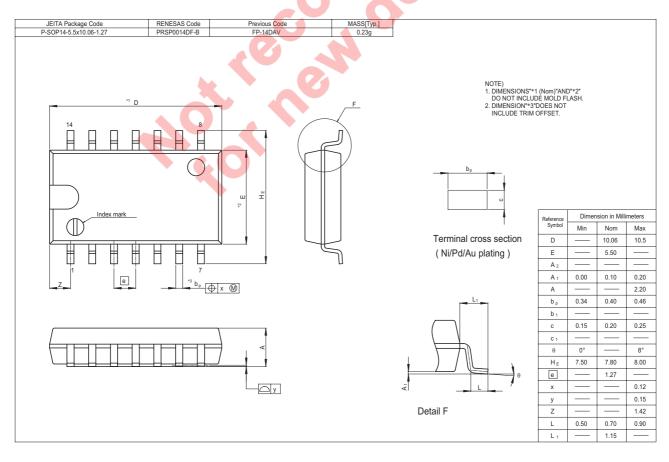


Notes:

- 1. Input pulse: $t_{TLH} \le 15$ ns, $t_{THL} \le 6$ ns, PRR = 1 MHz, duty cycle 50%
- 2. Waveform a is an output by internal conditions like "L" except for the case where an output is disabled by output control.
- 3. Waveform b is an output by internal conditions like "H" except for the case where an output is disabled by output control.

Package Dimensions





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