

# HVD397C

# Variable Capacitance Diode for VCO

REJ03G0022-0200 Rev.2.00 Mar 30, 2006

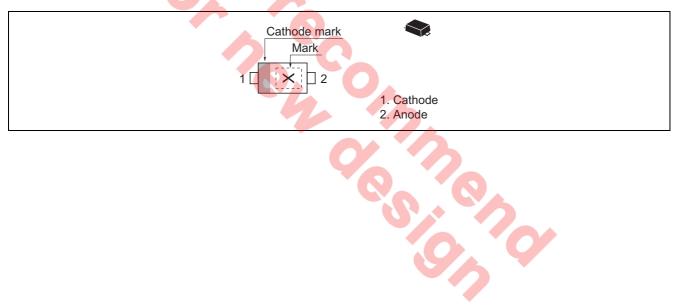
#### **Features**

- High capacitance ratio. (n = 2.9 min)
- Good C-V linearity
- Super small Flat Lead Package (SFP) is suitable for surface mount design.

#### **Ordering Information**

Type No.	/11	Laser Mark	Package Name	Package Code
HVD397C		X	SFP	PUSF0002ZB-A

## **Pin Arrangement**



## **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Value	Unit
Reverse voltage	V <sub>R</sub>	15	V
Junction temperature	Tj	125	°C
Storage temperature	Tstg	-55 to +125	°C

#### **Electrical Characteristics**

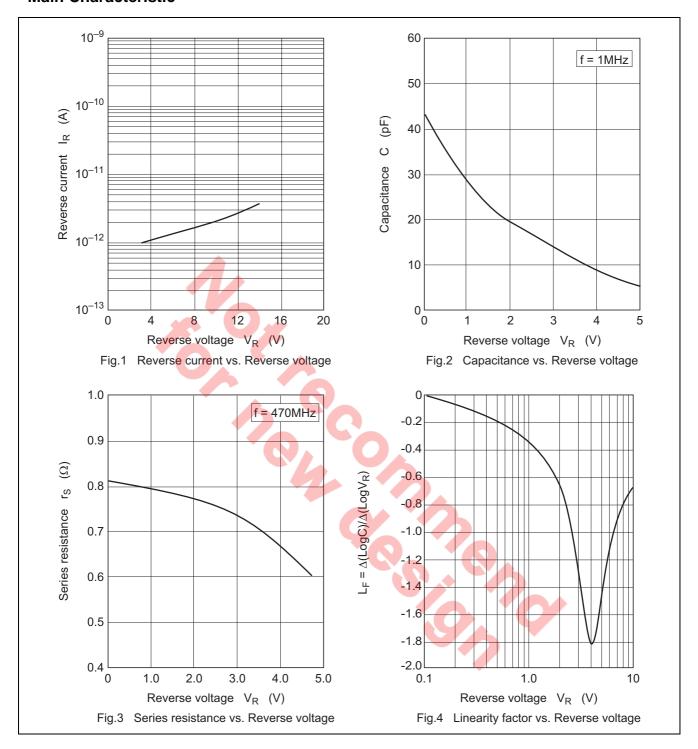
 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse current	I <sub>R1</sub>	_	_	10	nA	V <sub>R</sub> = 10 V
	I <sub>R2</sub>	_	_	50		V <sub>R</sub> = 10 V, Ta = 60°C
Capacitance	C <sub>1</sub>	27.0	_	28.5	pF	V <sub>R</sub> = 1 V, f = 1 MHz
	C <sub>2</sub>	18.0	_	20.0		$V_R = 2 V, f = 1 MHz$
	C <sub>4</sub>	6.80	_	8.50		$V_R = 4 \text{ V}, f = 1 \text{ MHz}$
Capacitance ratio	n <sub>1</sub>	1.3	_	_	_	C <sub>1</sub> / C <sub>2</sub>
	n <sub>2</sub>	2.9	_	_	_	C <sub>1</sub> / C <sub>4</sub>
Series resistance	rs		_	1.2	Ω	V <sub>R</sub> = 1 V, f = 470 MHz

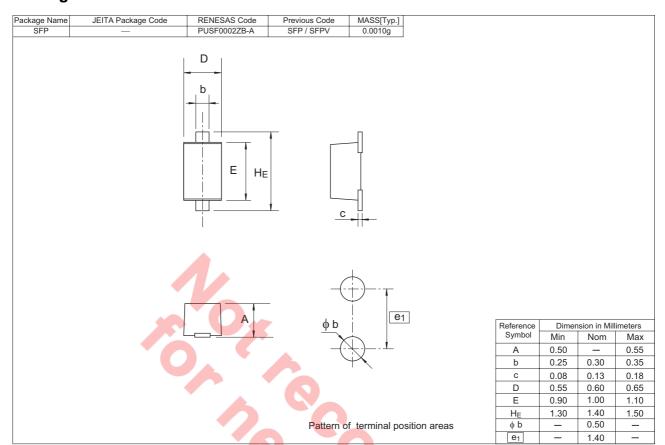
Note: For SFP package, the material of lead is exposed for cutting plane. There for, soldering nature of lead tip part is considered as unquestioned. Please kindly consider soldering nature.



#### **Main Characteristic**



## **Package Dimensions**



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