

TOSHIBA

Integrated single chip solutions for
Set Top Box and Digital TV

Donau Family

TC90400XBG/FG



Description

The TC90400XBG/FG family offers powerful and highly integrated single chip solutions for Set Top Boxes, Integrated Digital TVs, and combo systems with hard disks and DVDs. The family has several members to provide the most cost-effective system solution for both Free-to-Air applications and for Pay TV. A powerful Graphics Engine provides optimum support for interactive applications such as Multimedia Home Platform (MHP) or MHEG-5. The extensive use of Toshiba's extensible MeP* (Media embedded Processor) architecture frees the host CPU from compute-intensive tasks by delivering the processing power needed in such demanding applications.

The TC90400XBG/FG family supports a unified memory system as well as NAND and/or NOR Flash to drastically reduce system cost.

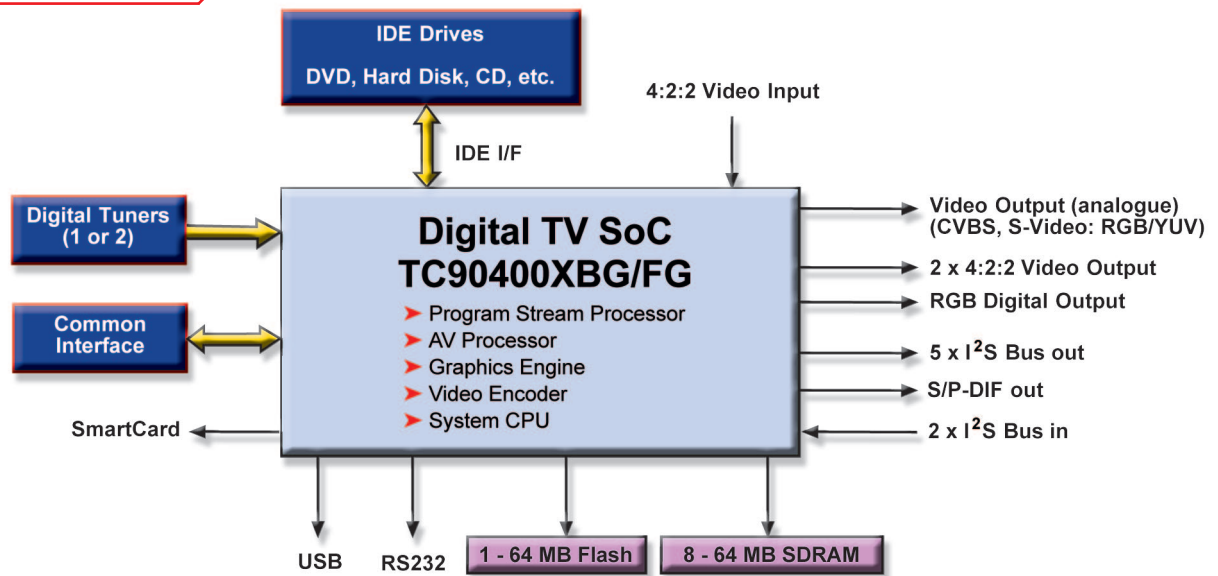


Applications

Based on the flexible family concept, TC90400XBG/FG devices offer a cost-optimised system solution for all types of digital TV applications. These solutions can range from a very basic, single decoding, standard definition 'Zapping-Box', to high-end MHP Integrated Digital TV combos with dual-stream decoding, personal digital recording, and DVD Player functionality.

In addition, the TC90400XBG/FG family supports 1/2 High-Definition decoding and HD-to-SD down-conversion to offer low-cost systems for US and Japanese markets.

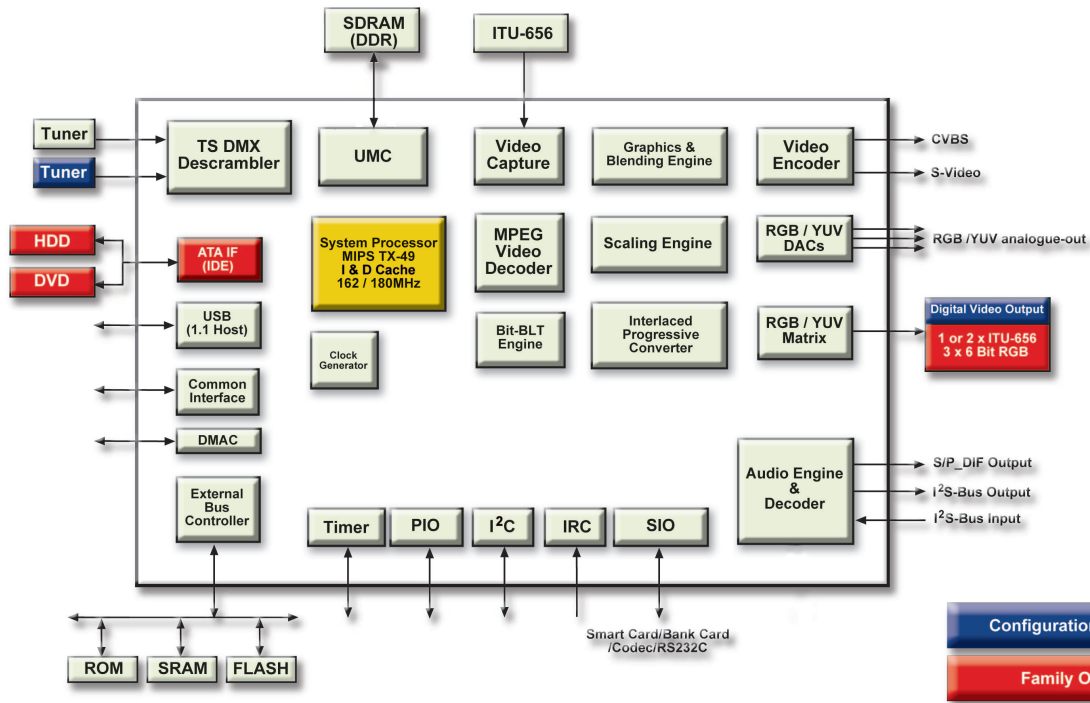
System Diagram



TC90400XBG/FG Features & Differences

Part Number	Package	External Bus	3 x 6 Bit RGB OUT	ITU-601/656 input	ITU-601/656 output	Concurrent support of ITU-601/656 input & output	IDE I/F
TC90400XBG	272PBGA	16 or 8 Bit	Yes	Yes	Yes	Yes	Yes
TC90400FG	208QFP	8 Bit	No	Yes	Yes	No	Yes

TC90400XBG/FG Block Diagram



Toshiba Offices in Europe

GERMANY
TOSHIBA ELECTRONICS EUROPE GMBH
CENTRAL EUROPEAN SALES
 Düsseldorf
 Hansaallee 181
 40549 Düsseldorf
 Tel.: +49-211-5 29 60
 Fax.: +49-211-5 29 64 00

Munich
 Hofmannstr. 52
 81378 Munich
 Tel.: +49-89-7 48 59 50
 Fax.: +49-89-74 85 95 42

ITALY
TOSHIBA ELECTRONICS ITALIANA S.R.L.
 Milan
 Centro Direzionale Colleoni
 Palazzo Perseo Ingresso 3
 20041 Agrate Brianza
 Tel.: +39-39-6 87 01
 Fax.: +39-39-6 87 02 05

UK
TOSHIBA ELECTRONICS (UK) LTD
 Camberley
 Riverside Way
 Camberley
 Surrey
 GU15 3YA
 Tel.: +44-1276-69 46 00
 Fax.: +44-1276-69 48 00

FRANCE
TOSHIBA ELECTRONICS FRANCE S.A.R.L.
 Paris
 Les Jardins du Golf
 6 rue de Rome
 93561 Rosny-Sous-Bois, Cédex
 Tel.: +33-1-48 12 48 12
 Fax.: +33-1-48 94 51 15

SPAIN
TOSHIBA ELECTRONICS ESPAÑA S.A.
 Madrid
 Parque Empresarial
 San Fernando
 28831 Madrid
 Tel.: +34-1-6 60 67 98
 Fax.: +34-1-6 60 67 99

SWEDEN
TOSHIBA ELECTRONICS SCANDINAVIA AB
 Bromma
 Gustavslundsvägen 18
 S-161 15 Bromma
 Tel.: +46-8-7 04 09 00
 Fax.: +46-8-80 84 59

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