

MC74HC and MC74HCT: High Speed and High Speed-TTL Compatible CMOS Logic Integrated Circuits

| Mfr.'s Type | | | | Description | No. of Leads |
|--------------|---------------|--------------|---------------|--|--------------|
| MC74HC | | MC74HCT | | | |
| PDIP | SOIC | PDIP | SOIC | | |
| MC74HC00AN | MC74HC00AD | — | — | Quad 2-Input NAND Gate | 14 |
| MC74HC02AN | MC74HC02AD | — | — | Quad 2-Input NOR Gate | 14 |
| MC74HC03AN | MC74HC03AD | — | — | Quad 2-Input NAND Gate With Open Drain Outputs | 14 |
| MC74HC04AN | MC74HC04AD | — | — | Hex Inverter | 14 |
| MC74HC08AN | MC74HC08AD | MC74HCT04AN | MC74HCT04AD | Quad 2-Input AND Gate | 14 |
| MC74HC125AN | MC74HC125AD | — | — | Quad 3-State Non-Inverting Buffer, Active Low Enable | 14 |
| MC74HC126AN | — | — | — | Quad 3-State Non-Inverting Buffer, Active High Enable | 14 |
| MC74HC132AN | MC74HC132AD | — | — | Quad 2-Input NAND Gate With Schmitt Trigger Inputs | 14 |
| MC74HC138AN | MC74HC138AD | MC74HCT138AN | — | 1 Of 8 Decoder/Demultiplexer | 16 |
| MC74HC139AN | — | — | — | Dual 1 Of 4 Decoder/Demultiplexer | 16 |
| MC74HC14AN | MC74HC14AD | MC74HCT14AN | MC74HCT14AD | Hex Schmitt Trigger Inverter | 14 |
| MC74HC157AN | MC74HC157AD | — | — | Quad 2-Input Data Selectors/Multiplexers | 16 |
| MC74HC161AN | MC74HC161AD | — | — | Asynchronous Presettable Binary Counter | 16 |
| MC74HC163AN | — | — | — | Synchronous Presettable Binary Counter | 16 |
| MC74HC174AN | — | — | — | Hex D Type Flip-Flop With Common Clock and Reset | 16 |
| MC74HC240AN | MC74HC240ADW | — | — | Octal 3-State Inverting Buffer/Line Driver | 20 |
| MC74HC244AN | MC74HC244ADW | MC74HCT244AN | MC74HCT244ADW | Octal 3-State Non-Inverting Buffer/Line Driver/Line Receiver | 20 |
| MC74HC245AN | MC74HC245ADW | MC74HCT245AN | MC74HCT245ADW | Octal 3-State Non-Inverting Bus Transceiver | 20 |
| MC74HC273AN | MC74HC273ADW | MC74HCT273AN | MC74HCT273ADW | Octal D Type Flip-Flop With Common Clock and Reset | 20 |
| MC74HC32AN | MC74HC32AD | — | — | Quad 2-Input OR Gate | 14 |
| MC74HC373AN | MC74HC373ADW | MC74HCT373AN | MC74HCT373ADW | Octal 3-State Non-Inverting Transparent Latch | 20 |
| MC74HC374AN | — | MC74HCT374AN | — | Octal 3-State Non-Inverting D Type Flip-Flop | 20 |
| MC74HC393AN | — | — | — | Dual 4-Stage Binary Ripple Counter | 14 |
| MC74HC4020AN | MC74HC4020AD | — | — | 14-Stage Binary Ripple Counter | 16 |
| MC74HC4040AN | — | — | — | 12-Stage Binary Ripple Counter | 16 |
| MC74HC4046AN | MC74HC4046AD | — | — | Phase Locked Loop | 16 |
| MC74HC4051AN | — | — | — | 8-Channel Single-Ended Break-before-Make Analog Multiplexer/Demultiplexer | 16 |
| MC74HC4052AN | MC74HC4052ADW | — | — | 4-Channel Differential Break-before-Make Analog Multiplexer/Demultiplexer | 16 |
| MC74HC4053AN | MC74HC4053ADW | — | — | Triple 2-Channel Single-Ended Break-before-Make Analog Multiplexer/Demultiplexer | 16 |
| MC74HC4060AN | — | — | — | 14-Stage Binary Ripple Counter With Oscillator | 16 |
| MC74HC4066N | MC74HC4066D | — | — | Quad Analog Switch/Multiplexer/Demultiplexer | 14 |
| MC74HC4316N | MC74HC4316D | — | — | Quad Analog Switch/Multiplexer/Demultiplexer With Separate Analog and Digital Power Supplies | 16 |
| MC74HC4538AN | MC74HC4538AD | — | — | Dual Precision Monostable Multivibrator, Retriggerable/Resetable | 16 |
| MC74HC540AN | MC74HC540ADW | — | — | Octal 3-State Inverting Buffer/Line Driver/Line Receiver | 20 |
| MC74HC541AN | MC74HC541ADW | — | MC74HCT541ADW | Octal 3-State Non-Inverting Buffer/Line Driver/Line Receiver | 20 |
| MC74HC573AN | MC74HC573ADW | MC74HCT573AN | MC74HCT573ADW | Octal 3-State Non-Inverting Transparent Latch | 20 |
| MC74HC574AN | MC74HC574ADW | MC74HCT574AN | MC74HCT574ADW | Octal 3-State Non-Inverting D Type Flip-Flop | 20 |
| MC74HC595AN | — | — | — | 8-Bit Serial-Input/Serial Or Parallel-Output Shift Register With Latched 3-State Outputs | 16 |
| MC74HC74AN | MC74HC74AD | — | — | Dual D Type Flip-Flop with Set and Reset | 14 |

SN74LS: Low Power Schottky TTL Logic Integrated Circuits

| Mfr.'s Type | | Description | No. of Leads | Mfr.'s Type | | Description | No. of Leads |
|-------------|-------------|--|--------------|-------------|-------------|--|--------------|
| PDIP | SOIC | | | PDIP | SOIC | | |
| SN74LS00N | SN74LS00D | Quad 2-Input NAND Gate | 14 | SN74LS245N | SN74LS245DW | Octal Bus Transceiver | 20 |
| SN74LS04N | SN74LS04D | Hex Inverter | 14 | SN74LS247N | — | BCD To Seven Segment Decoder/Driver With Open Collector Outputs | 16 |
| SN74LS05N | — | Hex Inverter With Open Collector Outputs | 14 | SN74LS251N | — | 8-Input Multiplexer With 3-State Outputs | 16 |
| SN74LS08N | SN74LS08D | Quad 2-Input AND Gate | 14 | SN74LS253N | — | Dual 4-Input Multiplexer With 3-State Outputs | 16 |
| SN74LS109AN | — | Dual JK Positive Edge-Triggered Flip-Flop | 16 | SN74LS259N | — | Quad 2-Input Multiplexer With 3-State Outputs, Non-Inverting | 16 |
| SN74LS122N | — | Retriggerable Monostable Multivibrator, One Shot | 14 | SN74LS260N | — | 8-Bit Addressable Latch | 16 |
| SN74LS123N | SN74LS123D | Dual Retriggerable Monostable Multivibrator, One Shot | 16 | SN74LS263N | — | Dual 5-Input NOR Gate | 14 |
| SN74LS125AN | — | Quad 3-State Buffer, Enable Low | 14 | SN74LS273N | — | Octal D Type Flip-Flop With Clear | 20 |
| SN74LS126AN | — | Quad 3-State Buffer, Enable High | 14 | SN74LS299N | — | 8-Bit Shift/Storage Register With 3-State Outputs | 20 |
| SN74LS132N | SN74LS132D | Quad 2-Input Schmitt Trigger NAND Gate | 14 | SN74LS32N | SN74LS32D | Quad 2-Input OR Gate | 14 |
| SN74LS138N | SN74LS138D | 1 Of 8 Decoder/Demultiplexer | 16 | SN74LS365AN | — | Hex 3-State Buffer With Common 2-Input NOR Enable | 16 |
| SN74LS139N | — | Dual 1 Of 4 Decoder/Demultiplexer | 16 | SN74LS367AN | — | Hex 3-State Buffer With Separate 2-Bit and 4-Bit Sections | 16 |
| SN74LS145N | — | 1 of 10 Decoder/Driver With Open Collector Outputs | 16 | SN74LS368AN | — | Hex 3-State Inverter Buffer With Separate 2-Bit and 4-Bit Sections | 16 |
| SN74LS148N | — | 8-Line To 3-Line Priority Encoder | 16 | SN74LS373N | SN74LS373DW | Octal Transparent Latch With 3-State Outputs | 20 |
| SN74LS14N | SN74LS14D | Hex Schmitt Trigger Inverter | 14 | SN74LS374N | SN74LS374DW | Octal D Type Flip-Flop With 3-State Outputs | 20 |
| SN74LS151N | — | 8-Input Multiplexer | 16 | SN74LS377N | — | Octal D Type Flip-Flop With Enable | 20 |
| SN74LS153N | — | Dual 4-Input Multiplexer | 16 | SN74LS38N | — | Quad 2-Input NAND Buffer With Open Collector Outputs | 14 |
| SN74LS156N | — | Dual 1 Of 4 Decoder/Demultiplexer With Open Collector Outputs | 16 | SN74LS393N | — | Dual 4-Stage Binary Ripple Counter | 14 |
| SN74LS157N | — | Quad 2-Input Multiplexer | 16 | SN74LS42N | — | 1 Of 10 Decoder | 16 |
| SN74LS161AN | — | Synchronous Presettable 4-Bit Binary Up Only Counter, Asynchronous Reset | 16 | SN74LS47N | — | BCD To Seven Segment Decoder/Driver | 16 |
| SN74LS163AN | — | Synchronous Presettable 4-Bit Binary Up Only Counter, Synchronous Reset | 16 | SN74LS541N | — | Octal Buffer/Line Driver With 3-State Outputs | 20 |
| SN74LS164AN | SN74LS164D | Serial-In Parallel-Out Shift Register | 14 | SN74LS640N | — | Octal Inverting Bus Transceiver With 3-State Outputs | 20 |
| SN74LS165N | — | 8-Bit Parallel-To-Serial Shift Register | 16 | SN74LS641N | — | Octal Bus Transceiver With Open Collector Outputs | 20 |
| SN74LS166N | — | 8-Bit Shift Register | 16 | SN74LS642N | — | Octal Inverting Bus Transceiver With Open Collector Outputs | 20 |
| SN74LS174N | — | Hex D Type Flip-Flop | 16 | SN74LS645N | — | Octal Bus Transceiver With 3-State Outputs | 20 |
| SN74LS175N | — | Quad D Type Flip-Flop | 16 | SN74LS682N | — | 8-Bit Magnitude Comparator | 20 |
| SN74LS193N | — | Dual Synchronous Presettable 4-Bit Binary Up/Down Counter | 16 | SN74LS684N | — | 8-Bit Magnitude Comparator | 20 |
| SN74LS221N | — | Dual Monostable Multivibrators With Schmitt Trigger Inputs | 16 | SN74LS688N | — | 8-Bit Magnitude Comparator | 20 |
| SN74LS240N | — | Dual 4-Bit Inverting Buffer/Driver, Enable Low, 3-State Outputs | 20 | SN74LS74AN | SN74LS74AD | Dual D Type Positive Edge Triggered Flip-Flop | 14 |
| SN74LS244N | SN74LS244DW | Dual 4-Bit Buffer/Driver, Enable Low, 3-State Outputs | 14 | SN74LS76AN | — | Dual JK Negative Edge Triggered Flip-Flop With Set and Clear | 16 |
| | | | | SN74LS85N | — | 4-Bit Magnitude Comparator | 16 |
| | | | | SN74LS86N | SN74LS86D | Quad 2-Input Exclusive OR Gate | 14 |

MC10EL and MC100EL: ECLinPS Lite 10 K and 100 K Emitter-Coupled Logic in Picoseconds Integrated Circuits

| Mfr.'s Type | | Description | No. of Leads | Mfr.'s Type | | Description | No. of Leads |
|-------------|------------|--------------------------------|--------------|-------------|------------|------------------------------------|--------------|
| MC10E | MC100E | | | MC10E | MC100E | | |
| — | MC100EL11D | 1:2 Differential Fanout Buffer | 8 | MC10EL32D | MC100EL32D | Differential Divide-By-2 Prescaler | 8 |
| — | MC100EL12D | Low Impedance Driver | 8 | MC10EL33D | MC100EL33D | Differential Divide-By-4 Prescaler | 8 |
| MC10EL16D | MC100EL16D | Differential Receiver | 8 | — | MC100EL51D | Differential Clock D Flip-Flop | 8 |
| MC10EL31D | MC100EL31D | D Flip-Flop With Set and Reset | 8 | — | — | — | — |