MC10ELT and MC100ELT: ECLinPS Lite Translators 10 K and 100 K Emitter-Coupled Logic in Picoseconds Integrated Circuits

Mfr.'s Type		Description	No. of Leads
MC10ELT	MC100ELT		
SOIC	SOIC		
—	MC100ELT21D	Differential PECL To TTL Translator	8
MC10ELT22D	MC100ELT22D	Dual TTL To Differential PECL Translator	8

Mfr.'s Type		Description	No. of Leads
MC10ELT	MC100ELT		
SOIC	SOIC		
—	MC100ELT23D	Dual Differential PECL-to-TTL Translator	8
MC10ELT25D	MC100ELT25D	Dual Differential ECL-to-TTL Translator	8

MC10H: MECL 10H Monolithic Emitter-Coupled Logic Integrated Circuits

Mfr.'s Type		Description	No. of Leads
PDIP	PLCC		
MC10H101P	—	Quad OR/NOR Gate	16/20
MC10H115P	MC10H115FN	Quad Line Receiver	16/20
MC10H116P	—	Triple Line Receiver	16/20
MC10H124P	MC10H124FN	Quad TTL-to-MECL Translator with TTL Strobe Input	16/20

Mfr.'s Type		Description	No. of Leads
PDIP	PLCC		
MC10H125P	—	Quad MECL-to-TTL Translator	16/20
MC10H131P	—	Dual Type D Master-Slave Flip-Flop	16/20
—	MC10H351FN	Quad TTL/NMOS To PECL Translator	20/20
—	—	—	—

MC10: MECL 10 K Monolithic Emitter-Coupled Logic Integrated Circuits

Mfr.'s Type	Description	No. of Leads
PDIP		
MC10102P	MECL Quad 2-Input NOR Gate	16/20
MC10103P	MECL Quad 2-Input OR Gate	16
MC10104P	MECL Quad 2-Input AND Gate	16
MC10107P	MECL Triple 2-Input Exclusive OR/Exclusive NOR	16
MC10113P	MECL Quad Exclusive OR Gate	16

Mfr.'s Type	Description	No. of Leads
PDIP		
MC10116P	MECL Triple Line Receiver	16/20
MC10124P	MECL Quad TTL To MECL Translator	16/20
MC10125P	MECL Quad MECL To TTL Translator	16/20
MC10131P	MECL Dual Type D Master-Slave Flip-Flop	16/20
MC10216P	MECL High Speed Triple Line Receiver	16/20

Line Receivers — EIA Standard

Mfr.'s Type		Single Ended or Differential	Type of Output	tprop Delay Time Max. (ns)	Party Line Operation	Strobe or Enable	Power Supplies (V)	Receivers Per Package	Companion Drivers	Comments	No. of Leads
PDIP	SOIC										
MC1489AP	—	Single Ended	Resistor Pull-Up	85	—	—	+5.0	4	MC1488	EIA-232-D	14
MC1489P	MC1489D	Single Ended	Resistor Pull-Up	85	—	—	+5.0	4	MC1488	EIA-232-D	14

Line Drivers

EIA Standard

Mfr.'s Type		Output Current Capability (mA)	Single Ended or Differential	tprop Delay Time Max. (ns)	Party Line Operation	Strobe or Enable	Power Supplies (V)	Receivers Per Package	Companion Drivers	Comments	No. of Leads
PDIP	SOIC										
MC1488P	MC1488D	10	Single Ended	350	—	—	±9.0 to ±12	4	MC1489	EIA-232-D	14
—	MC26LS30D	60	Single Ended, Differential	300	—	422 Yes, 423 No	±5.0	2 (422), 4 (423)	AM26LS32	EIA-422, EIA-423 Switchable	16

Peripheral Drivers

Mfr.'s Type	Output Current Capability (mA)	Input Capability	Propagation Delay Time Max. (us)	Output Clamp Diode	Off State Voltage Max. (V)	Drivers Per Package	Logic Function	Package Type
MC1413P	500	TTL, +5 V CMOS	1	Yes	50	7	Invert	16 Lead PDIP

CMOS Display Drivers

Mfr.'s Type	Function	Display Type	Input Format	Drive Capability Per Package	On-Chip Latch	Display Control	Segment Drive Current	Package Type
MC14543BCP	BCD-to-7 Segment Latch/Decoder/Driver	LCD, Direct Drive	Parallel BCD	7 Segments	Yes	Blank	Approx. 1.0 mA	16 Lead PDIP
MC14511BCP	BCD-to-7 Segment Latch/Decoder/Driver	LED, Incandescent, Fluorescent*	Parallel BCD	7 Segments	Yes	Blank, Lamp Test	25 mA	16 Lead PDIP
MC14513BCP	BCD-to-7 Segment Latch/Decoder/Driver	LED, Incandescent, Fluorescent*	Parallel BCD	7 Segments	Yes	Blank, Ripple Blank, Lamp Test	25 mA	18 Lead PDIP

*Absolute maximum working voltage=18 V.

Balanced Modulator/Demodulator

Mfr.'s Type		V_{CC} (V)	I_{CC} (mA)	Carrier Suppression	Common Mode Rejection	Function	No. of Leads
PDIP	SOIC						
MC1496P	MC1496D	5 to 30	10	65 dB typ. @ 0.5 MHz 50 dB typ. @ 10.0 MHz	85 dB typ.	Carrier Balance >50 dB, General Purpose Balanced Modulator/Demodulator For AM, SSB and FM Detection.	14