TOSHIBA CMOS DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

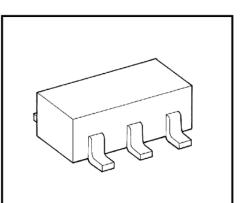
T C 4 S 5 8 4 F

SCHMITT TRIGGER

TC4S584F is the one circuit inverter having the schmitt trigger function at the input terminal. That is, since the circuit threshold level voltage at the leading and trailing edges of input waveform are different (Vp, V_N), the TC4S584F can be used in the broad range application, including line receiver, waveform shaping circuit, astable multivibrator, etc. In addition to ordinary inverter.



CHARACTERISTIC	SYMBOL	RATING	UNIT
DC Supply Voltage	V _{DD}	V _{SS} - 0.5~V _{SS} + 20	V
Input Voltage	VIN	$V_{SS} - 0.5 \sim V_{DD} + 0.5$	V
Output Voltage	Vout	V _{SS} – 0.5~V _{DD} + 0.5	V
DC Input Current	IIN	± 10	mA
Power Dissipation	PD	200	mW
Operating Temperature Range	T _{opr}	- 40~85	°C
Storage Temperature Range	T _{stg}	- 65~150	°C
Lead Temperature (10s)	тլ	260	°C

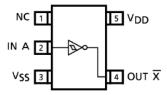


SSOP5-P-0.95

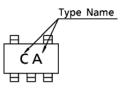
Weight : 0.016g (Typ.)

LOGIC DIAGRAM





MARKING



RECOMMENDED OPERATING CONDITIONS $(V_{SS} = 0V)$

CHARACTERISTIC	SYMBOL		MIN.	TYP.	MAX.	UNIT
DC Supply Voltage	V _{DD}	—	3	_	18	V
Input Voltage	VIN	—	0	_	V _{DD}	V

STATIC ELECTRICAL CHARACTERISTICS $(V_{SS} = 0V)$

CHARACTERISTIC		TEST CONDITION	V _{DD}	– 40°C		25°C			85	UNIT	
CHARACTERIST	BOL		(V)	MIN.	MAX.	MIN.	TYP.	MAX.	MIN.	MAX.	
High-Level		 lout <1μΑ	5	4.95	-	4.95	5.00	—	4.95	-	
Output Voltage	∣∨он	$V_{IN} = V_{SS}, V_{DD}$	10	9.95	—	9.95	10.00	_	9.95	—	
Output Voltage		VIN = VSS, VDD	15	14.95	—	14.95	15.00	—	14.95	—	v
Low-Level		lla	5	—	0.05	—	0.00	0.05	_	0.05	Ň
Output Voltage	VOL	lout <1μΑ V _{IN} = V _{SS} , V _{DD}	10	—	0.05	—	0.00	0.05	—	0.05	
Output Voltage		VIN = VSS, VDD	15	—	0.05	—	0.00	0.05	—	0.05	
		V _{OH} = 4.6V	5	- 0.61		- 0.51	- 1.0	—	- 0.42	-	
Output High		V _{OH} = 2.5V	5	- 2.5	—	- 2.1	- 4.0	—	– 1.7	—	
Current	∣юн	V _{OH} = 9.5V	10	- 1.5	—	– 1.3	- 2.2	—	- 1.1	—	
Current		V _{OH} = 13.5V	15	- 4.0	—	- 3.4	- 9.0	—	- 2.8	—	
		$V_{IN} = V_{SS}, V_{DD}$	1								
		V _{OL} = 0.4V	5	0.61	_	0.51	1.5	—	0.42	—	mA
Output Low		$V_{OL} = 0.5V$	10	1.5	—	1.3	3.8	_	1.1		
Current	^I OL	V _{OL} = 1.5V	15	4.0	—	3.4	15.0	_	2.8	—	
		$V_{IN} = V_{SS}, V_{DD}$	1								
Desitive Trieses		V _{OUT} = 0.5V	5	1.95	3.65	2.05	2.9	3.35	2.05	3.75	
Positive Trigger	VP	$V_{OUT} = 1.0V$	10	4.3	7.1	4.5	5.9	7.1	4.7	7.2	
Threshold Voltag	e*	$V_{OUT} = 1.5V$	15	6.9	10.7	7.1	9.0	10.6	7.1	10.8	
		V _{OUT} = 4.5V	5	1.05	2.75	1.1	2.1	2.6	0.95	2.65	
Negative Trigger	VN	V _{OUT} = 9.0V	10	2.1	4.9	2.2	3.5	4.7	2.0	4.8	v
Threshold Voltag	e*	V _{OUT} = 13.5V	15	3.2	7.0	3.3	5.0	6.8	3.1	6.9	
Hystersis Voltage* V _H		5	0.1	1.35	0.4	0.75	1.3	0.4	1.50		
	* VH	_	10	1.7	3.2	1.8	2.4	3.2	1.7	3.4	
			15	3.1	4.8	3.2	4.0	4.8	3.2	4.9	
Input H Leve	el IIH	V _{IH} = 18V	18	—	0.1	—	10-5		—	1.0	
Current L Leve	el I _{IL}	V _{IL} = 0V	18	—	- 0.1	—	- 10-5	- 0.1	—	- 1.0	μA
Quiescent Device Current			5	—	1	—	0.001	1	—	7.5	
	DD	$V_{IN} = V_{SS}, V_{DD}$	10	—	2	—	0.002	2	—	15	μΑ
Device Current			15		4	—	0.004	4		30	

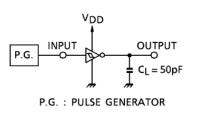
(Note) Values are different to TC4584BP, TC4584BF marked* (Vp, VN, VH).

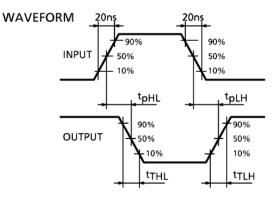
DYNAMIC ELECTRICAL CHARACTERISTICS (Ta = 25° C, V_{SS} = 0V, C_L = 50pF)

			—				
CHARACTERISTIC	SYMBOL	TEST CONDITION	V _{DD} (V)	MIN.	TYP.	MAX.	UNIT
Output Transition Time			5	-	80	200	
(Low to High)	ttlH	—	10	—	50	100	
			15	—	40	80	
Output Transition Time	ţтнг		5	_	80	200	ns
Output Transition Time (High to Low)		_	10	_	50	100	
			15	_	40	80	
	.		5	_	170	340	
Propagation Delay Time	t _{pLH} t _{pHL}	_	10	—	80	160	ns
			15	—	60	120	
Input Capacitance	CIN	_		—	5	7.5	рF

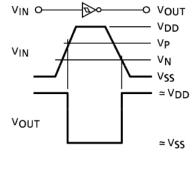
CIRCUIT AND WAVEFORM FOR MEASUREMENT OF DYNAMIC CHARACTERISTICS

CIRCUIT

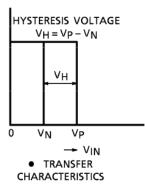




INPUT-OUTPUT VOLTAGE CHARACTERISTICS



• INPUT-OUTPUT VOLTAGE WAVEFORM

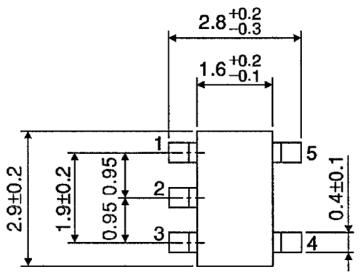


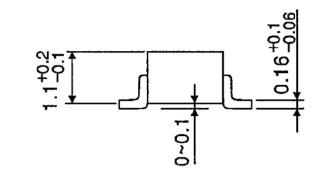
TOSHIBA

PACKAGE DIMENSIONS

SSOP5-P-0.95

Unit : mm





Weight : 0.016g (Typ.)

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