TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SZ04F,TC7SZ04FU

Inverter

Features

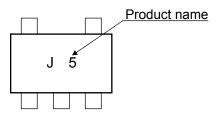
- High output current : ± 24 mA (min) at V_{CC} = 3 V
- Super high speed operation : t_{pd}=2.4 ns (typ.)

at V_{CC} = 5 V, C_L = 50 pF

: V_{CC} = 1.8 to 5.5 V

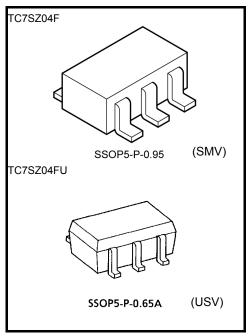
- Operation voltage range
- 5.5-V tolerant input
- 5.5-V power down protection output
- Matches the performance of TC74LCX series when operated at 3.3- V V_{CC}

Marking



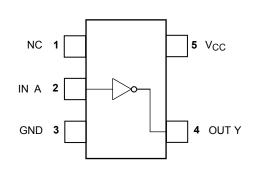
Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit	
Supply voltage	V _{CC}	–0.5 to 6	V	
DC input voltage	V _{IN}	–0.5 to 6	V	
DC output voltage	Vaum	-0.5 to 6 (Note 1)	V	
	Vout	-0.5 to V _{CC} + 0.5 (Note 2)	v	
Input diode current	I _{IK}	-20	mA	
Output diode current	I _{OK}	-20 (Note 3)	mA	
DC output current	IOUT	±50	mA	
DC V _{CC} /ground current	ICC	±50	mA	
Power dissipation	PD	200	mW	
Storage temperature	T _{stg}	–65 to 150	°C	
Lead temperature (10 s)	ΤL	260	°C	



Weight SSOP5-P-0.95 : 16.0 mg (typ.) SSOP5-P-0.65A: 6.0 mg (typ.)

Pin Assignment (top view)



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

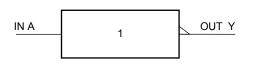
Note 1: $V_{CC} = 0V$

Note 2: High or Low state. Do not exceed I_{OUT} of absolute maximum ratings.

Note 3: V_{OUT} < GND

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IEC Logic Symbol



А	Y
L	Н
Н	L

Truth Table

Operating Ranges

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	1.8 to 5.5	V
		1.5 to 5.5 (Note 4)	v
Input voltage	V _{IN}	0 to 5.5	V
Output voltage	V _{OUT}	0 to 5.5 (Note 5)	V
		0 to V _{CC} (Note 6)	v
Operating temperature	T _{opr}	-40 to 85	°C
	dt/dv	0 to 20 (V_{CC} = 1.8 V, 2.5 V \pm 0.2 V)	
Input rise and fall time		0 to 10 (V_{CC} = 3.3 V \pm 0.3 V)	ns/V
		0 to 5 (V_{CC} = 5.0 V \pm 0.5 V)	

Note 4: Data retention only

Note 5: $V_{CC} = 0 V$

Note 6: High or Low state

Electrical Characteristics

DC Characteristics

Characteristics Symbol		ool Test Condition V _{CC}			Ta = 25°C			Ta = -40 to 85°C		Unit
				V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
High-level VIH VIH				1.8	V _{CC} × 0.88	_	_	V _{CC} × 0.88	_	v
			_	2.3 to 5.5	V _{CC} × 0.75	_	_	V _{CC} × 0.75	_	v
Low-level input voltage	M.	_		1.8		_	V _{CC} × 0.12	_	V _{CC} × 0.12	v
	VIL			2.3 to 5.5		_	V _{CC} × 0.25	_	V _{CC} × 0.25	
				1.8	1.7	1.8	_	1.7	_	
			I _{OH} = −100 μA	2.3	2.2	2.3	_	2.2	_	
				3.0	2.9	3.0	_	2.9	_	
High-level	Vari	VIN = VIL		4.5	4.4	4.5	_	4.4	_	V
output voltage	Vон		I _{OH} = -8 mA	2.3	1.9	2.15	—	1.9	_	
			$I_{OH} = -16 \text{ mA}$	3.0	2.4	2.8	_	2.4		
			$I_{OH} = -24 \text{ mA}$	3.0	2.3	2.68	—	2.3	_	
			$I_{OH} = -32 \text{ mA}$	4.5	3.8	4.2	—	3.8	_	
		V _{IN} = V _{IH}	I _{OL} = 100 μA	1.8		0	0.1		0.1	- V
				2.3		0	0.1		0.1	
				3.0		0	0.1		0.1	
Low-level	V _{OL}			4.5	_	0	0.1		0.1	
output voltage	VOL		$I_{OL} = 8 \text{ mA}$	2.3		0.1	0.3		0.3	
			I _{OL} = 16 mA	3.0	_	0.15	0.4	_	0.4	
			I _{OL} = 24 mA	3.0		0.22	0.55		0.55	
			I _{OL} = 32 mA	4.5	_	0.22	0.55		0.55	
Input leakage current	I _{IN}	$V_{IN} = 5.5 \text{ V or GND}$		0 to 5.5			±1	—	±10	μA
Power off leakage current	I _{OFF}	V_{IN} or $V_{OUT} = 5.5 V$		0.0		—	1	—	10	μA
Quiescent supply current	ICC	$V_{IN} = V_{CC}$	or GND	5.5	_	—	2		20	μA

AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition		Ta = 25°C		$Ta = -40$ to $85^{\circ}C$		Unit	
			V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
Propagation delay time	^t pLH tpHL	$C_L = 15 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	1.8	2.0	4.4	9.5	2.0	10.0	ns
			2.5 ± 0.2	0.8	2.9	6.5	0.8	7.0	
			3.3 ± 0.3	0.5	2.1	4.5	0.5	4.7	
			5.0 ± 0.5	0.5	1.8	3.9	0.5	4.1	
		$C_L = 50 \text{ pF},$ $R_L = 500 \Omega$	3.3 ± 0.3	1.5	2.9	5.0	1.5	5.2	
			5.0 ± 0.5	0.8	2.4	4.3	0.8	4.5	
Input capacitance	C _{IN}	—	0 to 5.5	_	4	_	_	_	pF
Power dissipation capacitance	C _{PD}	(Note 7)	3.3		20			_	рF
			5.5	_	26	_			

Note 7: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

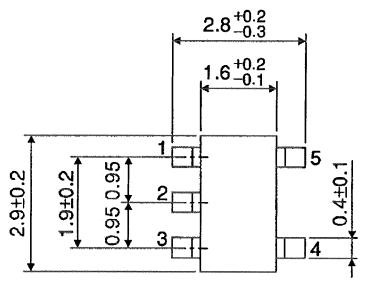
 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

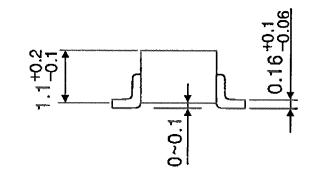
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Package Dimensions

SSOP5-P-0.95

Unit : mm



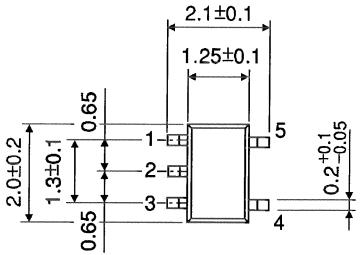


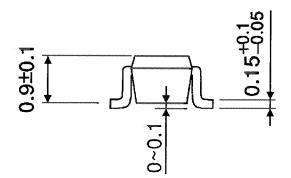
Weight: 16.0 mg (typ.)

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Package Dimensions

Unit : mm





Weight: 6.0 mg (typ.)

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