Unit: mm

TOSHIBA Diode Silicon Epitaxial Planar Type

1SS362

Ultra High Speed Switching Application

Small package

Low forward voltage $V_{F(3)} = 0.97 \text{ V (typ.)}$ Fast reverse recovery time: $t_{rr} = 1.6$ ns (typ.) Small total capacitance $: C_T = 0.5 \text{ pF (typ.)}$

Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit	
Maximum (peak) reverse voltage	V_{RM}	85	V	
Reverse voltage	V _R	80	V	
Maximum (peak) forward current	I _{FM}	240 *	mA	
Average forward current	Io	80 *	mA	
Surge current (10ms)	I _{FSM}	1 *	Α	
Power dissipation	Р	100	mW	
Junction temperature	Tj	125	°C	
Storage temperature	T _{stg}	-55 to 125	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating

Weight: 2.4 mg (typ.)

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temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Unit rating. Total rating = unit rating × 0.7

Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit	
Forward voltage	V _{F (1)}	_	I _F = 1mA	1	0.63	_	V	
	V _{F (2)}	_	I _F = 10mA	-	0.75	_		
	V _{F (3)}	_	I _F = 100mA	_	0.97	1.20		
Reverse current	I _{R (1)}	_	V _R = 30V	_	_	0.1	_	
	I _{R (2)}	_	V _R = 80V	_	_	0.5	μA	
Total capacitance	C _T	_	V _R = 0, f = 1MHz	_	0.5	3.0	pF	
Reverse recovery time	t _{rr}	_	I _F = 10mA, Fig.1	_	1.6	4.0	ns	

0.8±0.1 ANODE 1 CATHODE 2 ANODE 2 /CATHODE 1 SSM **JEDEC**

1-2S1C

Marking



2 2009-06-24

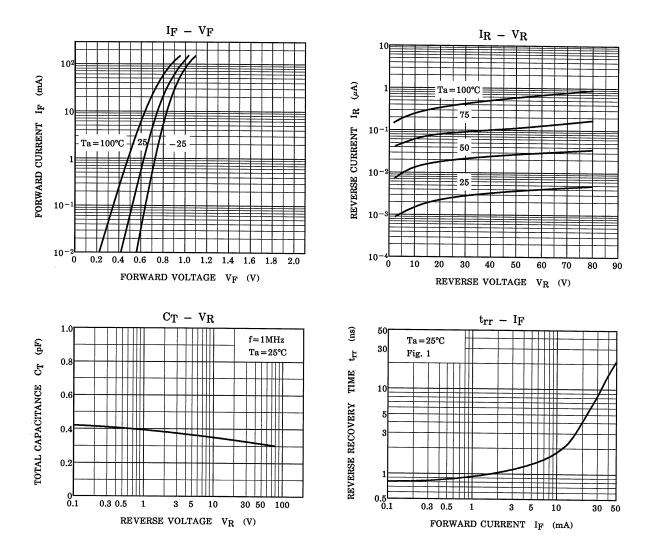
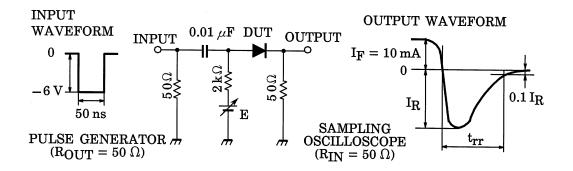


Fig.1 Reverse Recovery Time (trr) Test Circuit



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