TOSHIBA PHOTOCOUPLER PHOTO RELAY

TLP3118

Measurement Instruments

The TOSHIBA TLP3118 mini-flat photorelay is a small-outline photorelay, suitable for surface-mount assembly. The TLP3118 consists of a GaAs infrared-emitting diode optically coupled to a photo-MOSFET and is housed in a 4-pin package.

Features

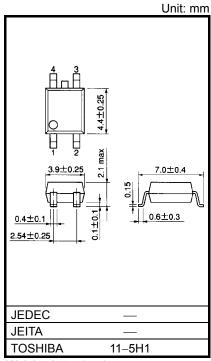
4-pin SOP (2.54SOP4): 2.1-mm high, 2.54-mm pitch

1-Form-A

• Peak Off-State Voltage: 80 V (min) • Trigger LED Current: 3 mA (max) • On-State Current: 40 mA (max)

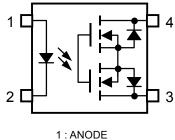
• On-State Resistance: 25Ω (max) • Output Capacitance: 3.5 pF (max)

Isolation Voltage: 1500 Vrms (min)



Weight: 0.1 g (typ.)

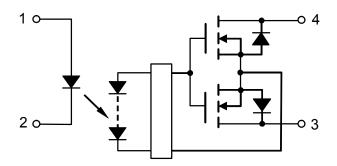
Pin Configuration (Top View)



2: CATHODE

3: DRAIN 4: DRAIN

Schematic



Absolute Maximum Ratings (Ta = 25°C)

	CHARACTERISTIC	SYMBOL	RATING	UNIT
	Forward Current	lF	50	mA
ED	Forward Current Derating (Ta ≥ 25°C)	ΔI _F /°C	-0.5	mA/°C
۳	Reverse Voltage	V _R	5	V
	Junction Temperature	Tj	125	°C
~	Off-State Output Terminal Voltage	V _{OFF}	80	V
DETECTOR	On-State Current	I _{ON}	40	mA
	On-State Current Derating (Ta ≥ 25°C)	Δl _{ON} /°C	-0.4	mA/°C
	Junction Temperature	Tj	125	°C
Storage Temperature Range		T _{stg}	-40 to 125	°C
Oper	ating Temperature Range	T _{opr}	-20 to 85	°C
Lead	Soldering Temperature (10 s)	T _{sol}	260	°C
Isolat	ion Voltage (AC, 1 minute, R.H. ≤ (60%) (Note 1)	BVS	1500	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Device considered a two-terminal device: Pins 1 and, 2 shorted together, and pins 3 and 4 shorted together.

Caution

This device is sensitive to electrostatic discharge. When using this device, please ensure that all tools and equipment are earthed.

Recommended Operating Conditions

CHARACTERISTIC	SYMBOL	MIN	TYP.	MAX	UNIT
Supply Voltage	V_{DD}	_	_	64	V
Forward Current	lF	5	_	30	mA
On-State Current	I _{ON}	_	_	40	mA
Operating Temperature	T _{opr}	25	_	60	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

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Individual Electrical Characteristics (Ta = 25°C)

	CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
	Forward Voltage	V _F	I _F = 10 mA	1.0	1.15	1.3	V
LED	Reverse Current	I _R	V _R = 5 V	_	_	10	μА
	Capacitance	C _T	V = 0, f = 1 MHz	_	15	_	pF
TECTOR	Off-State Current	l _{OFF}	V _{OFF} = 80 V, Ta = 60°C	_	_	1	nA
DETE	Capacitance	C _{OFF}	V = 0, f = 100 MHz, t < 1 s	_	2.5	3.5	pF

Coupled Electrical Characteristics (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Trigger LED Current	I _{FT}	I _{ON} = 40 mA	_	_	3	mA
Return LED Current	I _{FC}	I _{OFF} = 10 μA	0.1	_	_	mA
On-State Resistance	R _{ON}	I _{ON} = 40 mA, I _F = 5 mA	_	16	25	Ω

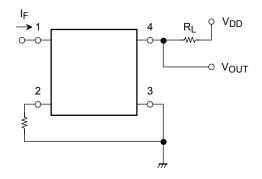
Isolation Characteristics (Ta = 25°C)

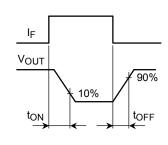
CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Capacitance Input to Output	Cs	V _S = 0 V, f = 1 MHz	_	0.8	_	pF
Isolation Resistance	R _S	V _S = 500 V, R.H. ≤ 60%	5 × 10 ¹⁰	10 ¹⁴	_	Ω
		AC, 1 minute	1500	_	_	Vrms
Isolation Voltage	BV_S	AC, 1 second (in oil) — 30	3000	_	VIIIIS	
		DC, 1 minute (in oil)	_	3000	_	Vdc

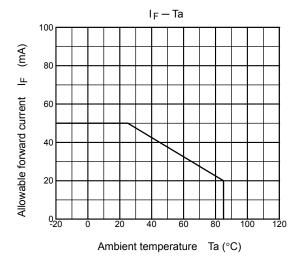
Switching Characteristics (Ta = 25°C)

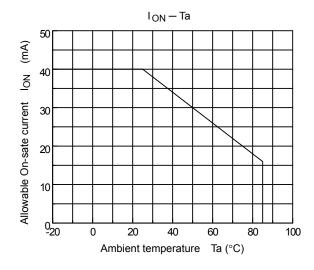
CHARACTERISTIC	SYMBOL	DL TEST CONDITION		TYP.	MAX	UNIT
Turn-on Time	t _{ON}	$R_L = 200 \Omega$ (Note 2)	_	0.07	0.5	. ms
Turn-off Time	toff	$V_{DD} = 10 \text{ V}, I_F = 5 \text{ mA}$	_	0.07	0.5	1113

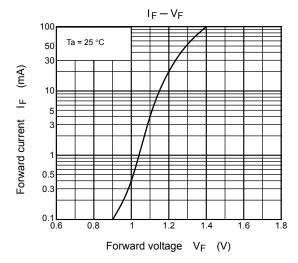
(Note 2): Switching time test circuit

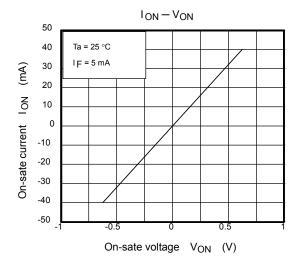


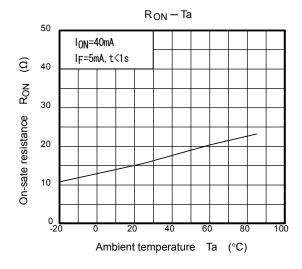


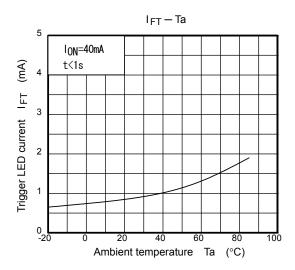




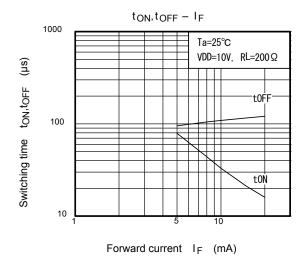


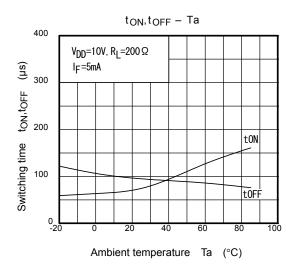


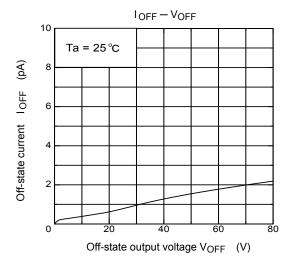


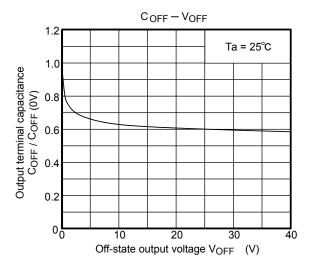


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