TOSHIBA Photocoupler Photorelay

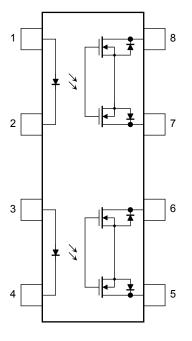
TLP4202G

Telecommunication Measurement Equipment Security Equipment FA

The Toshiba TLP4202G consists of an aluminum gallium arsenide infrared emitting diode optically coupled to a photo-MOSFET in a SOP package. This 2-form-B (NC) photorelay features a withstanding voltage of $350~\rm V$.

- 8-pin SOP (2.54SOP8): Height = 2.1 mm, pitch = 2.54 mm
- Normally closed (2-form-B) device
- Peak off-state voltage: 350 V (min)
- Trigger LED current: 3 mA (max)
- On-state current: 90 mA (max)
- On-state resistance: 50Ω (max)
- Isolation voltage: 1500 Vrms (min)
- UL Recognized: UL1577, File No. E67349

Pin Configuration (top view)



1, 3: Anode

2, 4: Cathode

5 : Drain D1

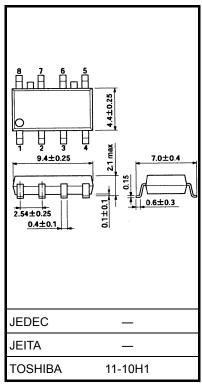
6 : Drain D2

7 : Drain D3

8 : Drain D4

1

Unit: mm



Weight: 0.2 g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

	Characteristics	Symbol	Rating	Unit
	Forward current	lF	50	mA
	Forward current derating (Ta ≥ 25°C)	ΔI _F /°C	-0.5	mA/°C
LED	Peak forward current (100 μs pulse, 100 pps)	I _{FP}	1	Α
	Reverse voltage	V _R	5	V
	Junction temperature	Tj	125	°C
	Off-state output terminal voltage	V _{OFF}	350	V
ctor	On-state current	I _{ON}	90	mA
Detector	On-state current derating (Ta ≧ 25°C)	Δl _{ON} /°C	-0.9	mA/°C
	Junction temperature	Tj	125	°C
Stora	age temperature range	T _{stg}	-55 to 125	°C
Operating temperature range		T _{opr} -40 to 85		°C
Lead soldering temperature (10 s)		T _{sol}	260	°C
Isola	tion voltage (AC, 1 min, R.H. \leq 60%) (Note 1)	BVS	1500	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Pins 1, 2, 3 and 4 are shorted together, and pins 5, 6, 7 and 8 are shorted together.

Recommended Operating Conditions

Characteristics	Symbol	Min	Тур.	Max	Unit
Supply voltage	V_{DD}	_	_	280	V
Forward current	lF	5	_	25	mA
On-state current	I _{ON}	_	_	90	mA
Operating temperature	T _{opr}	-20		65	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

Electrical Characteristics (Ta = 25°C)

	Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
	Forward voltage	V_{F}	I _F = 10 mA	1.0	1.15	1.3	V
LED	Reverse current	I _R	V _R = 5 V	_	_	10	μΑ
	Capacitance	C _T	V = 0, f = 1 MHz	_	30	_	pF
Detector	Off-state current	loff	V _{OFF} = 350 V, I _F = 5 mA	_	_	1	μΑ
Dete	Capacitance	C _{OFF}	$V = 0, f = 1 \text{ MHz}, I_F = 5 \text{ mA}$		30	_	pF

2

Coupled Electrical Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Trigger LED current	I _{FC}	I _{OFF} = 10 μA	_	1	3	mA
Return LED current	I _{FT}	I _{ON} = 90 mA	0.1	_	_	mA
On-state resistance	R _{ON}	I _{ON} = 90 mA	_	27	50	Ω

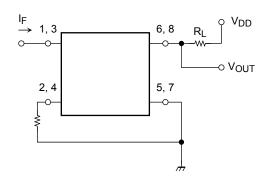
Isolation Characteristics (Ta = 25°C)

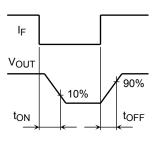
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Capacitance input to output	CS	V _S = 0, f = 1 MHz	_	8.0	_	pF
Isolation resistance	R _S	V _S = 500 V, R.H. ≦ 60%	5×10^{10}	10 ¹⁴	_	Ω
	AC, 1 min BV _S AC, 1 s, in o	AC, 1 min	1500	_	_	Vrms
Isolation voltage		AC, 1 s, in oil	_	3000	_	VIIIIS
		DC, 1 min, in oil	_	3000	_	Vdc

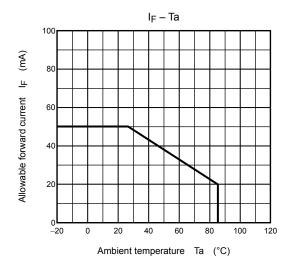
Switching Characteristics (Ta = 25°C)

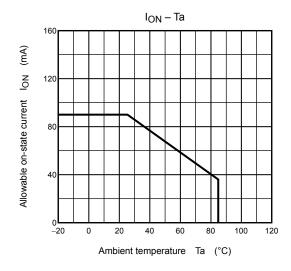
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Turn-on time	toN	$R_L = 200 \Omega$	_	0.25	0.5	ms
Turn-off time	t _{OFF}	$V_{DD} = 20 \text{ V, I}_F = 5 \text{ mA}$ (Note 2)	_	0.5	1	ms

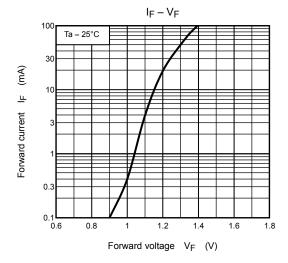
Note 2: Switching time test circuit

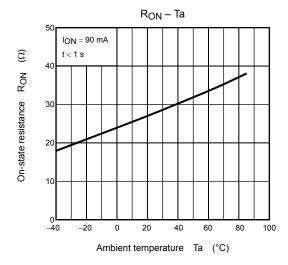


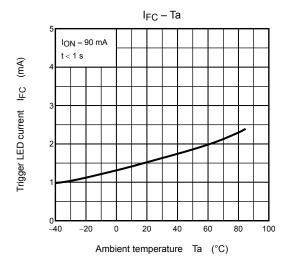


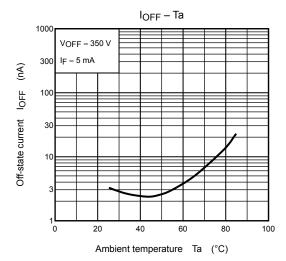


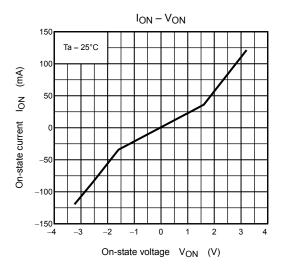


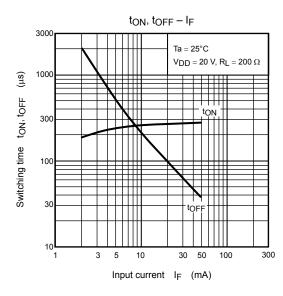


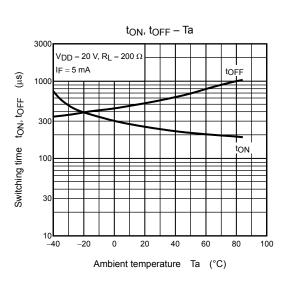












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