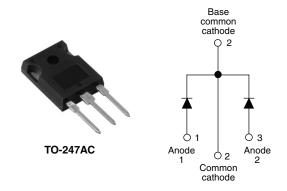


Vishay High Power Products

Schottky Rectifier, 2 x 15 A



PRODUCT SUMMARY				
I _{F(AV)} 2 x 15 A				
V_{R}	150 V			

FEATURES

- 175 °C T_J operation
- Center tap TO-247 package
- · Low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified for industrial level

DESCRIPTION

The 30CPQ150 center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	UNITS			
I _{F(AV)}	Rectangular waveform	30	Α		
V _{RRM}		150	V		
I _{FSM}	t _p = 5 μs sine	1000	Α		
V _F	15 Apk, T _J = 125 °C (per leg)	0.78	V		
T _J		- 55 to 175	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	30CPQ150	UNITS	
Maximum DC reverse voltage	V_{R}	150	V	
Maximum working peak reverse voltage	V_{RWM}	130	V	

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average	per device			30		
forward current See fig. 5	per leg	I _{F(AV)}	I _{F(AV)} 50 % duty cycle at T _C = 135 °C, rectangular wa		15	
Maximum peak one cycle non-repetitive surge current per leg I _{ESM}		I	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	1000	A
See fig. 7		IFSM	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	340	
Non-repetitive avalanche er	nergy per leg	g E_{AS} $T_{J} = 25 ^{\circ}C$, $I_{AS} = 0.50 A$, $L = 90 \text{mH}$		11.25	mJ	
Repetitive avalanche currer	nt per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		0.50	Α

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS VAI		VALUES	UNITS
	V _{FM} ⁽¹⁾	15 A	T _J = 25 °C	1.00	V
Maximum forward voltage drop per leg		30 A		1.19	
See fig. 1		15 A	T _J = 125 °C	0.78	
		30 A		0.93	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V _B = Rated V _B	0.1	mA
See fig. 2		T _J = 125 °C	v _R = nateu v _R	15	IIIA
Maximum junction capacitance per leg	C _T	V _R = 5 V _{DC} (test signal range 100 kHz to 1 MHz) 25 °C		340	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		7.5	nH
Maximum voltage rate of change	dV/dt	Rated V _R 10 000		V/µs	

Note

 $^{^{(1)}\,}$ Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storag temperature range	je	T _J , T _{Stg}		- 55 to 175	°C
Maximum thermal resistance junction to case per leg	,	D	DC operation See fig. 4	2.20	
Maximum thermal resistance junction to case per package	,	R_{thJC}	DC operation	1.10	°C/W
Typical thermal resistance, case to heatsink		R _{thCS}	R _{thCS} Mounting surface, smooth and greased		
Approximate weight				6	g
Approximate weight				0.21	OZ.
	minimum			6 (5)	kgf · cm
Mounting torque —	maximum			12 (10)	(lbf \cdot in)
Marking device			Case style TO-247AC (JEDEC)	30CP	Q150



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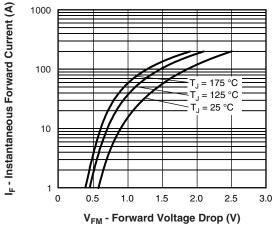


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

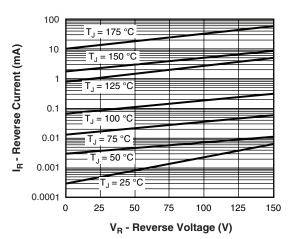


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

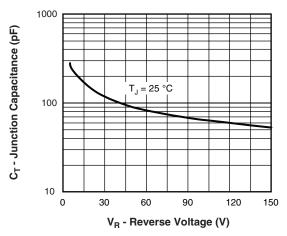


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

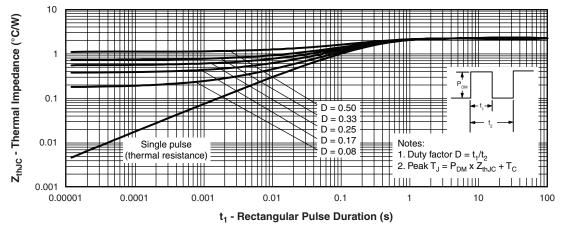


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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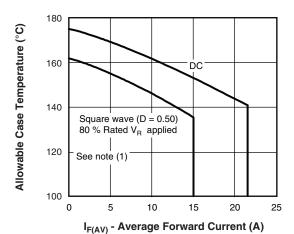


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

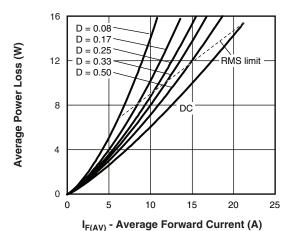


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

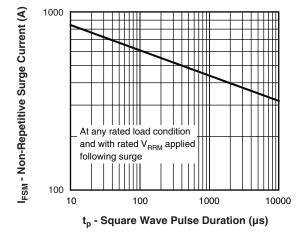


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

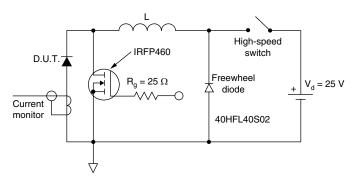


Fig. 8 - Unclamped Inductive Test Circuit

Note

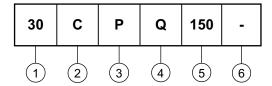
 $\begin{array}{ll} \text{(1)} \;\; \text{Formula used:} \; T_C = T_J - (Pd + Pd_{REV}) \; x \; R_{thJC}; \\ \text{Pd} = \text{Forward power loss} = I_{F(AV)} \; x \; V_{FM} \; \text{at} \; (I_{F(AV)}/D) \; (\text{see fig. 6}); \\ \text{Pd}_{REV} = \text{Inverse power loss} = V_{R1} \; x \; I_R \; (1 - D); \; I_R \; \text{at} \; V_{R1} = 80 \; \% \; \text{rated} \; V_R \\ \end{array}$



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ORDERING INFORMATION TABLE

Device code



1 - Current rating (30 = 30 A)

2 - Circuit configuration:

C = Common cathode

3 - Package:

P = TO-247

4 - Schottky "Q" series

5 - Voltage code (150 = 150 V)

6 - • None = Standard production

• PbF = Lead (Pb)-free

Tube standard pack quantity: 25 pieces

LINKS TO RELATED DOCUMENTS					
Dimensions http://www.vishay.com/doc?95223					
Part marking information	http://www.vishay.com/doc?95226				



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