

PZT4403

40 V, 600 mA PNP switching transistor Rev. 03 — 2 March 2010

Product data sheet

1. **Product profile**

1.1 General description

PNP switching transistor in a medium power SOT223 (SC-73) small Surface-Mounted Device (SMD) plastic package.

NPN complement: PZT4401.

1.2 Features and benefits

- High current (max. 600 mA)
- Low voltage (max. 40 V)

1.3 Applications

Switching and linear amplification

1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CEO}	collector-emitter voltage	open base		-40	V
I _C	collector current			-600	mΑ
h _{FE}	DC current gain	$V_{CE} = -1 \text{ V};$ $I_{C} = -150 \text{ mA}$	<u>[1]</u> 100 -	300	

^[1] Pulse test: $t_p \le 300~\mu s;~\delta \le 0.02.$

Pinning information 2.

Table 2. **Pinning**

Table 2.	riiiiiig		
Pin	Description	Simplified outline	Graphic symbol
1	base		
2, 4	collector	4	2, 4
3	emitter		1
			'_
		<u> </u>	3
			sym028



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3. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PZT4403	SC-73	plastic surface-mounted package with increased heatsink; 4 leads	SOT223

4. Marking

Table 4. Marking codes

Type number	Marking code
PZT4403	ZT4403

5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter	-	-40	V
V_{CEO}	collector-emitter voltage	open base	-	-40	V
V_{EBO}	emitter-base voltage	open collector	-	-6	V
I _C	collector current		-	-600	mA
I _{CM}	peak collector current		-	-800	mA
I _{BM}	peak base current		-	-200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	<u>[1]</u> -	1150	mW
Tj	junction temperature		-	150	°C
T _{amb}	ambient temperature		-65	+150	°C
T _{stg}	storage temperature		-65	+150	°C

^[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for collector 1 cm².

6. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	<u>[1]</u> -	-	106	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point		-	-	25	K/W

^[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².

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7. Characteristics

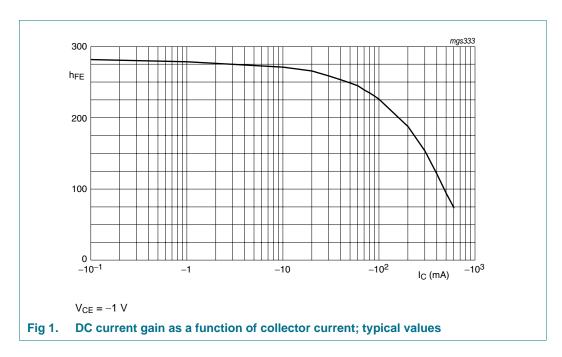
Table 7. Characteristics

 $T_{amb} = 25$ °C unless otherwise specified.

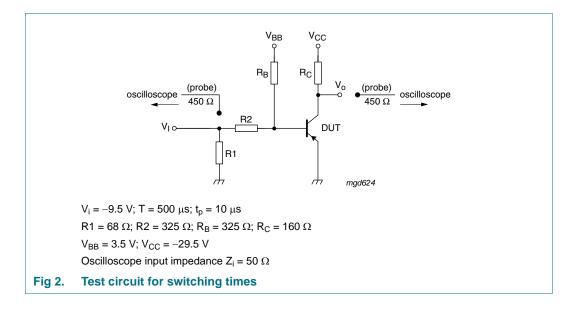
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off current	$V_{CB} = -40 \text{ V}; I_E = 0 \text{ A}$		-	-	-50	nA
ГЕВО	emitter-base cut-off current	$V_{EB} = -5 \text{ V}; I_C = 0 \text{ A}$		-	-	-50	nA
h _{FE}	DC current gain	$V_{CE} = -1 V;$ $I_{C} = -0.1 \text{ mA}$		30	-	-	
		$V_{CE} = -1 V;$ $I_{C} = -1 \text{ mA}$		60	-	-	
		$V_{CE} = -1 V;$ $I_{C} = -10 \text{ mA}$		100	-	-	
		$V_{CE} = -1 \text{ V};$ $I_{C} = -150 \text{ mA}$	<u>[1]</u>	100	-	300	
		$V_{CE} = -2 \text{ V};$ $I_{C} = -500 \text{ mA}$		20	-	-	
V _{CEsat}	collector-emitter saturation voltage	$I_C = -150 \text{ mA};$ $I_B = -15 \text{ mA}$	<u>[1]</u>	-	-	-400	mV
		$I_C = -500 \text{ mA};$ $I_B = -50 \text{ mA}$	[1]	-	-	−750	mV
V _{BEsat}	base-emitter saturation voltage	$I_C = -150 \text{ mA};$ $I_B = -15 \text{ mA}$	[1]	-	-	-950	mV
		$I_C = -500 \text{ mA};$ $I_B = -50 \text{ mA}$	[1]	-	-	-1300	mV
d	delay time	$V_{CC} = -29.5 \text{ V};$		-	-	15	ns
·r	rise time	$I_{C} = -150 \text{ mA};$ $I_{Bon} = -15 \text{ mA};$		-	-	30	ns
on	turn-on time	$I_{Boff} = 15 \text{ mA};$		-	-	40	ns
·s	storage time	V _{BB} = 3.5 V		-	-	300	ns
f	fall time			-	-	50	ns
off	turn-off time			-	-	350	ns
ŤΤ	transition frequency	$V_{CE} = -10 \text{ V};$ $I_{C} = -20 \text{ mA};$ $f = 100 \text{ MHz}$		200	-	-	MHz
C_c	collector capacitance	$V_{CB} = -5 \text{ V};$ $I_E = i_e = 0 \text{ A};$ $f = 1 \text{ MHz}$		-	-	8.5	pF
C _e	emitter capacitance	$V_{EB} = -500 \text{ mV};$ $I_{C} = i_{c} = 0 \text{ A};$ $f = 1 \text{ MHz}$		-	-	35	pF

^[1] Pulse test: $t_p \leq 300~\mu s;~\delta \leq 0.02.$

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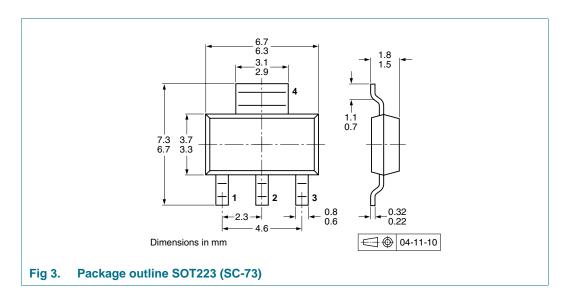


8. Test information



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9. Package outline



10. Packing information

Table 8. Packing methods

The indicated -xxx are the last three digits of the 12NC ordering code.[1]

Type number	Package	Description	Packing quantity	
			1000	4000
PZT4403	SOT223	8 mm pitch, 12 mm tape and reel	-115	-135

^[1] For further information and the availability of packing methods, see Section 13.

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11. Revision history

Table 9. Revision history

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	nended					
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1.4 "Quick reference data": a	dded					
 Section 3 "Ordering information": added 						
Section 4 "Marking": added						
Section 7 "Characteristics": amended						
 Section 8 "Test information": added 						
3: superseded by minimized page	ackage outline drawing					
Section 10 "Packing information": added						
12 "Legal information": updat	ed					
Product data sheet	-	PZT4403_1				
Product specification	-	-				
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12. Legal information

12.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions"
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For sales office addresses, please send an email to: salesaddresses@nxp.com

PZT4403 NXP Semiconductors

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