

### STM32 F4 series Cortex<sup>TM</sup>-M4 MCUs Releasing your creativity



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### 32-bit Flash MCU, up to 180 MHz/225 DMIPS, with DSP instructions, floating point unit and advanced peripherals

ST is extending its range of target applications with the STM32 F4 series. Based on the Cortex<sup>™</sup>-M4 core, this series opens the door to the digital signal controller (DSC) market. This extension to our STM32 product portfolio offers devices that are pin-to-pin and software compatible with the STM32 F2 series, but with more performance, DSP capability, a floating point unit, more SRAM, and peripheral improvements such as a TFT LCD controller with graphics acceleration, SDRAM, serial audio interface, less than 1 µA RTC and 2.4 MSPS ADCs. The ARM<sup>®</sup> Cortex-M4 core features built-in single-cycle multiply-accumulate (MAC) instructions, optimized SIMD and saturated arithmetic instructions.

The adaptive real-time ART Accelerator<sup>™</sup> combined with ST's 90 nm technology provides linear performance up to 180 MHz, unleashing the full performance of the core. The Chrom-ART Accelerator<sup>™</sup> offers twice as much performance for graphics content creation and handling. The STM32 F4 series now provides products offering the best balance between performance (105 DMIPS), following STM32 Dynamic Efficiency<sup>™</sup> guiding principle to achieve as low as 128 µA/MHz in Run mode, 9 µA typ in Stop mode, and offering high integration with packages as small as 3 x 3 mm package. These features expand the number of addressable applications in the industrial, consumer and healthcare segments. The STM32 F4 series includes devices with 128 Kbytes to 2 Mbytes of on-chip Flash memory, and up to 256 Kbytes of SRAM, and 20 communication interfaces. WLCSP (down to 3 x 3mm), UFQFPN48, LQFP64, LQFP100, LQFP144, LQFP176, UFBGA100, UFBGA169, UFBGA176, LQFP208, TFBGA64, TFBGA216 packages are available.

#### **KEY APPLICATIONS**

- Industrial and medical
- Control panels with LCD screens for alarm systems, high-end meters, factory automation
- Medical: respiratory equipment, patient monitors
- EPOS: scanners, cash registers, tax machines, vending machines, printers
- Industrial AC servos, general-purpose inverters, solar inverters and robots
- Surveillance cameras
- Consumer
- HMI for appliances
- DAB, car radios and infotainment
- Home audio, switch boxes, headsets
- · Sensor hubs for mobile devices

#### **BLOCK DIAGRAM**

	ART Accelerator	Up to 2-Mbyte dual bank Fl				
		Up to 256-Kbyte SRAM				
		Chrom-ART Accelerator™				
System		LCD-TFT controller <sup>4</sup>				
Power supply 1.2 V regulator POR/PDR/PVD	ARM Cortex-M4 84/168/180 MHz	FMC/SRAM/NOR/NAND/CF/ SDRAM				
Xtal oscillators 32 kHz + 4 to 26 MHz	04/100/100 Miliz	80-byte + 4-Kbyte backup SRAM				
Internal RC oscillators		512 OTP bytes				
	Floating point unit (FPU)	• • • •				
Clock control	Nested vector	Connectivity				
BTC/AWII	interrupt	Camera interface				
1x SysTick timer		6x SPI, 2x I <sup>2</sup> S, 3x I <sup>2</sup> C <sup>3</sup>				
2x watchdogs		Ethernet MAC 10/100 with IEEE 1588				
ndependent and window)	JTAG/SW debug/ETM	2x CAN 2.0B				
6/50/81/114/140/168 I/Os		1x USB 2.0 OTG FS/HS 1				
Cyclic redundancy check (CRC)		1x USB 2.0 OTG FS				
		1x SDIO				
	Multi-AHB bus matrix	4x USART + 4 UART LIN, smartcard, IrDA, modem control				
	16-channel DMA	1x SAI (Serial audio interface)				
Control	Crypto/hash processor <sup>2</sup>					
2x 16-bit motor control	3DES, AES 256, GCM, CCM	Analog				
PWM Synchronized AC timer	SHA-1, SHA-256, MD5,	2-channel 2x 12-bit DAC				
5x 16-bit timers 2x 32-bit timers	HMAC	3x 12-bit ADC 24 channels / 2 MSPS				
3x 16-bit timers	True random number	Temperature sensor				

- Crypto/hash processor on STM32F415, STM32F417, STM32F437 and STM32F439 only 2.
- With digital filter feature, up to 1 Mbit/s 3
- 4. For STM32F4x9 only

#### FEATURES AND BENEFITS

Features	Benefits						
High performance	<ul> <li>Boosted execution of control algorithms</li> </ul>						
• Up to 180 MHz/225 DMIPS Cortex-M4 with single cycle DSP MAC	<ul> <li>More features for your applications</li> </ul>						
and floating point unit	Ease of use						
CoreMark score: 608 at 180 MHz	Better code efficiency						
CoreMark/MHz: 3.37	Faster time to market						
	Elimination of scaling and saturation						
	<ul> <li>Easier support for meta-language tools</li> </ul>						
Maximum integration							
Up to 2 Mbytes of on-chip dual bank Flash memory, up to 256 Kbytes	Read while write operations support						
of SRAM, reset circuit, internal RCs, PLLs, ultra-small packages	More features in space constrained applications						
(VVLUSP)	Use of high-level languages: Java, .Net						

#### FEATURES AND BENEFITS

#### Features Designed for high performance and ultra-fast data transfers

- ART Accelerator™: memory accelerator
- Chrom-ART Accelerator<sup>™</sup>: graphics accelerator (rectangle filling, rectangle copy with pixel format conversion and blending)
- 32-bit. 7-laver AHB bus matrix with up to 10 masters and 8 slaves including 3 blocks of SRAM
- Multi DMA controllers: 2 general-purpose, 1 for USB HS, one for Ethernet
- One 4th SRAM block dedicated to the core
- Flexible memory interface with SDRAM support: up to 90 MHz, 32-bit parallel

#### Outstanding power efficiency

- Ultra-low dynamic power in Run mode: 128 uA/MHz at 84 MHz (STM32F401), 260 µA/MHz at 180 MHz (STM32F429/439) running CoreMark benchmark from Flash memory (peripherals off)
- RTC <1  $\mu$ A typ in V<sub>PAT</sub> mode
- Down to 9 μA typ (STM32F401) 100 μA typ (STM32F429/439) in Stop mode
- 3.6 V down to 1.7 V<sup>1</sup> V<sub>DD</sub>
- 1.2 V voltage regulator with power scaling capability

#### Superior and innovative peripherals and connectivity

- Connectivity: camera interface, crypto/hash HW processor with AES
   New possibilities to connect and communicate high-speed data GCM and CCM support, and SHA-256
- Ethernet MAC10/100 with IEEE 1588 v2 support, 2 USB OTG (one with HS support)
- Up to 20 communication interfaces (including 4x USART + 4x UART, 6x SPI, 3x I<sup>2</sup>C with digital filter, 2x CAN, SDIO)
- USART at 11.25 Mbit/s; SPI at 45 Mbit/s
- Audio: dedicated audio PLL, 2x I<sup>2</sup>S and 1x SAI with TDM<sup>2</sup> support
- LCD TFT controller
- Up to XGA (1024x768)
- Up to 24-bit RGB parallel pixel output
- 2-layer support with blending
- Analog: 2x 12-bit DACs, 3x 12-bit ADCs reaching 7.2 MSPS in interleaved mode
- Up to 17 timers: 16 and 32 bits running up to 180 MHz

#### **High integration**

• WLCSP49 3 x 3 mm (STM32F401, 256-Kbyte Flash/64-Kbyte SRAM), WLCSP90 4 x 4.2 mm (STM32F405/F415, 1-Mbyte Flash/192-Kbyte SRAM), WLCSP143 4.5 x 5.5 mm (STM32F429/439, 2-Mbyte Flash/256-Kbyte SRAM)

#### Extensive tools and software solutions

- Hardware sector protection with execute only access
- Various IDE, starter kits, libraries, RTOS and stacks, either open source or provided by ST or 3rd parties, including the ARM CMSIS DSP library optimized for Cortex-M4 instructions

#### Note:

- 1. Except LQFP64 and LQFP100 packages
- 2. TDM: time division multiplex

#### **Benefits**

Performance equivalent to zero-wait execution from Flash

 Graphics content is created twice as fast and independently from the CPU

Concurrent execution and data transfer

 Simplified resource allocation • High bandwidth for external memories Cost-effective external RAM

• Extra flexibility to reduce power consumption for applications requiring both high-processing and low-power performance when running at low voltage or on a rechargeable battery

 High-quality multi-channel audio support • Support for cost-effective standard displays

More precision thanks to high resolution

• Smaller board space allowing for smaller applications

 Software IP protection · A wide choice within the STM32 ecosystem to develop your applications

#### HIGH PERFOMANCE

#### **ART Accelerator performance**

Unleashing the full performance of the core beyond the embedded Flash's intrinsic speed is an art. Combined with ST's 90 nm technology, our ART Accelerator™ achieves a linear performance up to 180 MHz, offering 225 DMIPS and 608 CoreMark performance executing from Flash. The acceleration mechanism is made possible using a prefetch queue, a branch cache and a smart arbitration mechanism.

- MCUs using less advanced accelerators or slower embedded Flash memories impact execution performance as wait states occur.
- MCUs using faster Flash but no branch cache acceleration to achieve performance usually show higher power consumption as a result of more accesses to a power-hungry Flash.



#### **Richer graphics and animations with ST Chrom-ART Accelerator**

The ST Chrom-ART<sup>™</sup> Accelerator efficiently handles the repetitive and heavy graphics content creation operations. Raw data copies, image blending and pixel format conversion are processed by the Chrom-ART™ Accelerator independently from the CPU and twice more efficiently. STM32F429 MCUs combine an LCD-TFT controller with the external memory interface supporting static and SDRAM memories, inside a highperformance system architecture allowing maximum data throughput to support resolutions up to XGA (1024 x 768) with up to 20 frames/s motion rate while keeping CPU and resources available for real-time applications.

Human machine interface implementation example

- STM32F427/429 using Chrom-ART Accelerator, internal or external memory for frame buffer and TFT controller for display • Up to XGA (1024 x 768)
- 16-/32-bit external memory interface
- Recommended packages: LQFP100, LQFP144, LQFP176/BGA176 or LQFP208/BGA216



#### STM32 DYNAMIC EFFICIENCY™

#### Less dynamic power. More performance.

Technologies featured in STM32F401 follow STM32 Dynamic Efficiency<sup>™</sup> guiding principle and include the unique ART Accelerator<sup>™</sup>, a prefetch queue and branch cache. This allows zero-wait-state execution from Flash which boosts performance to 105 DMIPS (285 CoreMark) at 84 MHz and helps achieve RUN current down to 128µA/MHz. In addition, 90nm process technology boosts performance and reduces dynamic power, while dynamic voltage scaling optimizes the operating voltage to meet performance demands and minimize leakage. Stop mode current is only 9µA at 1.8V.





## Hardware tools

### A comprehensive choice of hardware tools helps you benefit from the STM32 F4's wide set of features.

• STM32 F4 Discovery kits are user-friendly demo boards available at affordable prices. Various versions are proposed with specific sets of companion devices (such as MEMS audio microphones, LCD displays, MEMS sensors) and software examples to facilitate evaluation. All include the following.

ST-LINK/V2 in-circuit debugger/programmer so that you can directly program STM32 F4 microcontrollers with your own applications. More details at: www.st.com/stm32discovery

Associated expansion boards are proposed adding Ethernet, IEEE 802.11b/g/n Wi-Fi, 1.3 Mpixel CMOS camera. More details at: www.st.com/stm32evaltools



• STM32 F4 evaluation boards are premium development platforms implementing the full range of device peripherals. Several form factors are proposed with a large set of resources such as up to 2-Mbyte Flash, up to 32-Mbyte SDRAM, up to 5.7 inch LCD display. All provide a large range of extensions, a ST-LINK/V2 in-circuit debugger/programmer, as well as a comprehensive free software library (image browser, audio player). You will find more details by typing "STM32 eval" in an ST website search.





STM32429I-EVAL1

STM32439I-EVAL2



• ST's new Nucleo boards propose open platforms with unified extension capability at budget costs. Arduino<sup>™</sup> connectivity support and full access to all device peripherals make it easy to expand the functionality of the STM32F401 Nucleo board with a wide choice of specialized shields. Besides, the STM32F401 Nucleo is mbed-enabled, so you can benefit from instant access to online IDE at mbed.org (nothing to install). Get the full benefits of your STM32F401 Nucleo in just a few minutes. More details at: www.st.com/stm32nucleo



- Complementary STM32 F4 board solutions from ST partners include:
- IAR Experiment! (magnetometer, game controller) and KickStart kits, Keil Starter kit, as well as the Raisonance EvoPrimer platform
- STM32F4 Java evaluation kit made in collaboration with IS2T
- Mountaineer Microsoft .NET Micro Framework platform





STM3240G-ETH/NMF and STM3240G-ETH/NMF



STM3242I-SK/IAR



STM3240G-SK/IAR



STM3240G-SK/KEI



STM32F4DIS-BB; STM32F4DIS-CAM; STM32F4DIS-LCD and STM32F4DIS-WIFI

# Software solutions

STM32 F4 microcontrollers can be programmed using classic C/C++, but also with some other means such as Java or Matlab/Simulink. ST's partners provide a large offer around the STM32 F4, from software tools to embedded software solutions.

### C/C++ DEVELOPMENT

STM32 F4 microcontrollers are supported by a wide choice of integrated development environments from partners. These include project managers, editors, debuggers, optimizing C/C++ compilers, flash loaders and example projects, as well as full collections of embedded software libraries.

#### Software development tools

- IAR Embedded Workbench EWARM IDE
- Keil MDK-ARM uVision IDF
- GCC-based IDEs

#### **Embedded software**

- Alpwise, with their Bluetooth stack solutions
- FreeRTOS, the open-source real-time operating system
- Micrium  $\mu$ C collection, with for instance  $\mu$ C/TCP-IP internet stack
- Express Logic, with for instance the TheadX real-time operating system
- HCC, with for instance with the USB libraries More information at http://www.st.com/stm32-stm8-firmware

ST complements these offers from partners with some unique software, dedicated to STM32 development.

- STM32Cube™: free from ST, simplifies and speeds up developers' work, by allowing them focus on their added value by offering an easy and fast way to configure the microcontroller. It is composed of 2 main elements: software on the PC and a complete set of embedded software bricks.
- STM32CubeMX: a software tool on the PC, providing an easy step-by-step approach to configure the STM32 via wizards (pinout conflict handling, clock and peripheral configuration, power consumption, and more), and generating initialization C code depending on user choices, including project files for user's favorite development environment.

• STM32CubeF4: a set of generic bricks for the STM32 F4, ensuring easy portability towards other STM32 series. Comes with full peripheral coverage. production-ready drivers and a set of middleware (USB, TCP/IIP, graphics from partnership around Segger emWin, RTOS, file system, and more) and hundreds of examples. The user can

therefore focus on the added value for the application and forget about implementation details. License terms are highly permissive, with the driver layer being fully open source. More on STM32Cube at www.st.com/stm32cube



• STM32 F4 applications are fine-tuned with STM Studio, a free graphical tool to monitor and display variables at run time. Connected to the STM32 F4 via a standard design probe, STM Studio reads variables on-the-fly while the application is running (non-intrusive). Various graphics views are available to meet your needs. More details at: www.st.com/stm-studio

#### Application-specific bricks:

- STM32 audio solutions: Full range of audio software bricks, optimized for the STM32 F4:
- Adapted transport layers, such as USB synchronization, Bluetooth profiles, and more
- Music codecs: MP3, WMA, AAC-LC, HE-AACv1, HE-AACv2, OGG Vorbis, SBC, and more
- Speech codecs: Speex, G726, G711, G729, G722, and more
- · Post-processing algorithms such as sample rate converters, filters (enabling a graphical equalizer, loudness, bass mix, and more), stereo widening, smart volume control (digital volume control with no saturation), and more. Coming with a PC tool to help user finetunina.
- Smartphone accessory libraries, such as iAP (iPod application protocol) interface or Android interfacing
- Contact your sales office for information on availability for specific STM32 part numbers.
- STM32 industrial protocols: Full range of supported industrial protocols, including Profinet, EtherCAT, Modbus, DeviceNet, CANopen, and more, via our partner network, making the most of the STM32 F4 with, in particular, its IEEE 1588 feature for synchronized nodes
- STM32 cryptographic library: Implementation of cryptographic algorithms, using STM32F41x or STM32F43x hardware acceleration when available, or 100% implemented by software for the others, but with same API.

### **BEYOND C/C++ DEVELOPMENT**

- STM32 Java development environment (www.st.com/stm32-java) featuring:
- Full development environment, Eclipse-based, and including a simulator
- Java Virtual Machine and mechanisms to call legacy C code
- Specific user interface pack, allowing users to develop GUI in Java while benefiting from STM32 hardware acceleration when available (Chrom-ART).

• NET Micro Framework enabling the use of Microsoft Visual Studio for STM32 development

 Matlab/Simulink integration with peripheral modelization - can be used together with Matlab 2013b that generates a code-based Cortex-M DSP library (download available for free from www.st.com/stm32-mat-target)









		STM32F429/439												
Main commor features Cortex™-	n M4	180 MHz 512-KB to 2-MB Flash 256-KB SRAM	Crypto /hash² RNG	2x 12-bit DAC	Ethernet IEEE 1588 2x CAN Camera I/F	SDRAM interface FMC	Serial audio interface (SAI)	Chrom-ART Accelerator	TF LCI contro					
(DSP + FF	PU)	STM32F427/437												
• Up to 2x US 2.0 OTG FS • SDIO	SB S/HS	180 MHz 1 to 2-MB Flash 256-KB SRAM	Crypto /hash² RNG	2x 12-bit DAC	Ethernet IEEE 1588 2x CAN Camera I/F	SDRAM interface FMC	Serial audio interface (SAI)	Chrom-ART Accelerator						
USART, SPI	I, I <sup>2</sup> C	STM32F407/417												
<ul> <li>PLL</li> <li>16- and 32 timers</li> </ul>	<ul> <li>PLL</li> <li>16- and 32-bit timers</li> </ul>	168 MHz 512-KB to 1-MB Flash 192-KB SRAM	Crypto /hash² RNG	2x 12-bit DAC	Ethernet IEEE 1588 2x CAN Camera I/F									
		STM32F405/415												
• Up to 3x 12 ADC (0.41	2-bit µs)	168 MHz 512-KB to 1-MB Flash 192-KB SRAM	Crypto /hash² RNG	2x 12-bit DAC										
		STM32F401												
• Low voltag 1.7 <sup>1</sup> to 3.6 <sup>1</sup>	e V	84 MHz 128- to 512-KB Flash 96-KB SRAM	<ul> <li>STM32 Dynamic Efficiency™:</li> <li>Run mode down to 128 µA/MHz</li> <li>Stop mode down to 9 µA typ</li> <li>Small form factor: down to 3 x 3 m</li> </ul>											

Notes:

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1. 1.7 V min on specific packages

2. Hardware crypto/hash on F415/417 and F437/439 only





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Note:

\* Available in July 2014

Pin count

15

### STM32 F4 SERIES - ARM CORTEX™-M4 HIGH-PERFORMANCE MCUS WITH DSP AND FPU

<b>)</b>	Device	summary

#### STM32 F4 SERIES - ARM CORTEX™-M4 HIGH-PERFORMANCE MCUS WITH DSP AND FPU

	Flock	Internal		Timer f	functions	12-bit						Serial interface							Supply current (lcc)	
Part number	Flash size (Kbytes)	Internal RAM size (Kbytes)	Package	16-/ 32-bit timers	Others	ADC	DAC	I/Os	SPI	SAI	I2S	I2C	USART + UART <sup>4</sup>	USB OTG	CAN 2.0B	SDIO	Ethernet MAC10/ 100	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)
			STM32F4	401 line:	USB OTG	(FS),	low po	ower (	9 µA	typ. i	in St	op n	node) -	84 MH	z CPU					
STM32F401CB	128	64	WLCSP49 UFQFPN48	6/2		10		36	3		2	3	3	1				1.7 <sup>3</sup> to 3.6	1.8	128
STM32F401RB	128	64	LQFP64	6/2		16		50	3		2	3	3	1		1		1.7 <sup>3</sup> to 3.6	1.8	128
STM32F401VB	128	64	LQFP100 UFBGA100	6/2		16		81	4		2	3	3	1		1		1.7 <sup>3</sup> to 3.6	1.8	128
STM32F401CC	256	64	WLCSP49 UFQFPN48	6/2		10		36	3		2	3	3	1				1.7 <sup>3</sup> to 3.6	1.8	128
STM32F401RC	256	64	LQFP64	6/2	Ûv	16		50	3		2	3	3	1		1		1.7 <sup>3</sup> to 3.6	1.8	128
STM32F401VC	256	64	LQFP100 UFBGA100	6/2	WDG, RTC,	16		81	4		2	3	3	1		1		1.7 <sup>3</sup> to 3.6	1.8	128
STM32F401CD	384	96	WLCSP49 UFQFPN48	6/2	24-bit down	10		36	3		2	3	3	1				1.7 <sup>3</sup> to 3.6	1.8	137
STM32F401RD	384	96	LQFP64	6/2	counter	16		50	3		2	3	3	1		1		1.7 <sup>3</sup> to 3.6	1.8	137
STM32F401VD	384	96	LQFP100 UFBGA100	6/2		16		81	4		2	3	3	1		1		1.7 <sup>3</sup> to 3.6	1.8	137
STM32F401CE	512	96	WLCSP49 UFQFPN48	6/2		10		36	3		2	3	3	1				1.7 <sup>3</sup> to 3.6	1.8	137
STM32F401RE	512	96	LQFP64	6/2		16		50	3		2	3	3	1		1		1.7 <sup>3</sup> to 3.6	1.8	137
STM32F401VE	512	96	LQFP100 UFBGA100	6/2		16		81	4		2	3	3	1		1		1.7 <sup>3</sup> to 3.6	1.8	137
			STM32	F405/41	5 line: USI	B OTG	(FS/H	S¹), cr	ypto/	/hash	ı pro	ces	sor² - 16	8 MHz	CPU					
STM32F4050E	512	192	WLCSP90	12/2		13	2	72	3		2	3	4+2	2	2	1		1.7 <sup>3</sup> to 3.6	2.5	238
STM32F4050G	1024	192	WLCSP90	12/2		13	2	72	3		2	3	4+2	2	2	1		1.7 <sup>3</sup> to 3.6	2.5	238
STM32F4150G <sup>2</sup>	1024	192	WLCSP90	12/2		13	2	72	3		2	3	4+2	2	2	1		1.7³ to 3.6	2.5	238
STM32F405RG	1024	192	LQFP64	12/2	2x WDG.	16	2	51	3		2	3	4+2	2	2	1		1.8 to 3.6	2.5	238
STM32F415RG <sup>2</sup>	1024	192	LQFP64	12/2	RTC, 24-bit	16	2	51	3		2	3	4+2	2	2	1		1.8 to 3.6	2.5	238
STM32F405VG	1024	192	LQFP100	12/2	down counter	16	2	82	3		2	3	4+2	2	2	1		1.8 to 3.6	2.5	238
STM32F415VG <sup>2</sup>	1024	192	LQFP100	12/2		16	2	82	3		2	3	4+2	2	2	1		1.8 to 3.6	2.5	238
STM32F405ZG	1024	192	LQFP144	12/2		24	2	114	3		2	3	4+2	2	2	1		1.7 <sup>3</sup> to 3.6	2.5	238
STM32F415ZG <sup>2</sup>	1024	192	LQFP144	12/2		24	2	114	3		2	3	4+2	2	2	1		1.7 <sup>3</sup> to 3.6	2.5	238

		Internel	Package	Timer 1	functions	12	-bit						Serial	interfa	ice				Supply current (lcc)	
Part number	Flash size (Kbytes)	RAM size (Kbytes)		16-/ 32-bit timers	Others	ADC	DAC	I/Os	SPI	SAI	I2S	I²C	USART + UART4	USB Otg	CAN 2.0B	SDIO	Ethernet MAC10/ 100	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)
		S	TM32F407/41	7 line: 2	2x USB OT	G (FS/	HS¹), (	camer	a IF, o	cryp	to/ha	ish p	processo	or <sup>2</sup> - 1	68 MI	Iz CPL	J			
STM32F407IE	512	192	UFBGA176 LQFP176	12/2		24	2	140	3		2	3	4+2	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	2.5	238
STM32F417IE <sup>2</sup>	512	192	UFBGA176 LQFP176	12/2		24	2	140	3		2	3	4+2	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	2.5	238
STM32F407VE	512	192	LQFP100	12/2		16	2	82	3		2	3	4+2	2	2	1	Yes	1.8 to 3.6	2.5	238
STM32F417VE <sup>2</sup>	512	192	LQFP100	12/2		16	2	82	3		2	3	4+2	2	2	1	Yes	1.8 to 3.6	2.5	238
STM32F407ZE	512	192	LQFP144	12/2	0.	24	2	114	3		2	3	4+2	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	2.5	238
STM32F417ZE <sup>2</sup>	512	192	LQFP144	12/2	WDG, RTC,	24	2	114	3		2	3	4+2	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	2.5	238
STM32F407IG	1024	24 192 UFBGA176 12/2 24-t LQFP176 12/2 dow coun	24-bit down	24	2	140	3		2	3	4+2	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	2.5	238		
STM32F417IG <sup>2</sup>	1024	192	UFBGA176 LQFP176	12/2	counter	24	2	140	3		2	3	4+2	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	2.5	238
STM32F407VG	1024	192	LQFP100	12/2		16	2	82	3		2	3	4+2	2	2	1	Yes	1.8 to 3.6	2.5	238
STM32F417VG <sup>2</sup>	1024	192	LQFP100	12/2		16	2	82	3		2	3	4+2	2	2	1	Yes	1.8 to 3.6	2.5	238
STM32F407ZG	1024	192	LQFP144	12/2		24	2	114	3		2	3	4+2	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	2.5	238
STM32F417ZG <sup>2</sup>	1024	192	LQFP144	12/2		24	2	114	3		2	3	4+2	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	2.5	238
	STM32F42	27/437 lin	e: 2x USB OT	G (FS/H	S <sup>1</sup> ), camer	a IF, c	rypto/	'hash j	oroce	esso	r², SI	DRAI	VI interfa	ice, du	ial-ba	nk Fla	sh - 180 N	IHz CPU		
STM32F427AG⁵	1024	256	UFBGA169	12/2		24	2	140	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	2.5	260
STM32F427IG	1024	256	UFBGA176 LQFP176	12/2		24	2	140	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	2.5	260
STM32F427VG	1024	256	LQFP100	12/2		16	2	82	6	1	2	3	4+4	2	2	1	Yes	1.8 to 3.6	2.5	260
STM32F427ZG	1024	256	LQFP144	12/2		24	2	114	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	2.5	260
STM32F437IG <sup>2</sup>	1024	256	UFBGA176 LQFP176	12/2	Ûv	24	2	140	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	2.5	260
STM32F437VG <sup>2</sup>	1024	256	LQFP100	12/2	WDG, RTC,	16	2	82	6	1	2	3	4+4	2	2	1	Yes	1.8 to 3.6	2.5	260
STM32F437ZG <sup>2</sup>	1024	256	LQFP144	12/2	24-bit down	24	2	114	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	2.5	260
STM32F427AI⁵	2048	256	UFBGA169	12/2	counter	24	2	140	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	2.5	260
STM32F42711	2048	256	UFBGA176 LQFP176	12/2		24	2	140	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	2.5	260
STM32F427VI	2048	256	LQFP100	12/2		16	2	82	6	1	2	3	4+4	2	2	1	Yes	1.8 to 3.6	2.5	260
STM32F427ZI	2048	256	LQFP144	12/2		24	2	114	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	2.5	260
STM32F437AI⁵	2048	256	UFBGA169	12/2		24	2	140	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	2.5	260

#### STM32 F4 SERIES - ARM CORTEX™-M4 HIGH-PERFORMANCE MCUS WITH DSP AND FPU

	Floob	Internal		Timer f	unctions	12-bit							Serial interface						Supply current (lcc)	
Part number	Flash size (Kbytes)	Internal RAM size (Kbytes)	Package	16-/ 32-bit timers	Others	ADC	DAC	I/Os	SPI	SAI	I2S	I²C	USART + UART⁴	USB OTG	CAN 2.0B	SDIO	Ethernet MAC10/ 100	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)
STM32F437II <sup>2</sup>	2048	256	UFBGA176 LQFP176	12/2	2x WDG.	24	2	140	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	2.5	260
STM32F437VI <sup>2</sup>	2048	256	LQFP100	12/2	RTC, 24-bit	16	2	82	6	1	2	3	4+4	2	2	1	Yes	1.8 to 3.6	2.5	260
STM32F437ZI <sup>2</sup>	2048	256	LQFP144	12/2	down counter	24	2	114	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	2.5	260
			STM32F429/	/439 line	e: Same as	s STM	32F42	7/437	line	+ TF	T LC	D co	ntroller	- 180	MHz	CPU	1			
STM32F429BE	512	256	LQFP208	12/2		16	2	168	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	2.5	260
STM32F429IE	512	256	UFBGA176 LQFP176	12/2		16	2	140	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	2.5	260
STM32F429NE	512	256	TFBGA216	12/2		16	2	168	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	2.5	260
STM32F429VE	512	256	LQFP100	12/2		16	2	82	6	1	2	3	4+4	2	2	1	Yes	1.8 to 3.6	2.5	260
STM32F429ZE	512	256	LQFP144	12/2		16	2	114	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	2.5	260
STM32F429BG	1024	256	LQFP208	12/2		24	2	168	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	2.5	260
STM32F429IG	1024	256	UFBGA176 LQFP176	12/2		24	2	140	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	2.5	260
STM32F429NG	1024	256	TFBGA216	12/2		24	2	168	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	2.5	260
STM32F429VG	1024	256	LQFP100	12/2		16	2	82	6	1	2	3	4+4	2	2	1	Yes	1.8 to 3.6	2.5	260
STM32F429ZG	1024	256	LQFP144 WLCSP143	12/2		24	2	114	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	2.5	260
STM32F439BG <sup>2</sup>	1024	256	LQFP208	12/2	2x WDG,	24	2	168	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	2.5	260
STM32F439IG <sup>2</sup>	1024	256	UFBGA176 LQFP176	12/2	RTC, 24-bit	24	2	140	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	2.5	260
STM32F439NG <sup>2</sup>	1024	256	TFBGA216	12/2	down counter	24	2	168	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	2.5	260
STM32F439VG <sup>2</sup>	1024	256	LQFP100	12/2		16	2	82	6	1	2	3	4+4	2	2	1	Yes	1.8 to 3.6	2.5	260
STM32F439ZG <sup>2</sup>	1024	256	LQFP144 WLCSP143	12/2		24	2	114	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	2.5	260
STM32F429Al⁵	2048	256	UFBGA169	12/2		24	2	140	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	2.5	260
STM32F429BI	2048	256	LQFP208	12/2		24	2	168	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	2.5	260
STM32F429II <sup>2</sup>	2048	256	UFBGA176 LQFP176	12/2		24	2	140	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	2.5	260
STM32F429NI	2048	256	TFBGA216	12/2		24	2	168	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	2.5	260
STM32F429VI	2048	256	LQFP100	12/2		16	2	82	6	1	2	3	4+4	2	2	1	Yes	1.8 to 3.6	2.5	260
STM32F429ZI	2048	256	LQFP144 WLCSP143	12/2		24	2	114	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	2.5	260
STM32F439AI <sup>5</sup>	2048	256	UFBGA169	12/2		24	2	140	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	2.5	260
STM32F439BI <sup>2</sup>	2048	256	LQFP208	12/2		24	2	168	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	2.5	260

#### STM32 F4 SERIES - ARM CORTEX™-M4 HIGH-PERFORMANCE MCUS WITH DSP AND FPU

	Flack	Internal RAM size (Kbytes)		Timer f	12-bit			Serial interface										Supply current (lcc)		
Part number	Flash size (Kbytes)		Package	16-/ 32-bit timers	Others	ADC	DAC	I/Os	SPI	SAI	I²S	I²C	USART + UART4	USB OTG	CAN 2.0B	SDIO	Ethernet MAC10/ 100	Supply voltage (V)	Lowest power mode (µA)	Run mode (per MHz) (µA)
STM32F439II <sup>2</sup>	2048	256	UFBGA176 LQFP176	12/2	Οv	24	2	140	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	2.5	260
STM32F439NI <sup>2</sup>	2048	256	TFBGA216	12/2	WDG, RTC,	24	2	168	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	2.5	260
STM32F439VI <sup>2</sup>	2048	256	LQFP100	12/2	24-bit down	16	2	82	6	1	2	3	4+4	2	2	1	Yes	1.8 to 3.6	2.5	260
STM32F439ZI <sup>2</sup>	2048	256	LQFP144 WLCSP143	12/2	counter	24	2	114	6	1	2	3	4+4	2	2	1	Yes	1.7 <sup>3</sup> to 3.6	2.5	260

Notes: Operating temperature: -40 to 85 °C for WLCSP packages and -40 to 105 °C for all other packages

1. HS requires an external PHY connected to ULPI interface

2. Crypto/hash processor on STM32F417, STM32F415, STM32F437, STM32F439

3. 1.7 V requires external reset circuitry and the device operates in the 0 to 70 °C temperature range

4. Marked in the table (3+2) means 3 USART and 2 UART. All UARTs have LIN master/slave function. All USARTs have IrDA, ISO 7816, modern control and LIN master/slave functions 5. Available in July 2014

# life.augmented



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