TOSHIBA PHOTOCOUPLER GaAIAs IRED & PHOTO-TRIAC

TLP3064(S)

OFFICE MACHINE HOUSEHOLD USE EQUIPMENT TRIAC DRIVER SOLID STATE RELAY

The TOSHIBA TLP3064(S) consists of a zero voltage crossing turn-on photo-triac optically coupled to a GaAlAs infrared emitting diode in a six lead plastic DIP package.

: 3 mA(Max)

- Peak Off-State Voltage : 600 V(Min)
- Trigger LED Current
- On-State Current
- Isolation Voltage
- UL Recognized
- SEMKO Approved
- : 100 mA(Max) : 5000 Vrms(Min)
- : UL1577,File No.E67349
- : SS EN60065 SS EN60950, File No.9841113
- BSI Approved
- BS EN600550, File No.8385
 BS EN60950, File No.8385
- Option (D4) type

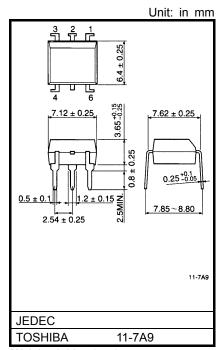
VDE approved: DIN EN60747-5-2 Approved No. 40009302

Maximum operating insulation voltage: 890 V_{PK} Highest permissible over voltage: 8000 V_{PK}

(Note):When a EN60747-5-2 approved type is needed, please designate the "Option (D4)"

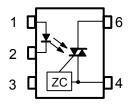
Construction Mechanical Rating

	7.62 mm pitch Standard Type	10.16 mm pitch TLPxxxxF Type
Creepage Distance	7.0 mm (Min)	8.0 mm (Min)
Clearance	7.0 mm (Min)	8.0 mm (Min)
Insulation Thickness	0.5 mm (Min)	0.5 mm (Min)



Weight: 0.39 g (typ.)

Pin Configuration (top view)



1: Anode 2: Cathode 3: N.C. 4:Terminal 1 6:Terminal 2

ZC:Zero-cross Circuit

Absolute Maximum Ratings (Ta = 25°C)

CHARACTERISTIC				IBOL RATING		
	Forward Current	١ _F	30	mA		
Q	Forward Current Derating (Ta \ge 25°C)	∆I _F /°C	-0.3	mA /°C		
LED	Peak Forward Current (100 μ s pulse, 100 pps)	I _{FP}	1	А		
	Reverse Voltage	VR	5	V		
	Junction Temperature	Tj	125	°C		
	Off-State Output Terminal Voltage	V _{DRM}	600	V		
	On-State RMS Current	Ta = 25°C		100	mA	
OR		Ta = 70°C	I _{T(RMS)}	50		
DETECTOR	On-State Current Derating (Ta $\ge 25^{\circ}$ C)	∆I _T /°C	-1.1	mA /°C		
DEJ	Peak On-State Current (100 μ s pulse, 120 pps)	I _{TP}	2	А		
	Peak Nonrepetitive Surge Current (Pw = 10 ms, DC =	ITSM	1.2	А		
	Junction Temperature	Тј	115	°C		
Stor	age Temperature Range	T _{stg}	-55 to 150	°C		
Ope	rating Temperature Range	T _{opr}	-40 to 100	°C		
Lea	d Soldering Temperature (10 s)	T _{sol} 260		°C		
Isola	ation Voltage (AC, 1 min., R.H.≤60%)	BVS	BV _S 5000			

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

(Note 2) Device considered a two terminal device:Pins1, 2 and 3 shorted together and pin 4 and pin 6 shorted together.

Recommended Operating Conditions

CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT
Supply Voltage	V _{AC}	_	_	240	Vac
Forward Current	١ _F	4.5	6	7.5	mA
Peak On-State Current	I _{TP}	_	_	1	А
Operating Temperature	T _{opr}	-10		85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

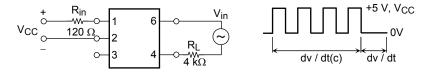
Individual Electrical Characteristics (Ta=25°C)

	CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
	Forward Voltage	VF	I _F = 10 mA	1.2	1.4	1.7	V
LED	Reverse Current	I _R	V _R = 3 V	_	_	10	μA
	Capacitance	CT	V = 0, f = 1 MHz	_	30	_	pF
Ř	Peak Off-State Current	I _{DRM}	V _{DRM} = 600 V	_	10	1000	nA
	Peak On-State Voltage	V _{TM}	I _{TM} = 100 mA	_	_	3.0	V
CTOR	Holding Current	Ι _Η	—	_	0.6	—	mA
DETE(Critical Rate of Rise of Off-State Voltage	dv / dt	Vin = 240 Vrms, Ta = 85°C (Fig.1)	200	500	_	V/µs
	Critical Rate of Rise of Commutating Voltage	dv / dt(c)	Vin = 60 Vrms, I _T = 15 mA (Fig.1)	—	0.2	_	V/µs

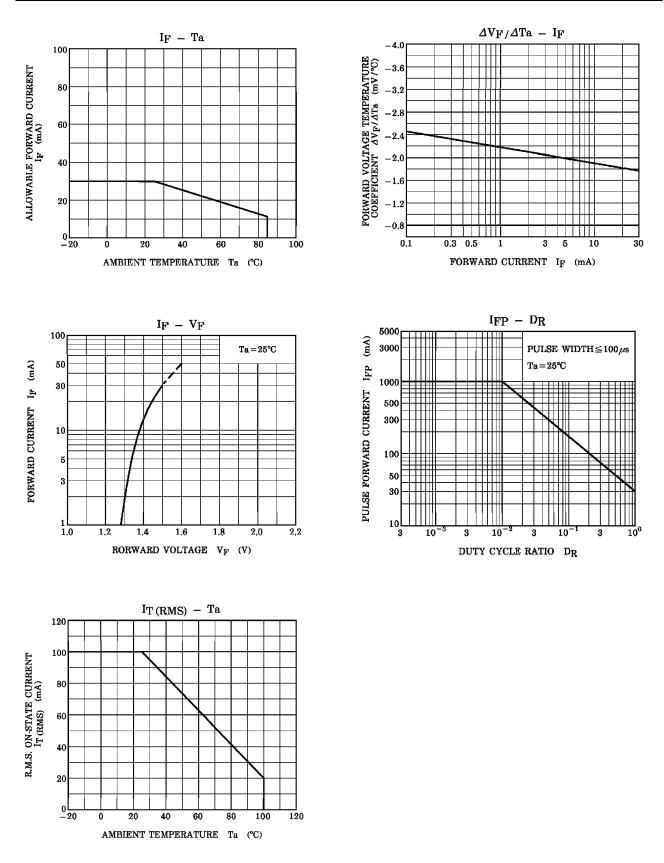
Coupled Electrical Characteristics (Ta=25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT	
Trigger LED Current	I _{FT}	V _T = 3 V ,Resistive Load	_	—	3	mA	
Inhibit Voltage	VIH	I _F = Rated I _{FT}		—	50	V	
Leakage in Inhibited State	IIH	I_F = Rated I_{FT} , V_T = Rated V_{DRM}	_	—	600	μA	
Capacitance (Input to Output)	CS	V _S =0, f=1 MHz	_	0.8	—	pF	
Isolation Resistance	R _S	V _S = 500 V, R.H.≤60%	1×10 ¹²	10 ¹⁴	_	Ω	
		AC, 1 minute	5000	—	_	Vrms	
Isolation Voltage	BVS	AC, 1 second, in oil	_	10000	—	VIIIIS	
		DC, 1 minute, in oil	_	10000	_	Vdc	

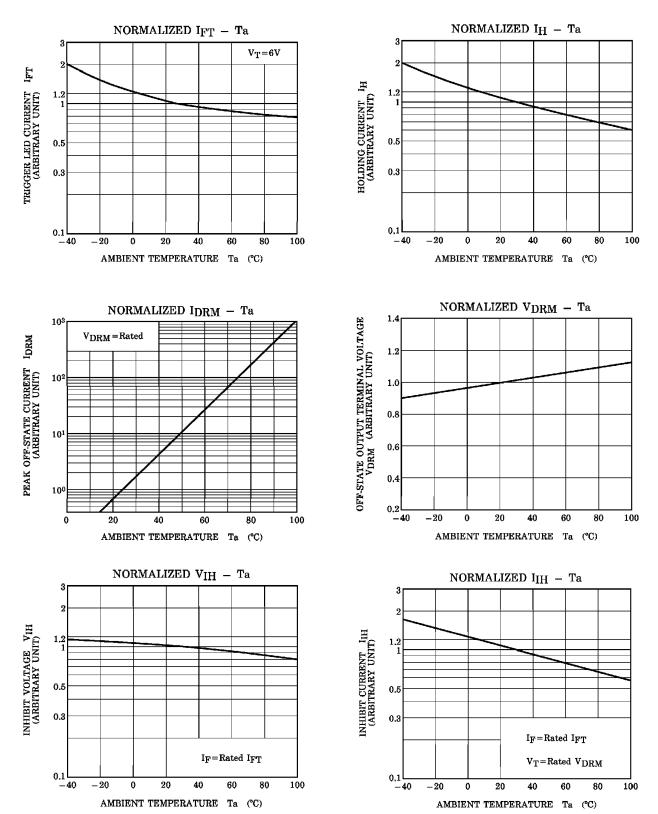
Fig. 1 dv / dt test circuit



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