

BFQ540 NPN wideband transistor Rev. 05 — 21 March 2013

Product data sheet

1. Product profile

1.1 General description

NPN wideband transistor in a SOT89 plastic package.

1.2 Features and benefits

- High gain
- High output voltage
- Low noise

- Gold metallization ensures excellent reliability
- Low thermal resistance.

1.3 Applications

VHF, UHF and CATV amplifiers.

1.4 Quick reference data

Table 1. Quick reference data

| Table I. | Quick reference uata | | | | | |
|------------------|---------------------------|---|------------|-----|-----|------|
| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
| V _{CBO} | collector-base voltage | open emitter | | | 20 | V |
| V _{CES} | collector-emitter voltage | R _{BE} = 0 | | | 15 | V |
| V _{EBO} | emitter-base voltage | open collector | | | 2.5 | V |
| I _C | collector current (DC) | | | | 120 | mA |
| P _{tot} | total power dissipation | $T_s \le 60 \ ^\circ C$ | <u>[1]</u> | | 1.2 | W |
| h _{FE} | DC current gain | $I_C = 40 \text{ mA}; V_{CE} = 8 \text{ V};$ $T_j = 25 \text{ °C}$ | 100 | 120 | 250 | |
| f _T | transition frequency | $I_C = 40 \text{ mA}; V_{CE} = 8 \text{ V};$ f = 1 GHz; T _{amb} = 25 °C | | 9 | | GHz |
| $ s_{21} ^2$ | insertion power gain | $\label{eq:IC} \begin{array}{l} I_{C} = 40 \text{ mA}; V_{CE} = 8 \text{V}; \\ f = 900 \text{MHz}; T_{amb} = 25 ^{\circ}\text{C} \end{array}$ | 12 | 13 | | dB |
| F | noise figure | $I_{C} = 40 \text{ mA}; V_{CE} = 8 \text{ V};$ f = 900 MHz; $\Gamma_{S} = \Gamma_{opt}$ | | 1.9 | 2.4 | dB |
| | | | | | | |

[1] T_s is the temperature at the soldering point of the collector pin.



2. Pinning information

| Table 2. | Pinning | | |
|----------|-------------|--------------------|----------------|
| Pin | Description | Simplified outline | Graphic symbol |
| 1 | emitter | | |
| 2 | collector | | 2 |
| 3 | base | | 3 |

3. Ordering information

| Table 3. Ordering information | | | | | |
|-------------------------------|---------|--|---------|--|--|
| Type number | Package | | | | |
| | Name | Description | Version | | |
| BFQ540 | - | plastic surface-mounted package; collector pad for good heat transfer; 3 leads | SOT89 | | |

4. Marking

| Table 4. | Marking codes | |
|-------------|---------------|--------------|
| Type number | | Marking code |
| BFQ540 | | N4 |

5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

| SymbolParameterConditionsMinMax V_{CBO} collector-base voltageopen emitter20 V_{CES} collector-emitter voltage $R_{BE} = 0$ 15 V_{EBO} emitter-base voltageopen collector2.5 I_C collector current (DC)120 P_{tot} total power dissipation $T_s \le 60 \ ^\circ C$ 1.2 T_{stg} storage temperature-65+150 T_j operating junction temperature175 | | | | , | | |
|--|------------------|-----------------------------------|-------------------------|-----|------|------|
| $\begin{array}{c c c c c c c c c c c c c c c c c c c $ | Symbol | Parameter | Conditions | Min | Max | Unit |
| $\begin{array}{c c c c c c c c } V_{EBO} & emitter-base voltage & open collector & 2.5 \\ I_C & collector current (DC) & 120 \\ P_{tot} & total power dissipation & T_s \leq 60 \ ^{\circ}C & 1.2 \\ T_{stg} & storage temperature & -65 & +150 \\ T_j & operating junction & 175 \\ \end{array}$ | V _{CBO} | collector-base voltage | open emitter | | 20 | V |
| $\begin{array}{c c} I_C & \mbox{collector current (DC)} & 120 \\ \hline P_{tot} & \mbox{total power dissipation} & T_s \leq 60 \ ^{\circ}C & 1.2 \\ \hline T_{stg} & \mbox{storage temperature} & -65 & +150 \\ \hline T_j & \mbox{operating junction} & 175 \\ \end{array}$ | V _{CES} | collector-emitter voltage | $R_{BE} = 0$ | | 15 | V |
| $\begin{array}{c c c c c c c } \hline P_{tot} & total power dissipation & T_s \leq 60 \ ^\circ C & 1.2 \\ \hline T_{stg} & storage temperature & -65 & +150 \\ \hline T_j & operating junction & 175 \end{array}$ | V _{EBO} | emitter-base voltage | open collector | | 2.5 | V |
| T_{stg} storage temperature-65+150 T_j operating junction175 | I _C | collector current (DC) | | | 120 | mA |
| T _j operating junction 175 | P _{tot} | total power dissipation | $T_s \le 60 \ ^\circ C$ | | 1.2 | W |
| , | T _{stg} | storage temperature | | -65 | +150 | °C |
| Composition | Tj | operating junction temperature | | | 175 | °C |

6. Thermal characteristics

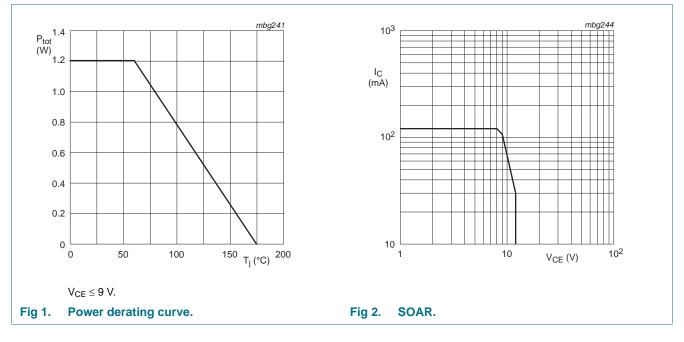
| al resistance from junction | $T_{s} \le 60 \text{ °C}; P_{tot} = 1.2 \text{ W}$ | 95 | 12041 |
|--|---|---|--|
| - | $r_{s} \ge 00^{\circ}$ C, $r_{tot} = 1.2^{\circ}$ W | 90 | K/W |
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BFQ540_5

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BFQ540



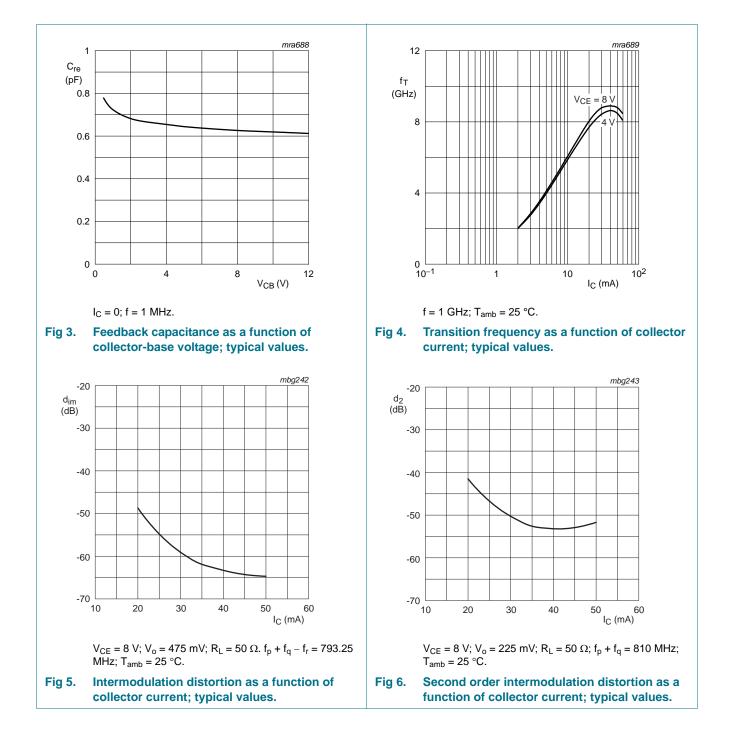
7. Characteristics

Table 7.Characteristics $T_1 = 25 \ \odot$ unless otherwise s

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|----------------------|--|---|------------|-----|-----|------|
| V _{(BR)CBO} | collector-base breakdown voltage | open emitter; $I_C = 10 \ \mu A$; $I_E = 0$ | 20 | | | V |
| V _{(BR)CES} | collector-emitter breakdown voltage | $R_{BE} = 0; I_C = 40 \ \mu A$ | 15 | | | V |
| V _{(BR)EBO} | emitter-base breakdown voltage | $I_{E} = 100 \ \mu A; \ I_{C} = 0$ | 2 | | | V |
| I _{CBO} | collector-base leakage current | $V_{CB} = 8 V; I_E = 0$ | | | 50 | nA |
| I _{EBO} | emitter-base leakage current | $V_{CB} = 1 V; I_C = 0$ | | | 200 | nA |
| h _{FE} | DC current gain | $I_{C} = 40 \text{ mA}; V_{CE} = 8 \text{ V}$ | 100 | 120 | 250 | |
| f _T | transition frequency | I_{C} = 40 mA; V_{CE} = 8 V; f_{m} = 1 GHz | | 9 | | GHz |
| C _e | emitter capacitance | $I_{C} = i_{e} = 0; V_{EB} = 0.5 V;$ f = 1 MHz | | 2 | | рF |
| C _{re} | feedback capacitance | $I_{C} = 0; V_{CE} = 8 V; f = 1 MHz$ | | 0.9 | | pF |
| $ s_{21} ^2$ | insertion power gain | $\label{eq:IC} \begin{array}{l} I_C = 40 \text{ mA}; \ V_{CE} = 8 \text{ V}; \\ f = 900 \text{ MHz}; \ T_{amb} = 25 \ ^\circ\text{C} \end{array}$ | 12 | 13 | | dB |
| Vo | output voltage | | <u>[1]</u> | 500 | | mV |
| | | | [2] | 350 | | mV |
| d ₂ | second order intermodulation distortion | | <u>[3]</u> | | -53 | dB |
| F | noise figure | $I_C = 40 \text{ mA}; \text{ VCE} = 8 \text{ V};$ f = 900 MHz; $\Gamma_S = \Gamma_{opt}$ | | 1.9 | 2.4 | dB |

BFQ540_5 Product data sheet

- [1] $d_{im} = -60 \text{ dB}$ (DIN45004B); $V_{CE} = 8 \text{ V}$; $I_C = 40 \text{ mA}$; $R_L = 50 \Omega$; $V_p = V_o$; $V_q = V_o 6 \text{ dB}$; $V_r = V_o 6 \text{ dB}$; $f_p = 795.25 \text{ MHz}$; $f_q = 803.25 \text{ MHz}$; $f_r = 805.5 \text{ MHz}$; measured at $f_p + f_q f_r = 793.25 \text{ MHz}$.
- [2] $d_{im} = -60 \text{ dB}$ (DIN 45004B); $I_C = 40 \text{ mA}$; $V_{CE} = 8 \text{ V}$; $R_L = 50 \Omega$; $V_p = V_q = V_o$; $f_p = 806 \text{ MHz}$; $f_q = 810 \text{ MHz}$; measured at $2f_p f_q = 802 \text{ MHz}$.
- [3] $I_C = 40$ mA; $V_{CE} = 8$ V; $R_L = 50 \Omega$; $V_p = V_q = 225$ mV; $f_p = 250$ MHz; $f_q = 560$ MHz; measured at $f_p + f_q = 810$ MHz.



NPN wideband transistor

BFQ540

8. Package outline

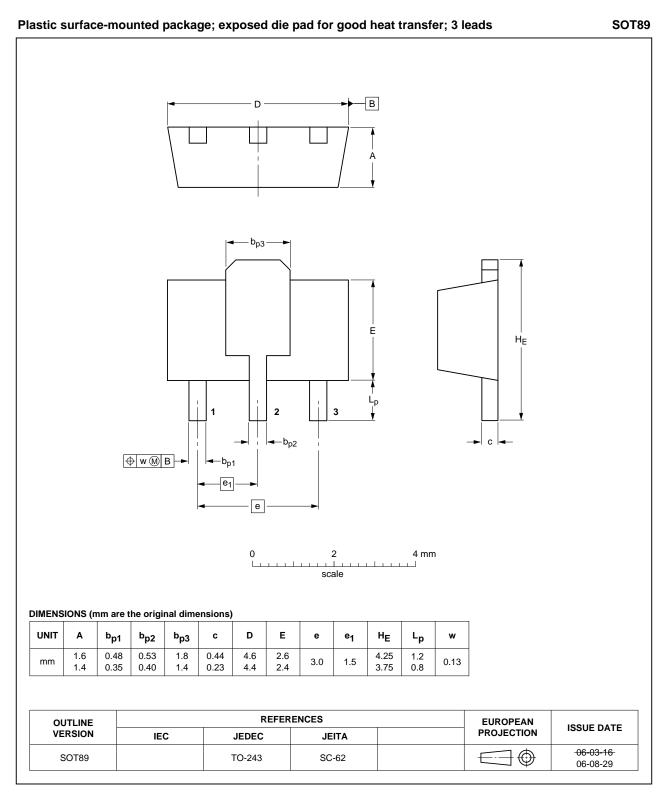


Fig 7. Package outline SOT89 (TO-243).

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9. Revision history

| Table 8. Revision | history | | | |
|------------------------------|-----------------------------|--|---------------------|------------------|
| Document ID | Release date | Data sheet status | Change notice | Supersedes |
| BFQ540 v.5 | 20130321 | Product data sheet | - | BFQ540_N_4 |
| Modifications: | guidelines o Legal texts | of this data sheet has been of NXP Semiconductors. have been adapted to the n utline drawings have been u updated. | ew company name whe | ere appropriate. |
| BFQ540_N_4 | 20070925 | Product data sheet | - | BFQ540_3 |
| BFQ540_3 (9397 750 07064) | 20000523 | Product specification | | BFQ540_2 |
| BFQ540_2 (9397 750 04296) | 19980827 | Product specification | | BFQ540_1 |
| BFQ540_1 | 19950904 | Product specification | | - |
| | | | | |

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|--------------------------------|-------------------------------|---|
| Objective [short] data sheet | Development | This document contains data from the objective specification for product development. |
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BFQ540 5

BFQ540

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BFQ540

12. Contents

| 1 | Product profile 1 |
|------|---------------------------|
| 1.1 | General description 1 |
| 1.2 | Features and benefits 1 |
| 1.3 | Applications 1 |
| 1.4 | Quick reference data 1 |
| 2 | Pinning information 2 |
| 3 | Ordering information 2 |
| 4 | Marking 2 |
| 5 | Limiting values 2 |
| 6 | Thermal characteristics 2 |
| 7 | Characteristics 3 |
| 8 | Package outline 5 |
| 9 | Revision history 6 |
| 10 | Legal information 7 |
| 10.1 | Data sheet status 7 |
| 10.2 | Definitions7 |
| 10.3 | Disclaimers |
| 10.4 | Trademarks 8 |
| 11 | Contact information 8 |
| 12 | Contents 9 |

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