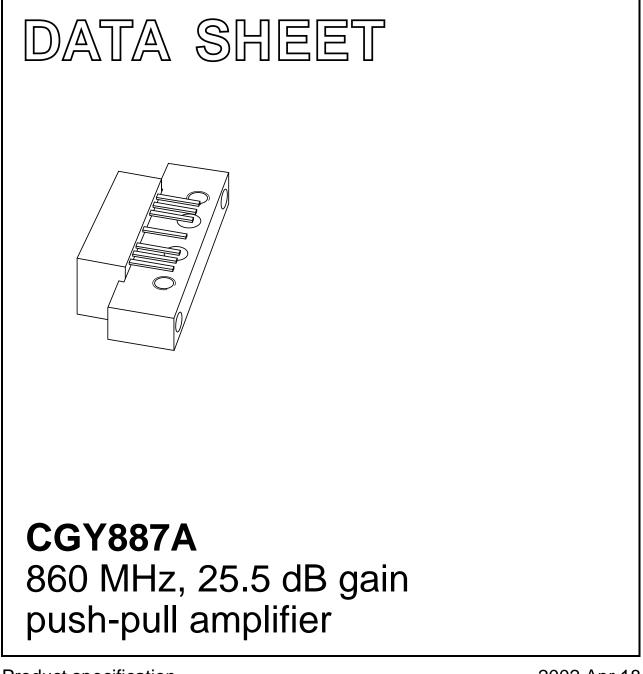
DISCRETE SEMICONDUCTORS



Product specification Supersedes data of 2001 Oct 25 2002 Apr 18



CGY887A

FEATURES

- High gain
- Superior linearity
- Extremely low noise
- Rugged construction
- Gold metallization ensures excellent reliability.

APPLICATIONS

• CATV systems operating in the 40 to 870 MHz frequency range.

DESCRIPTION

Hybrid dynamic range amplifier module in a SOT115J package operating with a voltage supply of 24 V (DC), employing both GaAs and Si dies.

PINNING - SOT115J

PIN	DESCRIPTION	
1	nput	
2	ommon	
3	common	
5	+V _B	
7	common	
8	common	
9	output	

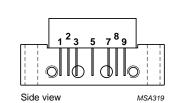


Fig.1 Simplified outline.

QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
G _p	power gain	f = 50 MHz	25.2	25.8	dB
		f = 870 MHz	25.7	27	dB
I _{tot}	total current consumption (DC)	V _B = 24 V	-	240	mA

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER		MAX.	UNIT
V _i RF input voltage		-	75	dBmV
T _{stg}	T _{stg} storage temperature		+100	°C
T _{mb}	operating mounting base temperature	-20	+100	°C

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CHARACTERISTICS

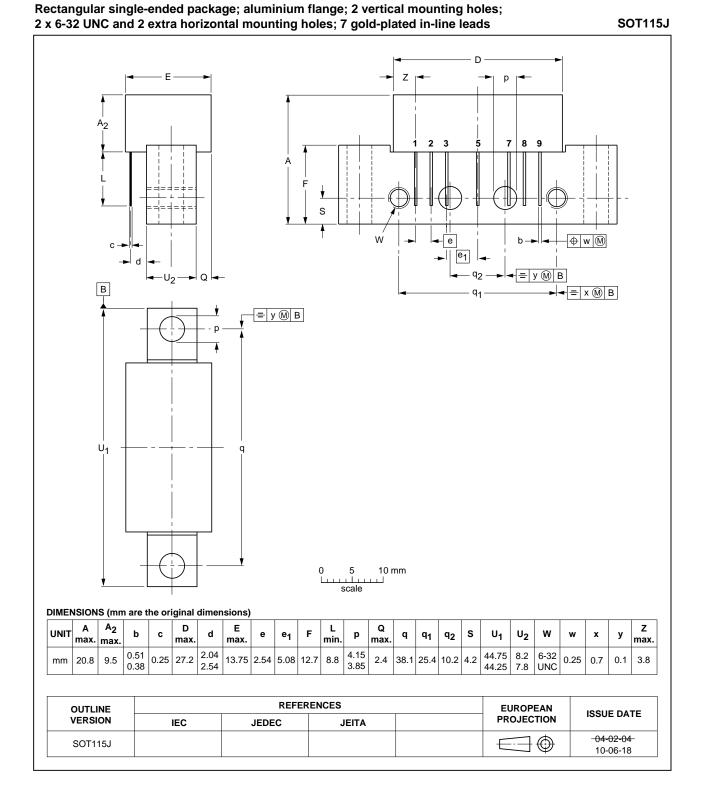
Bandwidth 40 to 870 MHz; V_B = 24 V; T_{case} = 30 °C; Z_S = Z_L = 75 $\Omega.$

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
G _p	power gain	f = 50 MHz	25.2	25.8	dB
		f = 870 MHz	25.7	27	dB
SL	straight line	f = 40 to 870 MHz	0.5	1.4	dB
FL	flatness of frequency response	f = 40 to 870 MHz	_	±0.5	dB
s ₁₁	input return losses	f = 40 to 80 MHz	20	-	dB
		f = 80 to 160 MHz	20	-	dB
		f = 160 to 320 MHz	20	-	dB
		f = 320 to 550 MHz	20	-	dB
		f = 550 to 640 MHz	19	-	dB
		f = 640 to 750 MHz	17	-	dB
		f = 750 to 870 MHz	17	-	dB
s ₂₂	output return losses	f = 40 to 80 MHz	21	-	dB
		f = 80 to 160 MHz	19	-	dB
		f = 160 to 320 MHz	17	-	dB
		f = 320 to 550 MHz	16	-	dB
		f = 550 to 640 MHz	16	-	dB
		f = 640 to 750 MHz	16	-	dB
		f = 750 to 870 MHz	16	-	dB
s ₂₁	phase response	f = 50 MHz	-45	+45	deg
СТВ	composite triple beat	129 channels flat; V _o = 40 dBmV; measured at 745.25 MHz	-	-62	dB
X _{mod}	cross modulation	129 channels flat; $V_0 = 40 \text{ dBmV}$; measured at 55.25 MHz	-	-56	dB
CSO	composite second order distortion	129 channels flat; $V_o = 40 \text{ dBmV}$; measured at 860.5 MHz	-	-59	dB
		129 channels flat; $V_o = 40 \text{ dBmV}$; measured at 150 MHz	-	-69	dB
d ₂	second order distortion	note 1	_	-67	dB
Vo	output voltage	d _{im} = -60 dB; note 2	62	-	dBmV
NF	noise figure	f = 50 MHz	-	5.5	dB
		f = 100 to 870MHz	_	5	dB
I _{tot}	total current consumption (DC)	note 3	-	240	mA

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Notes

- $\begin{array}{ll} \mbox{1.} & \mbox{f}_{p} = 55.25 \mbox{ MHz}; \mbox{ V}_{p} = 50 \mbox{ dBmV}; \\ \mbox{f}_{q} = 805.25 \mbox{ MHz}; \mbox{ V}_{q} = 50 \mbox{ dBmV}; \\ \mbox{measured at } \mbox{f}_{p} + \mbox{f}_{q} = 860.5 \mbox{ MHz}. \end{array}$
- 2. Measured according DIN45004B: $f_p = 851.25 \text{ MHz}; V_p = V_0;$ $f_q = 858.25 \text{ MHz}; V_q = V_o -6 \text{ dB};$ $f_r = 860.25 \text{ MHz}; V_r = V_o -6 \text{ dB};$ measured at $f_p + f_q - f_r = 849.25 \text{ MHz}.$
- 3. The module normally operates at $V_B = 24$ V, but is able to withstand supply transients up to 30 V.



PACKAGE OUTLINE

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DATA SHEET S	TATUS
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DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION	
Objective data sheet	Development	This document contains data from the objective specification for product development.	
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.	
Product data sheet	Production	This document contains the product specification.	

Notes

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Customer notification

This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content, except for package outline drawings which were updated to the latest version.

Contact information

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Printed in The Netherlands

613518/05/pp8

Date of release: 2002 Apr 18

Document order number: 9397 750 09599