

ESD Protection Diodes Silicon Epitaxial Planar

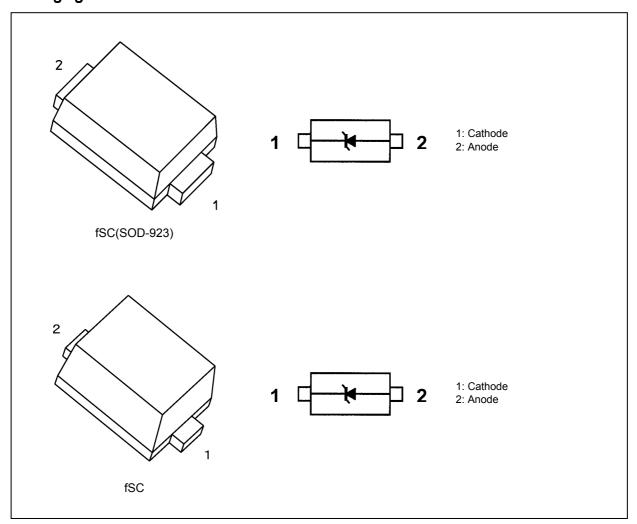
# DF2S24FS

# 1. Applications

· ESD Protection

Note: This product is designed for protection against electrostatic discharge (ESD) and is not intended for any other purpose, including, but not limited to, voltage regulation.

# 2. Packaging and Internal Circuit





#### 3. Absolute Maximum Ratings (Note) (Unless otherwise specified, Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Electrostatic discharge voltage (IEC61000-4-2)(Contact)	V <sub>ESD</sub>	±10	kV
Junction temperature	Tj	150	°C
Storage temperature	T <sub>stg</sub>	-55 to 150	°C

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### 4. Electrical Characteristics (Unless otherwise specified, T<sub>a</sub> = 25°C)

V<sub>RWM</sub>: Working peak reverse

voltage

Vz: Zener voltage

V<sub>BR</sub>: Reverse breakdown voltage

Z<sub>Z</sub>: Dynamic impedance

Iz: Zener current

I<sub>BR</sub>: Reverse breakdown current

I<sub>R</sub>: Reverse current V<sub>C</sub>: Clamp voltage

IPP: Peak pulse current R<sub>DYN</sub>: Dynamic resistance

I<sub>F</sub>: Forward current

V<sub>F</sub>: Forward voltage

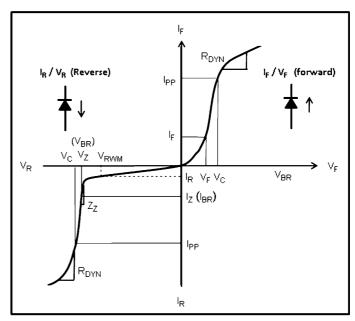


Fig. 4.1 Definitions of Electrical Characteristics

Characteristics	Symbol	Note	Test Condition	Min	Тур.	Max	Unit
Working peak reverse voltage	$V_{RWM}$		_	_	_	19	V
Zener voltage (Reverse breakdown voltage)	V <sub>Z</sub> (V <sub>BR</sub> )		$I_Z = 5 \text{ mA}$ $(I_{BR})$	22.8	24.0	25.6	V
Dynamic impedance	Z <sub>Z</sub>		$I_Z = 5 \text{ mA}$ $(I_{BR})$	_	_	70	Ω
Reverse current	I <sub>R</sub>		V <sub>RWM</sub> = 19 V	_	_	0.5	μΑ
Total capacitance	Ct		V <sub>R</sub> = 0 V, f = 1 MHz	_	8.5	_	pF

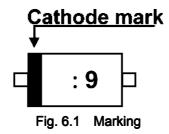


# 5. Guaranteed ESD Protection (Note)

Test Condition	ESD Protection		
IEC61000-4-2 (Contact discharge)	±10 kV		

Note: Criterion: No damage to devices.

# 6. Marking



# 7. Land Pattern Dimensions (for reference only)

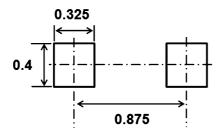


Fig. 7.1 fSC(SOD-923) (unit: mm)

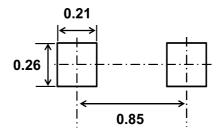
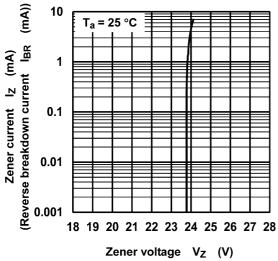


Fig. 7.2 fSC (unit: mm)

# 8. Characteristics Curves (Note)



Zener voltage  $V_Z$  (V) (Reverse breakdown voltage  $V_{BR}$  (V)) Fig. 8.1  $I_Z$  -  $V_Z$  ( $I_{BR}$  -  $V_{BR}$ )

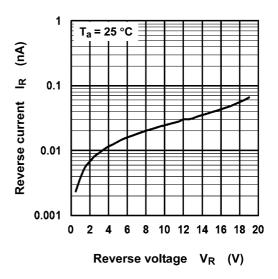
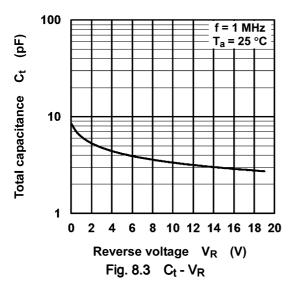


Fig. 8.2 I<sub>R</sub> - V<sub>R</sub>

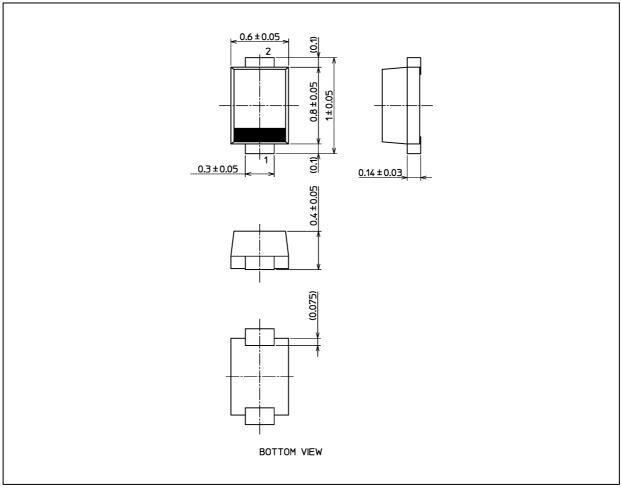


Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



# **Package Dimensions**

Unit: mm



The shapes and dimensions of the package vary, depending on the manufacturing plant. For details, contact the Toshiba sales representative.

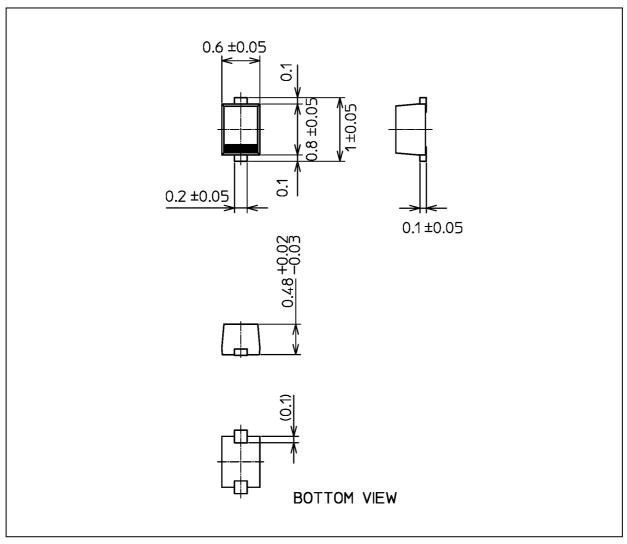
Weight: 0.55 mg (typ.)

Package Name(s)		
TOSHIBA: 1-1AH1A		
Nickname: fSC(SOD-923)		



# **Package Dimensions**

Unit: mm



The shapes and dimensions of the package vary, depending on the manufacturing plant. For details, contact the Toshiba sales representative.

Weight: 0.6 mg (typ.)

	Package Name(s)
TOSHIBA: 1-1L1S	
Nickname: fSC	



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