COMPLIANT HALOGEN

FREE





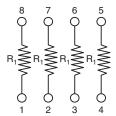
# Molded, 50 mil Pitch, Dual-In-Line Thin Film Resistor, Surface Mount Network





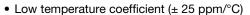
ORN series resistor networks feature four isolated resistors with standard 50 mil pitch lead spacing. The networks feature close TCR tracking and tight ratio tolerance and are ideally suited for unity gain operational amplifier circuitry. The standard resistance offering listed are available for immediate delivery.

### **SCHEMATIC**



### **FEATURES**

- 0.068" (1.73 mm) maximum seated height
- Rugged molded case construction with no internal solder



- JEDEC MS-012 STD variation AA package
- Compliant to RoHS Directive 2002/95/EC
- Halogen-free according to IEC 61249-2-21 definition

### Note

\* Pb containing terminations are not RoHS compliant, exemptions may apply

### **TYPICAL PERFORMANCE**

|      | ABSOLUTE | TRACKING |
|------|----------|----------|
| TCR  | 25       | 5        |
|      | ABSOLUTE | RATIO    |
| TOL. | 0.1      | 0.05     |

| STANDARD RESISTANCE OFFERING (R <sub>1</sub> =) |        |  |
|---|--------|--|
| 49.9 Ω  | 10 kΩ  |  |
| 100 Ω   | 20 kΩ  |  |
| 500 Ω   | 50 kΩ  |  |
| 1 kΩ  | 100 kΩ |  |
| 2 kΩ  | 200 kΩ |  |
| 4.99 kΩ   | 500 kΩ |  |
| 5 kΩ  |        |  |

#### Note

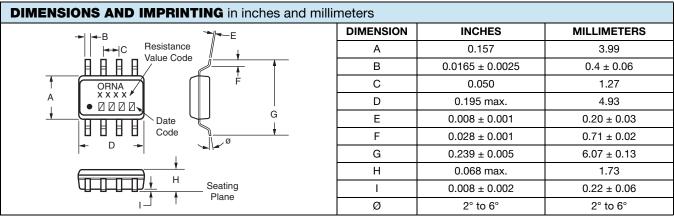
· Consult factory for additional values and schematics

| STANDARD ELECTRICAL SPECIFICATIONS |  |                     |  |
|------------------------------------|--|---------------------|--|
| TEST                               | SPECIFICATIONS                             | CONDITIONS          |  |
| Material                           | Passivated nichrome                        | -                   |  |
| Pin/Lead Number                    | 8  | -                   |  |
| Resistance Range                   | 33 $\Omega$ to 500 k $\Omega$ per resistor | -                   |  |
| TCR: Absolute                      | ± 25 ppm/°C                                | - 55 °C to + 125 °C |  |
| TCR: Tracking                      | ± 5 ppm/°C                                 | - 55 °C to + 125 °C |  |
| Tolerance: Absolute                | ± 0.05 % to ± 1.0 %                        | + 25 °C             |  |
| Tolerance: Ratio                   | ± 0.01 % to ± 0.5 %                        | + 25 °C             |  |
| Power Rating: Resistor             | 100 mW                                     | Maximum at + 70 °C  |  |
| Power Rating: Package              | 400 mW                                     | Maximum at + 70 °C  |  |
| Stability: Absolute                | ΔR ± 0.05 %                                | 2000 h at + 70 °C   |  |
| Stability: Ratio                   | $\Delta R \pm 0.015 \%$                    | 2000 h at + 70 °C   |  |
| Voltage Coefficient                | 0.1 ppm/V (typical)                        | -                   |  |
| Working Voltage                    | 100 V max. not to exceed √P x R            | -                   |  |
| Operating Temperature Range        | - 55 °C to + 125 °C                        | -                   |  |
| Storage Temperature Range          | - 55 °C to + 150 °C                        | -                   |  |
| Noise                              | < - 30 dB                                  |                     |  |
| Thermal EMF                        | 0.08 μV/°C                                 |                     |  |
| Shelf Life Stability: Absolute     | ΔR ± 0.01 %                                | 1 year at + 25 °C   |  |
| Shelf Life Stability: Ratio        | ΔR ± 0.002 %                               | 1 year at + 25 °C   |  |

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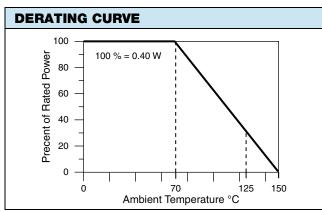
## Vishay Dale Thin Film

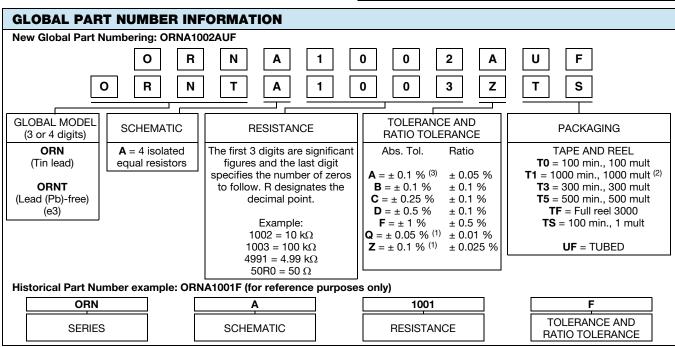


#### Note

· Marking - Vishay symbol, part number from ordering information

| MECHANICAL SPECIFICATIONS          |                     |  |
|------------------------------------|---------------------|--|
| Resistive Element                  | Passivated nichrome |  |
| Substrate Material                 | Silicon             |  |
| Body                               | Molded epoxy        |  |
| Terminals                          | Copper alloy        |  |
| Lead (Pb)-free Option              | 100 % matte tin     |  |
| Tin Lead Option                    | Sn90                |  |
| Tin Lead and Lead (Pb)-free Finish | Plated              |  |





## Notes

- 1) Tol. available 1K and up
- (2) Preferred packaging code
- $^{(3)}$  Ratio tolerance available 250  $\Omega$  and up



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Vishay

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