# Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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# RJK0331DPB

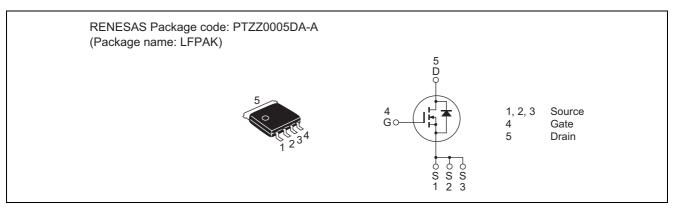
Silicon N Channel Power MOS FET Power Switching

> REJ03G1640-0400 Rev.4.00 Apr 10, 2008

### Features

- High speed switching
- Capable of 4.5 V gate drive
- Low drive current
- High density mounting
- Low on-resistance
  - $R_{DS(on)} = 2.6 \text{ m}\Omega \text{ typ.} (at V_{GS} = 10 \text{ V})$
- Pb-free

### Outline



## **Absolute Maximum Ratings**

			$(Ta = 25^{\circ}C)$
Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	30	V
Gate to source voltage	V <sub>GSS</sub>	±20	V
Drain current	ID	40	A
Drain peak current	Note1 I <sub>D(pulse)</sub>	160	A
Body-drain diode reverse drain current	I <sub>DR</sub>	40	A
Avalanche current	I <sub>AP</sub> Note 2	20	A
Avalanche energy	E <sub>AR</sub> Note 2	40	mJ
Channel dissipation	Pch Note3	50	W
Channel to Case Thermal Resistance	θch-C	2.5	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1.  $PW \leq 10 \ \mu s, \ duty \ cycle \leq 1\%$ 

2. Value at Tch = 25°C, Rg  $\ge$  50  $\Omega$ 

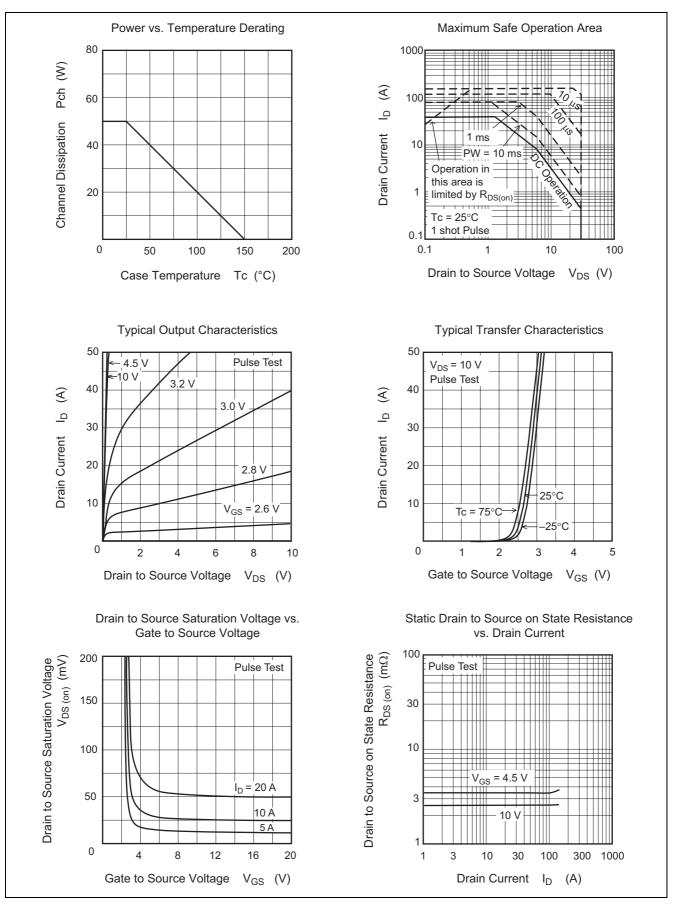
3. Tc = 25°C

## **Electrical Characteristics**

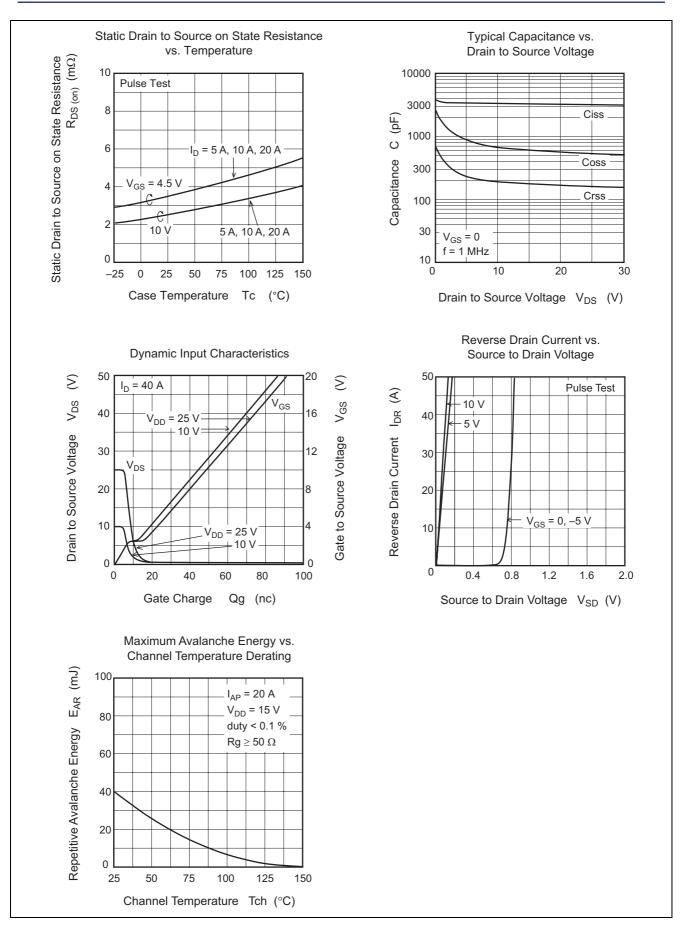
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V <sub>(BR)DSS</sub>	30	—	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_	_	±0.1	μΑ	$V_{GS} = \pm 20 \text{ V}, \text{ V}_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>		—	1	μΑ	$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0$
Gate to source cutoff voltage	V <sub>GS(off)</sub>	1.2	—	2.5	V	$V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$
Static drain to source on state	R <sub>DS(on)</sub>		2.6	3.4	mΩ	$I_D = 20 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note4}}$
resistance	R <sub>DS(on)</sub>		3.5	4.9	mΩ	$I_D = 20 \text{ A}, V_{GS} = 4.5 \text{ V}^{Note4}$
Forward transfer admittance	y <sub>fs</sub>	_	80	_	S	$I_D = 20 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note4}}$
Input capacitance	Ciss	_	3380	_	pF	$V_{DS} = 10 \text{ V},  V_{GS} = 0,$ f = 1 MHz
Output capacitance	Coss	_	660	_	pF	
Reverse transfer capacitance	Crss		190		pF	
Gate Resistance	Rg		0.6		Ω	
Total gate charge	Qg		22		nC	$V_{DD} = 10 \text{ V}, \text{ V}_{GS} = 4.5 \text{ V},$ $I_D = 40 \text{ A}$
Gate to source charge	Qgs		7.8		nC	
Gate to drain charge	Qgd		4.8		nC	
Turn-on delay time	t <sub>d(on)</sub>		5.8		ns	
Rise time	tr		3.9		ns	
Turn-off delay time	t <sub>d(off)</sub>		45		ns	
Fall time	t <sub>f</sub>		4.6		ns	
Body-drain diode forward voltage	V <sub>DF</sub>	_	0.82	1.07	V	$I_F = 40 \text{ A}, V_{GS} = 0^{Note4}$
Body–drain diode reverse recovery time	t <sub>rr</sub>	—	30	_	ns	$I_F = 40 \text{ A}, V_{GS} = 0$ $di_F/dt = 100 \text{ A}/\mu \text{s}$
Body-drain diode reverse recovery charge	Qrr	_	26	—	nC	1

Notes: 4. Pulse test

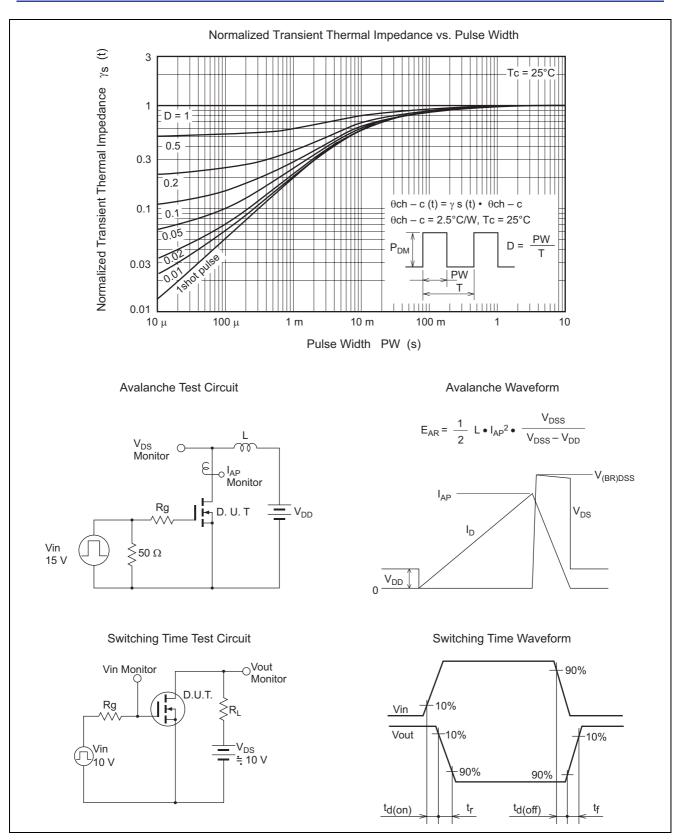
#### **Main Characteristics**



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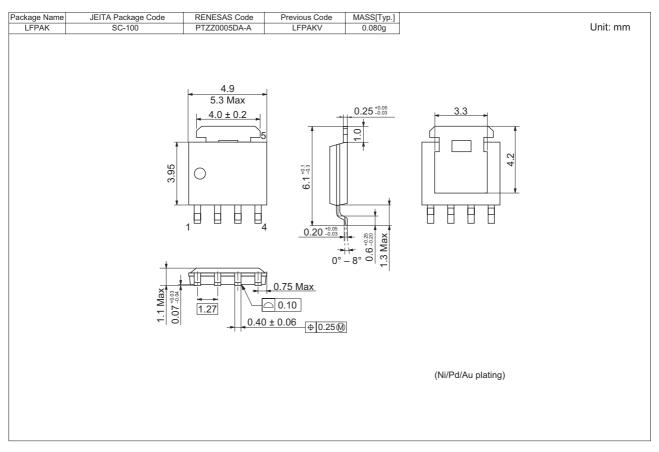


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### **Package Dimensions**



### **Ordering Information**

Part No.	Quantity	Shipping Container
RJK0331DPB-00-J0	2500 pcs	Taping

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