Unit: mm

TOSHIBA Field-Effect Transistor Silicon N-Channel MOS Type (U-MOSIV)

SSM3K315T

High-Speed Switching Applications

4.5-V drive
 Low ON-resistance : R_{on} = 41.5 mΩ (max) (@V_{GS} = 4.5 V)

: R_{on} = 27.6 m Ω (max) (@V_{GS} = 10 V)

Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating	Unit	
Drain-Source voltage		V_{DSS}	30	V	
Gate-Source voltage		V_{GSS}	±20	V	
Drain current	DC	I _D (Note	1) 6.0	A	
	Pulse	I _{DP} (Note	1) 12.0		
Drain power dissipation		P _D (Note	1) 700	mW	
		t = 10) s 1250		
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55 to 150	°C	

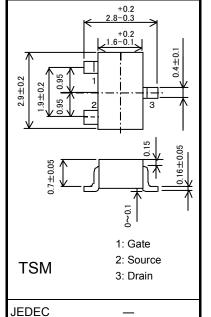
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: The junction temperature should not exceed 150°C during use.

Note 2: Mounted on an FR4 board.

(25.4 mm × 25.4 mm × 1.6 mm, Cu Pad: 645 mm²)



2-3S1A

Weight: 10 mg (typ.)

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Electrical Characteristics (Ta = 25°C)

Char	acteristic	Symbol	Test Conditions		Min	Тур.	Max	Unit
Drain-Source breakdown voltage		V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$ $I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$		30	_	_	V
		V (BR) DSX			15	_	_	
Drain cut-off curre	ent	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V		_	_	1	μА
Gate leakage cur	leakage current I_{GSS} $V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$			_	_	±0.1	μА	
Gate threshold vo	oltage	V _{th}	$V_{DS} = 5 \text{ V}, I_{D} = 1 \text{ mA}$		1.3	_	2.5	V
Forward transfer	admittance	Y _{fs}	V _{DS} = 5 V, I _D = 4 A	(Note 3)	11.5	23.0	_	S
Drain-source ON-resistance		R _{DS (ON)}	I _D = 4.0 A, V _{GS} = 10 V	(Note 3)	_	20.5	27.6	mΩ
			I _D = 2.0 A, V _{GS} = 4.5 V	(Note 3)	_	27.0	41.5	
Input capacitance		C _{iss}	V _{DS} = 15 V, V _{GS} = 0 V, f = 1 MHz		_	450	_	pF
Output capacitance		Coss			_	120	_	
Reverse transfer capacitance		C _{rss}			_	77	_	
Total Gate Charge		Qg	V _{DS} = 15 V, I _D =6.0 A, V _{GS} = 10 V			10.1		nC
Gate-Source Charge		Q _{gs}			_	7.6	_	
Gate-Drain Charge		Q _{gd}			_	2.5	_	
Switching time	Turn-on time	t _{on}	$V_{DD} = 15 \text{ V}, I_D = 2.0 \text{ A},$ $V_{GS} = 0 \text{ to } 4.5 \text{ V}, R_G = 10 \Omega$			21		ns
	Turn-off time	t _{off}			_	15	_	
Drain-Source forward voltage		V _{DSF}	I _D = -6.0 A, V _{GS} = 0 V	(Note 3)	-	-0.85	-1.2	٧

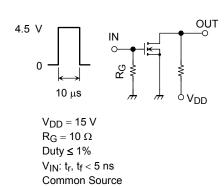
Note 3: Pulse test

Start of commercial production 2008-09

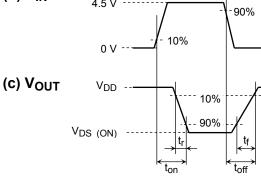
Switching Time Test Circuit

 $Ta = 25^{\circ}C$

(a) Test Circuit

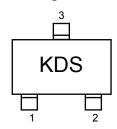


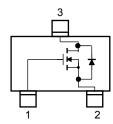
(b) V_{IN}



Marking

Equivalent Circuit (top view)





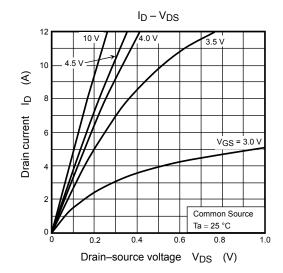
Handling Precaution

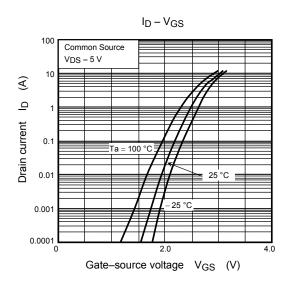
When handling individual devices that are not yet mounted on a circuit board, make sure that the environment is protected against electrostatic discharge. Operators should wear antistatic clothing, and containers and other objects that come into direct contact with devices should be made of antistatic materials.

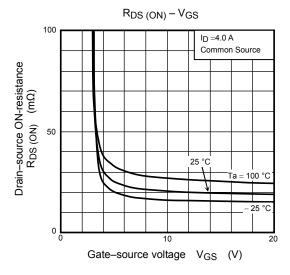
Usage Consideration

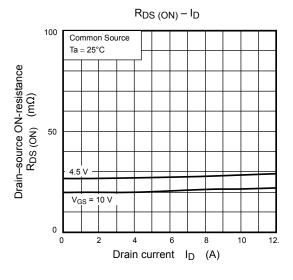
Let V_{th} be the voltage applied between gate and source that causes the drain current (I_D) to be low (1 mA for the SSM3K315T). Then, for normal switching operation, $V_{GS(on)}$ must be higher than V_{th} , and $V_{GS(off)}$ must be lower than V_{th} . This relationship can be expressed as: $V_{GS(off)} < V_{th} < V_{GS(on)}$.

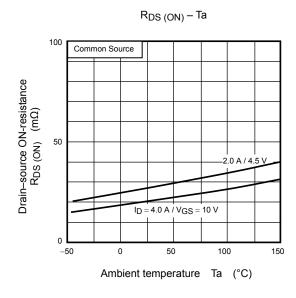
Take this into consideration when using the device

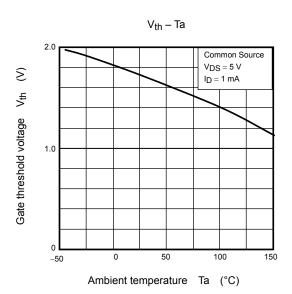


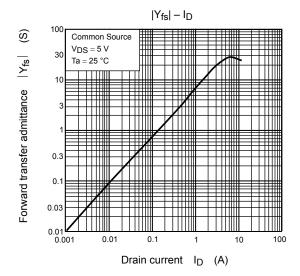


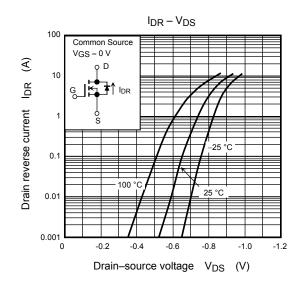


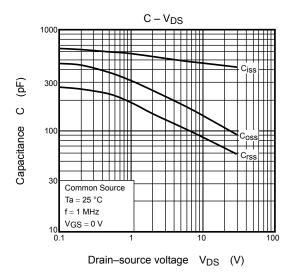


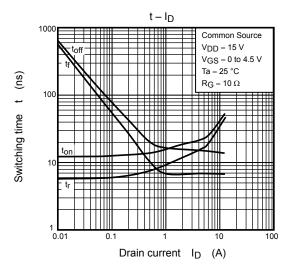


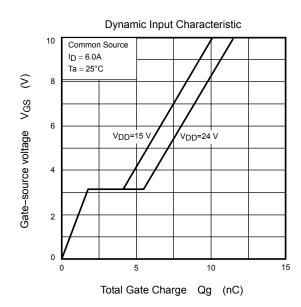


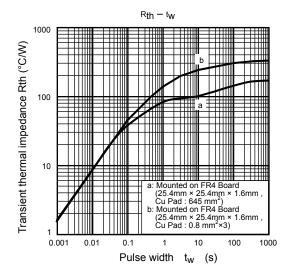


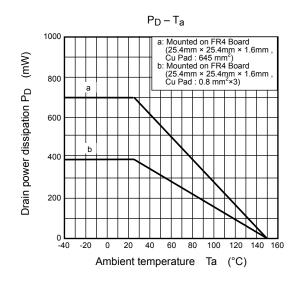












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