TOSHIBA Field Effect Transistor Silicon P Channel MOS Type (U-MOS V)

TPC8118

Notebook PC Applications

- Small footprint due to small and thin package
- Low drain-source ON-resistance: $RDS(ON) = 5.5 \text{ m}\Omega \text{ (typ.)}$
- High forward transfer admittance: $|Y_{fs}| = 36 \text{ S (typ.)}$
- Low leakage current: $IDSS = -10 \mu A (max) (VDS = -30 V)$
- Enhancement mode: $V_{th} = -0.8 \text{ to } -2.0 \text{ V (V}_{DS} = -10 \text{ V}, I_D = -1 \text{ mA)}$

Absolute Maximum Ratings (Ta = 25°C)

| Characteri | stics | Symbol | Rating | Unit |
|-------------------------|-----------------------------|------------------|------------|--|
| Drain-source voltage | | V_{DSS} | -30 | $\langle \rangle$ |
| Drain-gate voltage (Ro | _{SS} = 20 kΩ) | V_{DGR} | -30 | \ \ V |
| Gate-source voltage | | V _{GSS} | ±20 | \\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\ |
| Drain current | DC (Note 1) | ΙD | -13 | A |
| | Pulse (Note 1) | I _{DP} | -52 | A |
| Drain power dissipatio | n (t = 10 s) (Note 2a) | P _D | 1.9 | W |
| Drain power dissipatio | n (t = 10 s) (Note 2b) | PD | 1.0 | X |
| Single pulse avalanche | e energy (Note 3) | EAS | 110 | mJ |
| Avalanche current | | ((IAR)) | -13 | A |
| Repetitive avalanche (N | energy lote 2a) (Note 4) | EAR | 0.030 |)E |
| Channel temperature | | √7 _{ch} | 150 | ပ္) |
| Storage temperature r | ange | T _{stg} | -55 to 150 | °C |

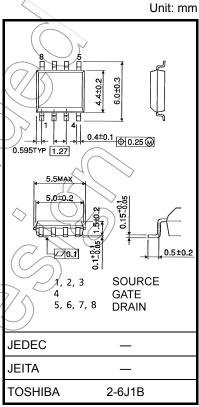
Note 1, Note 2, Note 3 and Note 4: See the next page.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating

temperature/current/voltage, etc.) are within the absolute maximum ratings.

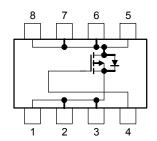
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

This transistor is an electrostatic-sensitive device. Handle with care.



Weight: 0.080 g (typ.)

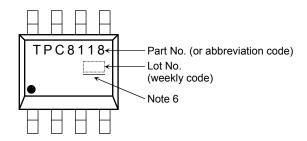
Circuit Configuration



Thermal Characteristics

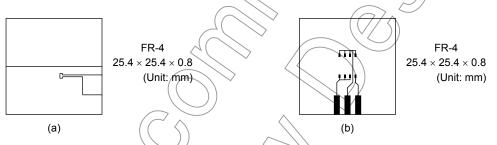
| Characteristics | Symbol | Max | Unit |
|---|------------------------|------|------|
| Thermal resistance, channel to ambient (t = 10 s) (Note 2a) | R _{th (ch-a)} | 65.8 | °C/W |
| Thermal resistance, channel to ambient (t = 10 s) (Note 2b) | R _{th (ch-a)} | 125 | °C/W |

Marking (Note 5)



Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: (a) Device mounted on a glass-epoxy board (b) Device mounted on a glass-epoxy board (b)



Note 3: $V_{DD} = -24 \text{ V}$, $T_{ch} = 25^{\circ}\text{C}$ (initial), $L = 500 \text{ }\mu\text{H}$, $R_G = 25 \Omega$, $L_{AR} = -13 \text{ A}$

Note 4: Repetitive rating: pulse width limited by maximum channel temperature

Note 5: • on the lower left of the marking indicates Pin 1.

* Weekly code: (Three digits)

Week of manufacture
(01 for first week of year, continuing up to 52 or 53)

Year of manufacture
(The last digit of the calendar year)

Note 6: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Pb]]/INCLUDES > MCV

Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

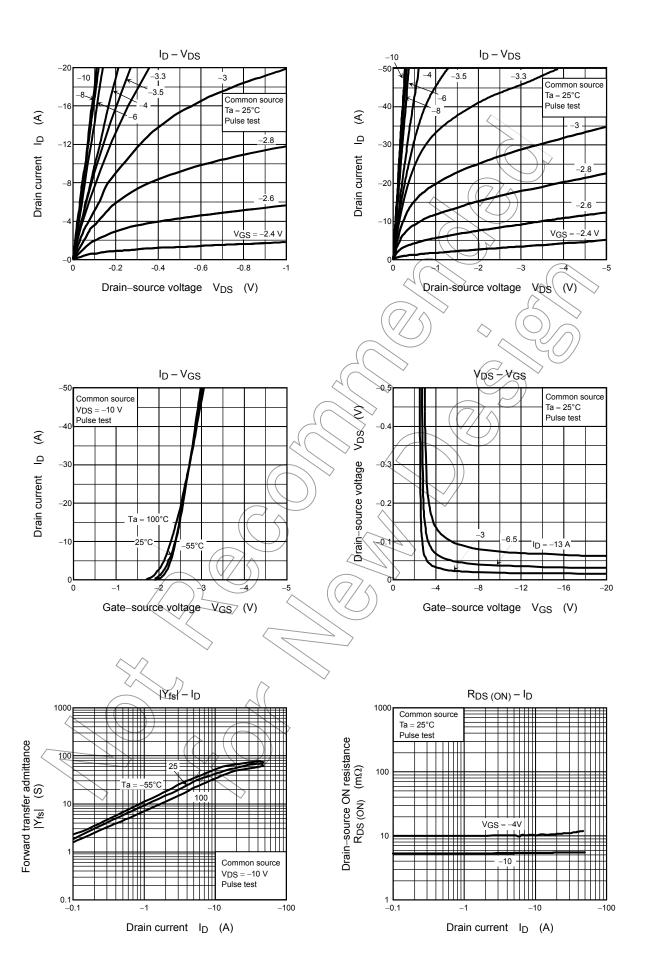
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Electrical Characteristics (Ta = 25°C)

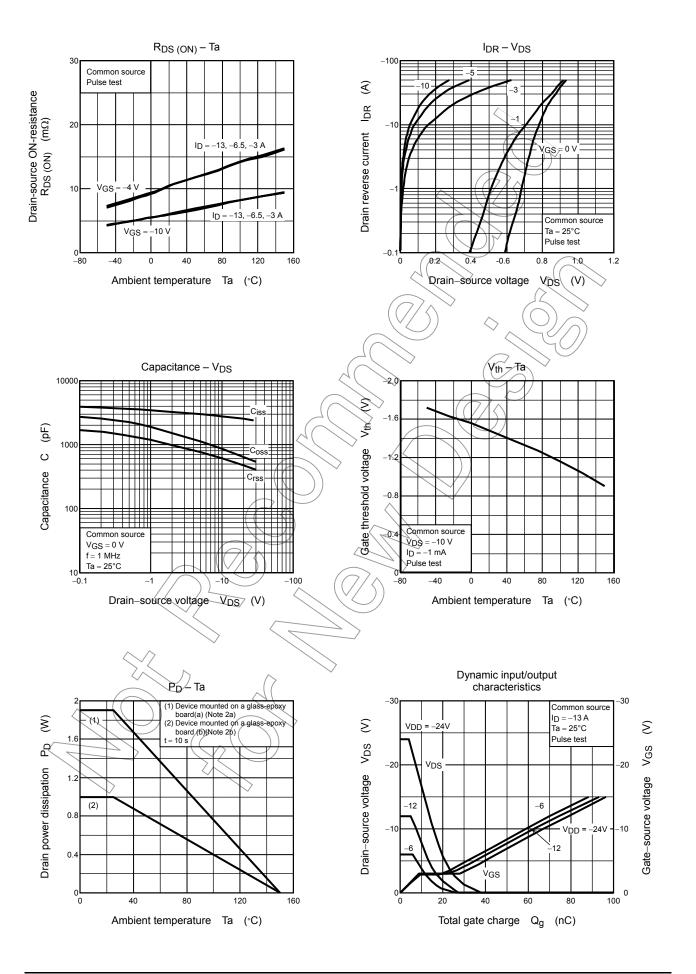
| Cha | racteristics | Symbol | Test Condition | Min | Тур. | Max | Unit |
|-------------------------------------|----------------|----------------------|---|---------------|------|------------|------|
| Gate leakage curre | ent | I _{GSS} | $V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$ | _ | _ | ±100 | nA |
| Drain cut-OFF cur | rent | I _{DSS} | $V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}$ | _ | _ | -10 | μА |
| Drain-source breakdown voltage | | V _{(BR)DSS} | $I_D = -10 \text{ mA}, V_{GS} = 0 \text{ V}$ | -30 | _ | _ | V |
| Dialii-Source brea | Kuowii voltage | V _{(BR)DSX} | $I_D = -10 \text{ mA}, V_{GS} = 20 \text{ V}$ | =13 | | v | |
| Gate threshold vol | tage | V _{th} | $V_{DS} = -10 \text{ V}, I_D = -1 \text{ mA}$ | 0.8 |) }_ | -2.0 | V |
| Drain-source ON-resistance | | D | $V_{GS} = -4 \text{ V}, I_D = -6.5 \text{ A}$ |) | 10 | 15 | - mΩ |
| | | R _{DS} (ON) | $V_{GS} = -10 \text{ V}, I_D = -6.5 \text{ A}$ | \rightarrow | 5.5 | 7.0 | |
| Forward transfer a | dmittance | Y _{fs} | $V_{DS} = -10 \text{ V}, I_D = -6.5 \text{ A}$ | 18 | 36 | _ | S |
| Input capacitance | | C _{iss} | | _ | 2700 | _ | |
| Reverse transfer capacitance | | C _{rss} | $V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$ | _ | 600 | _ | pF |
| Output capacitanc | e | Coss | | | 860 | \nearrow | |
| | Rise time | t _r | V _{GS} -10 + V _G -6.5 A | -(| 9 | > _ | |
| Cuddahin a tima | Turn-on time | t _{on} | | | 18 | _ | |
| Switching time | Fall time | tf | | 180 | _ | ns | |
| | Turn-off time | t _{off} | V _{DD} ≈ 15 V Duty ≤ 1%, t _w = 10 μs |) — | 460 | _ | |
| Total gate charge (gate-source plus | gate-drain) | Qg | V _{DD} ≈ -24 V, V _{GS} ≥ -10 V, | _ | 65 | _ | |
| Gate-source charge 1 | | Q_{gs1} | I _D = -13 A | _ | 10 | _ | nC |
| Gate-drain ("miller") charge | | Qgd | | _ | 20 | _ | |

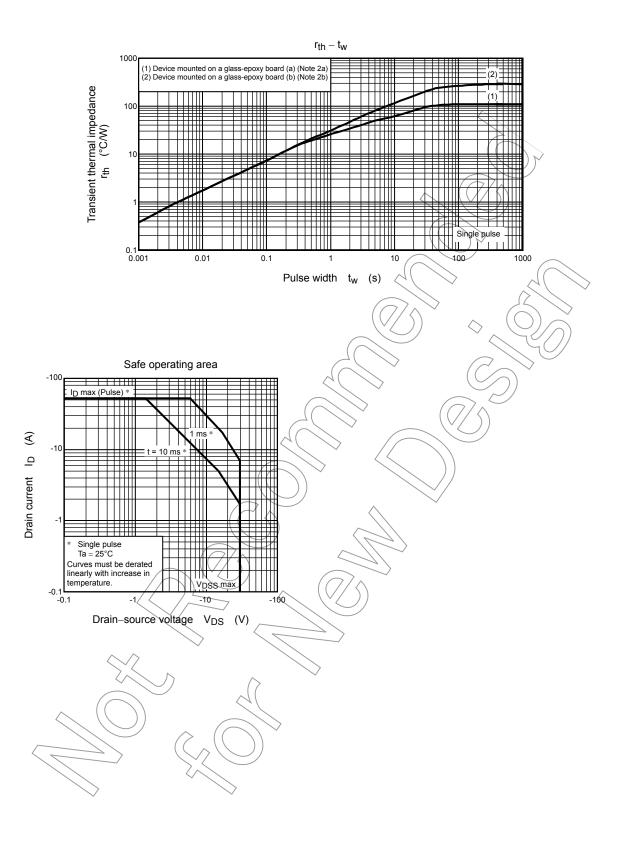
Source-Drain Ratings and Characteristics (Ta = 25°C)

| Charac | cteristics | Symbol | Test Condition | Min | Тур. | Max | Unit |
|-----------------------|----------------|------------------|--|-----|------|-----|------|
| Drain reverse current | Pulse (Note 1) | I _{DRP} | _ | | | -52 | Α |
| Forward voltage (dio | de) | VDSF | $1_{DR} = -13 \text{ A}, V_{GS} = 0 \text{ V}$ | _ | _ | 1.2 | V |



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