

TOSHIBA Field Effect Transistor with Built-in Schottky Barrier Diode  
Silicon N-Channel MOS Type (U-MOS V-H)

# TPC8A03-H

High Efficiency DC-DC Converter Applications

Notebook PC Applications

Portable Equipment Applications

- Built-in schottky barrier diode  
Low forward voltage:  $V_{DSF} = -0.6$  V (max)
- High-speed switching
- Small gate charge:  $Q_{SW} = 8.4$  nC (typ.)
- Low drain-source ON-resistance:  $R_{DS(ON)} = 4.1$  m $\Omega$  (typ.)
- High forward transfer admittance:  $|Y_{fs}| = 54$  S (typ.)
- Low leakage current:  $I_{DSS} = 100$   $\mu$ A (max) ( $V_{DS} = 30$  V)
- Enhancement mode:  $V_{th} = 1.3$  to  $2.3$  V ( $V_{DS} = 10$  V,  $I_D = 1$  mA)

## Absolute Maximum Ratings ( $T_a = 25^\circ\text{C}$ )

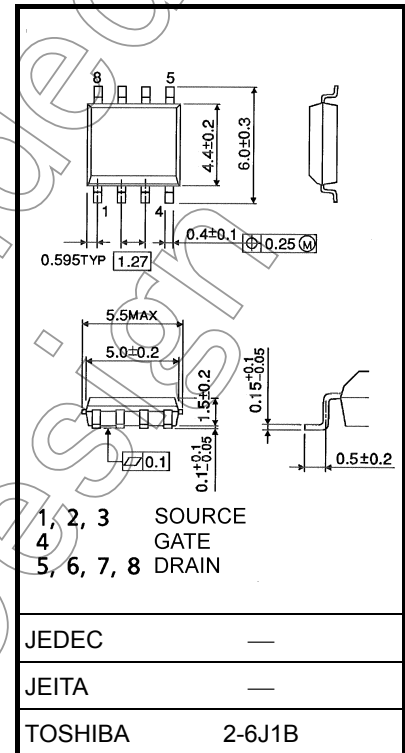
Characteristic		Symbol	Rating	Unit
Drain-source voltage		$V_{DSS}$	30	V
Drain-gate voltage ( $R_{GS} = 20$ k $\Omega$ )		$V_{DGR}$	30	V
Gate-source voltage		$V_{GSS}$	$\pm 20$	V
Drain current	DC (Note 1)	$I_D$	17	A
	Pulsed (Note 1)	$I_{DP}$	68	
Drain power dissipation ( $t = 10$ s) (Note 2a)		$P_D$	1.9	W
Drain power dissipation ( $t = 10$ s) (Note 2b)		$P_D$	1.0	W
Single-pulse avalanche energy (Note 3)		$E_{AS}$	188	mJ
Avalanche current		$I_{AR}$	17	A
Repetitive avalanche energy ( $T_c = 25^\circ\text{C}$ ) (Note 4)		$E_{AR}$	0.108	mJ
Channel temperature		$T_{ch}$	150	$^\circ\text{C}$
Storage temperature range		$T_{stg}$	-55 to 150	$^\circ\text{C}$

Note: For Notes 1 to 4, refer to the next page.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

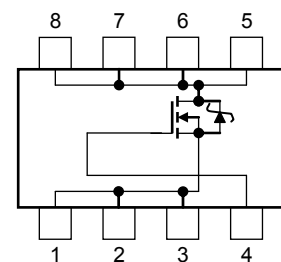
This transistor is an electrostatic-sensitive device. Handle with care.

Unit: mm



Weight: 0.085g (typ.)

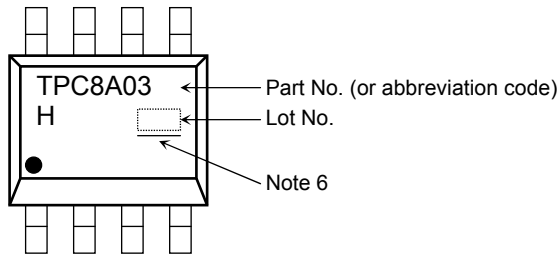
## Circuit Configuration



## Thermal Characteristics

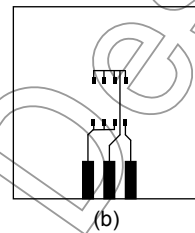
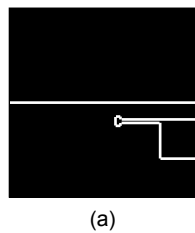
Characteristic	Symbol	Max	Unit
Thermal resistance, channel to ambient (t = 10 s) (Note 2a)	$R_{th}(ch-a)$	65.8	°C/W
Thermal resistance, channel to ambient (t = 10 s) (Note 2b)	$R_{th}(ch-a)$	125	°C/W

## Marking (Note 5)



Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: (a) Device mounted on a glass-epoxy board (a) (b) Device mounted on a glass-epoxy board (b)

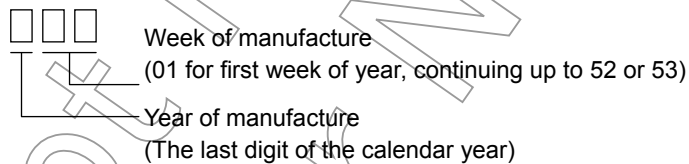


Note 3:  $V_{DD} = 24\text{ V}$ ,  $T_{ch} = 25^\circ\text{C}$  (initial),  $L = 0.5\text{ mH}$ ,  $R_G = 25\ \Omega$ ,  $I_{AR} = 17\text{ A}$

Note 4: Repetitive rating: pulse width limited by maximum channel temperature

Note 5:

\* Weekly code: (Three digits)



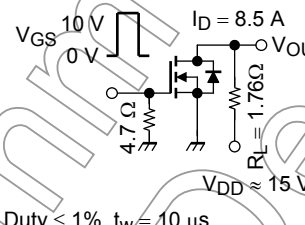
Note 6: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Pb]]/INCLUDES > MCV

Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

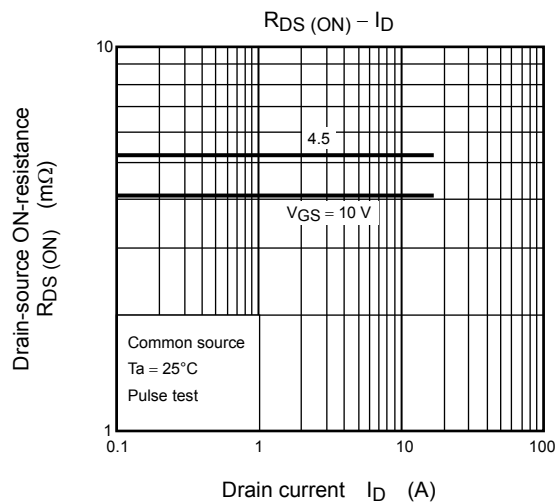
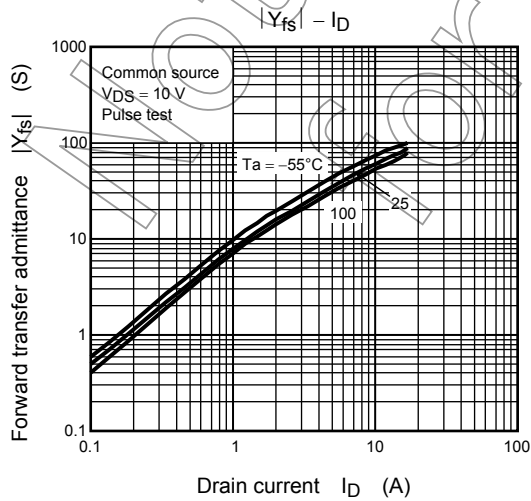
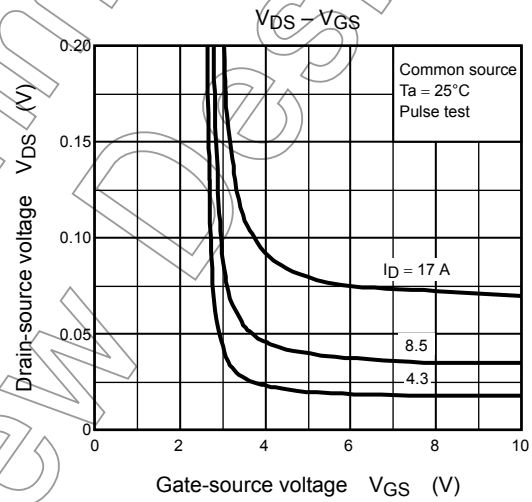
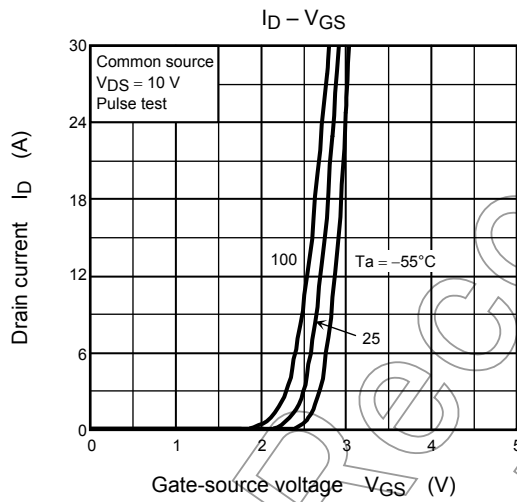
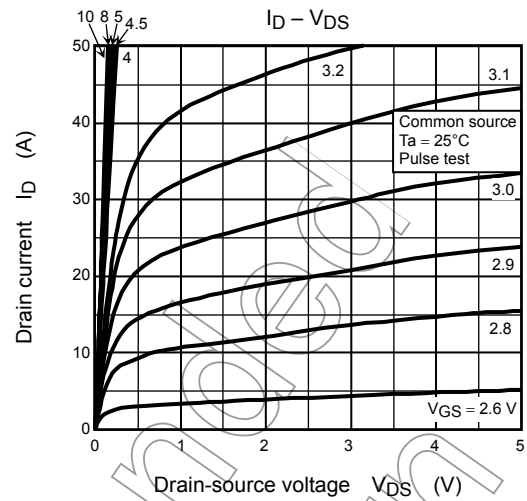
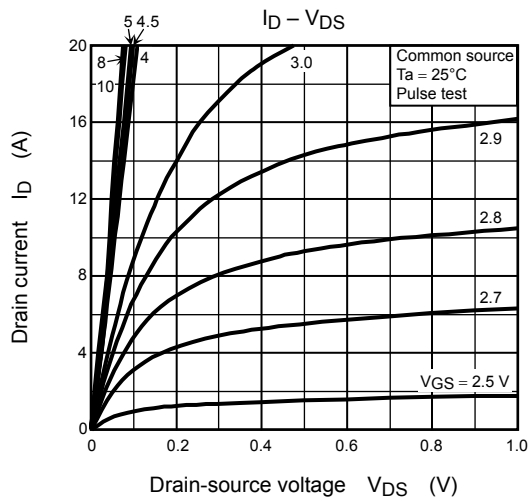
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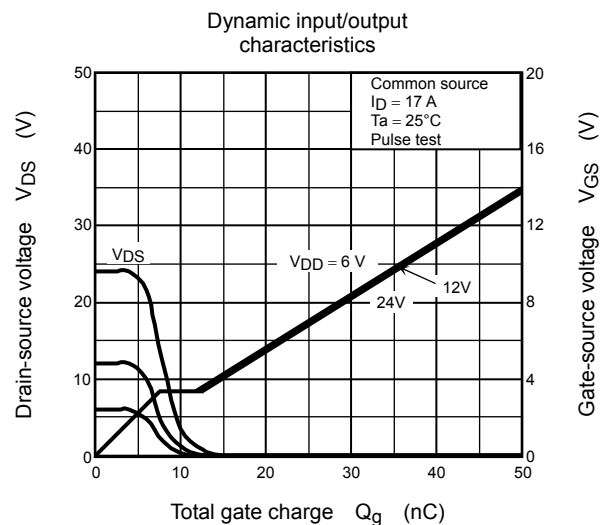
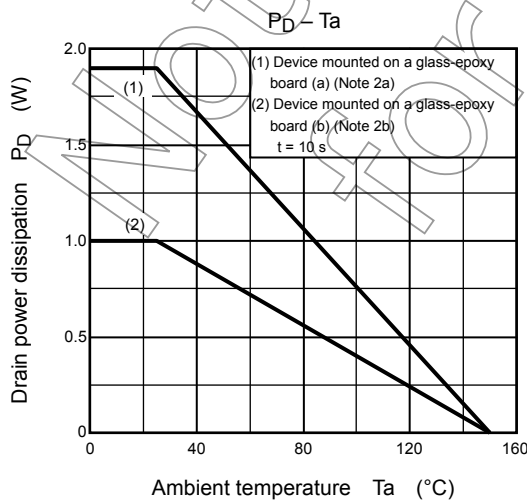
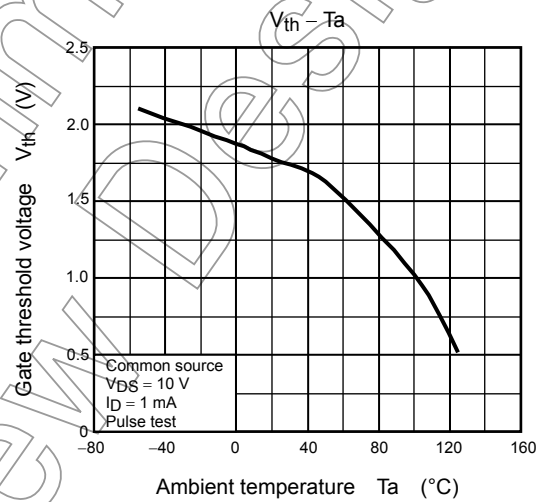
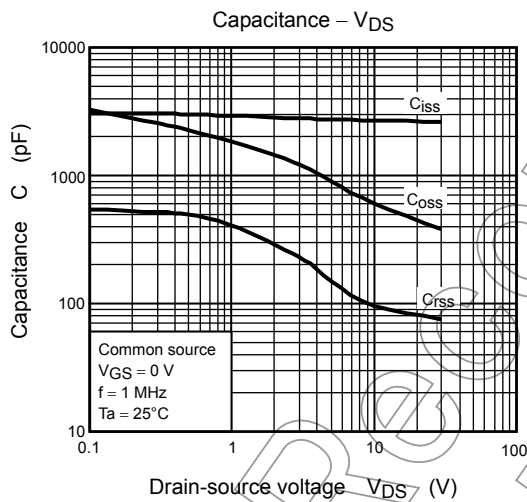
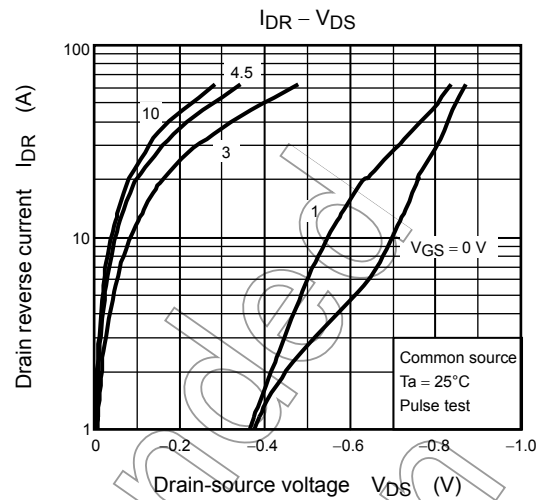
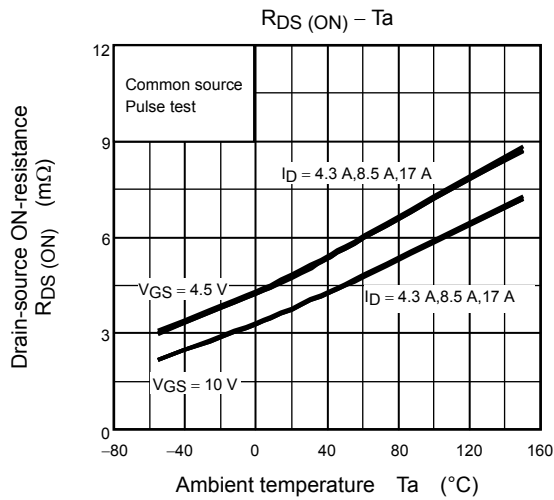
## Electrical Characteristics (Ta = 25°C)

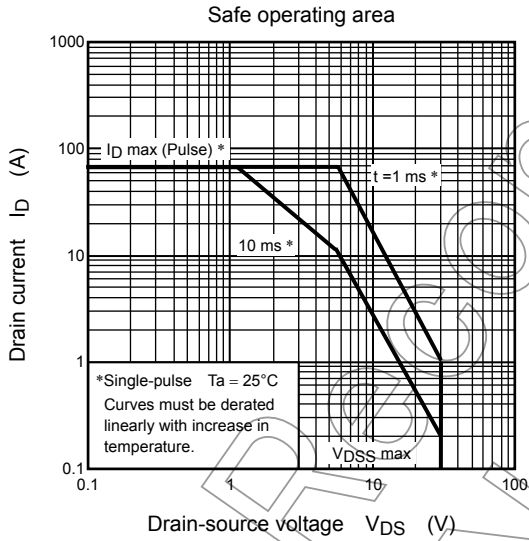
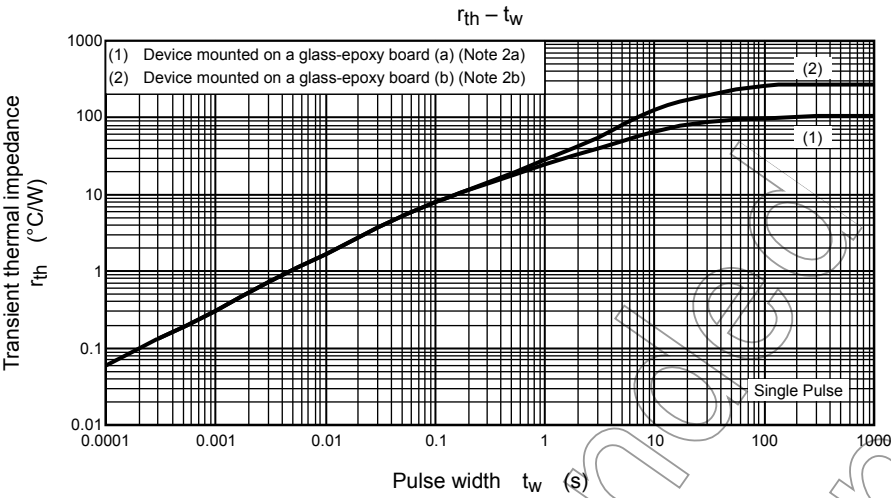
Characteristic		Symbol	Test Condition	Min	Typ.	Max	Unit
Gate leakage current		I <sub>GSS</sub>	V <sub>GS</sub> = ±20 V, V <sub>DS</sub> = 0 V	—	—	±100	nA
Drain cut-off current		I <sub>DSS</sub>	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V	—	—	100	μA
Drain-source breakdown voltage		V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	30	—	—	V
		V (BR) DSX	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = -20 V	15	—	—	
Gate threshold voltage		V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	1.3	—	2.3	V
Drain-source ON-resistance		R <sub>DS (ON)</sub>	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 8.5 A	—	5.1	7.0	mΩ
			V <sub>GS</sub> = 10 V, I <sub>D</sub> = 8.5 A	—	4.1	5.6	
Forward transfer admittance		Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 8.5 A	27	54	—	S
Input capacitance		C <sub>iss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz	—	2640	3430	pF
Reverse transfer capacitance		C <sub>rss</sub>		—	100	150	
Output capacitance		C <sub>oss</sub>		—	610	—	
Gate resistance		r <sub>g</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 5 MHz	—	1.0	1.5	Ω
Switching time	Rise time	t <sub>r</sub>		—	3.6	—	ns
	Turn-on time	t <sub>on</sub>		—	11.0	—	
	Fall time	t <sub>f</sub>		—	7.2	—	
	Turn-off time	t <sub>off</sub>		Duty ≤ 1%, t <sub>w</sub> = 10 μs	—	42	
Total gate charge (gate-source plus gate-drain)		Q <sub>g</sub>	V <sub>DD</sub> ≈ 24 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 17 A	—	36	—	nC
			V <sub>DD</sub> ≈ 24 V, V <sub>GS</sub> = 5 V, I <sub>D</sub> = 17 A	—	19	—	
Gate-source charge 1		Q <sub>gs1</sub>	V <sub>DD</sub> ≈ 24 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 17 A	—	7.6	—	
Gate-drain ("Miller") charge		Q <sub>gd</sub>		—	5.0	—	
Gate switch charge		Q <sub>SW</sub>		—	8.4	—	

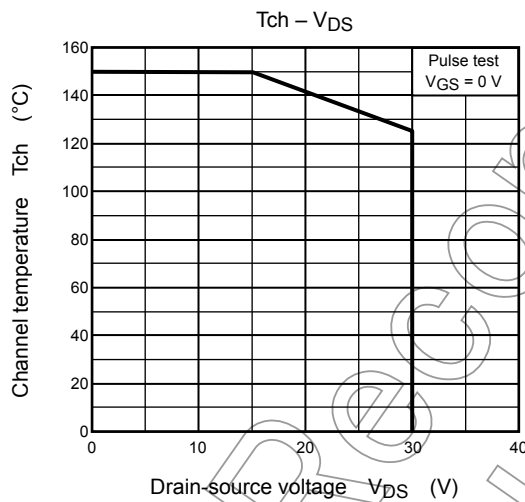
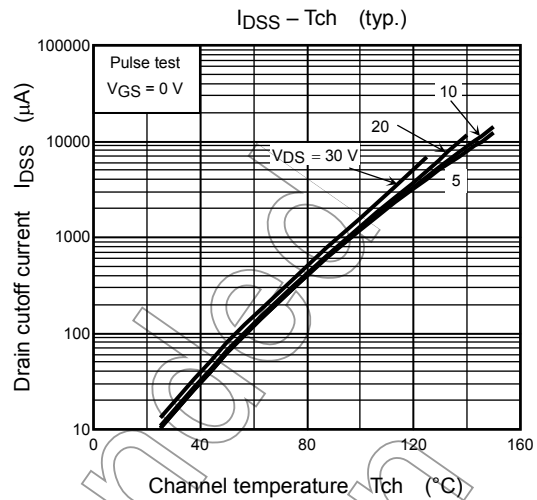
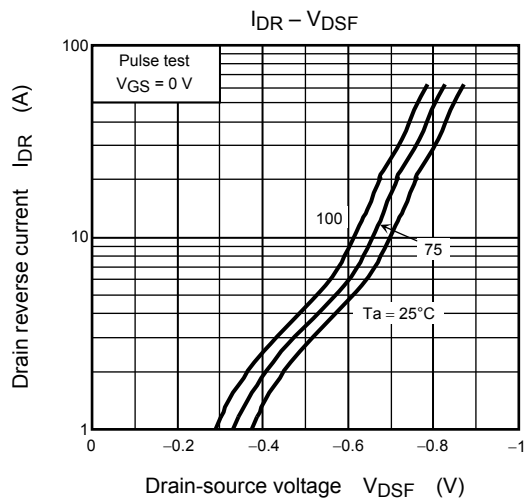
## Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristic		Symbol	Test Condition	Min	Typ.	Max	Unit
Drain reverse current	Pulse (Note 1)	$I_{DRP}$	—	—	—	68	A
Forward voltage (diode)		$V_{DSF}$	$I_{DR} = 1 \text{ A}, V_{GS} = 0 \text{ V}$	—	-0.4	-0.6	V
			$I_{DR} = 17 \text{ A}, V_{GS} = 0 \text{ V}$	—	—	-1.2	V









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