TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (U-MOS V-H)

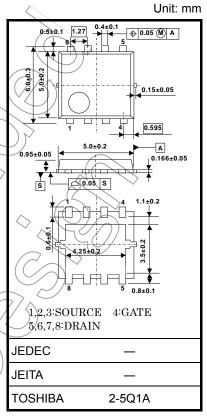
TPCA8018-H

High-Efficiency DC/DC Converter Applications Notebook PC Applications Portable Equipment Applications

- Small footprint due to a small and thin package
- High-speed switching
- Small gate charge: QSW = 9.3 nC (typ.)
- Low drain-source ON-resistance: RDS (ON) = 4.7 m Ω (typ.)
- High forward transfer admittance: $|Y_{fs}| = 76 \text{ S (typ.)}$
- Low leakage current: $I_{DSS} = 10 \mu A \text{ (max) (V}_{DS} = 30 \text{ V)}$
- Enhancement mode: $V_{th} = 1.5$ to 2.5 V ($V_{DS} = 10$ V, $I_{D} = 1$ mA)/

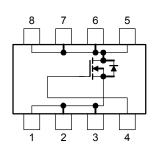
Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	30	V	
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)		V _{DGR}	30	V	
Gate-source voltage		V _{GSS}	±20	< <v< td=""></v<>	
Drain current	DC (Note 1)	ID((30	A	
	Pulsed (Note 1)		90	,	
Drain power dissipa	ation (Tc=25°C)	(P_D)	45	\/ w	
Drain power dissipation (t = 10 s) (Note 2a)		PD	2.8	W	
Drain power dissipation (t = 10 s) (Note 2b)		PD	(1.6/)	W	
Single-pulse avalanche energy (Note 3)		EAS	117	mJ	
Avalanche current		I _{AR}	30	Α	
Repetitive avalanche energy (Tc=25°C) (Note 4)		EAR	3.7	mJ	
Channel temperature)		Tch	150	°C	
Storage temperature range		T _{stg}	-55 to 150	°C	



Weight: 0.069 g (typ.)

Circuit Configuration



Note: For Notes 1 to 4, refer to the next page.

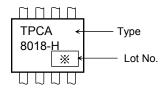
Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

This transistor is an electrostatic-sensitive device. Handle with care.

Thermal Characteristics

Characteristic	Symbol	Max	Unit
Thermal resistance, channel to case (Tc=25°C)	R _{th (ch-c)}	2.78	°C/W
Thermal resistance, channel to ambient $(t=10 \; s) \eqno(Note \; 2a)$	R _{th (ch-a)}	44.6	°C/W
Thermal resistance, channel to ambient (t = 10 s) (Note 2b)	R _{th (ch-a)}	78.1	°C/W

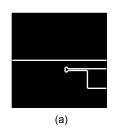
Marking (Note 5)



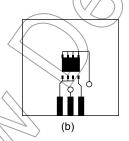
Note 1: The channel temperature should not exceed 150 °C during use

Note 2: (a) Device mounted on a glass-epoxy board (a)

(b) Device mounted on a glass-epoxy board (b)



FR-4 25.4 × 25.4 × 0.8 (Unit: mm)



FR-4 $25.4 \times 25.4 \times 0.8$ (Unit: mm)

Note 3: $V_{DD} = 24 \text{ V}$, $T_{Ch} = 25^{\circ}\text{C}$ (initial), $L = 100 \mu\text{H}$, $R_{G} \neq 25^{\circ}\Omega$, $I_{AR} = 30 \text{ A}$

Note 4: Repetitive rating; pulse width limited by max, channel temperature

Note 5: * Weekly code: (Three digits)

Week of manufacture

(01) for first week of year, continuing up to 52 or 53)

2

Year of manufacture

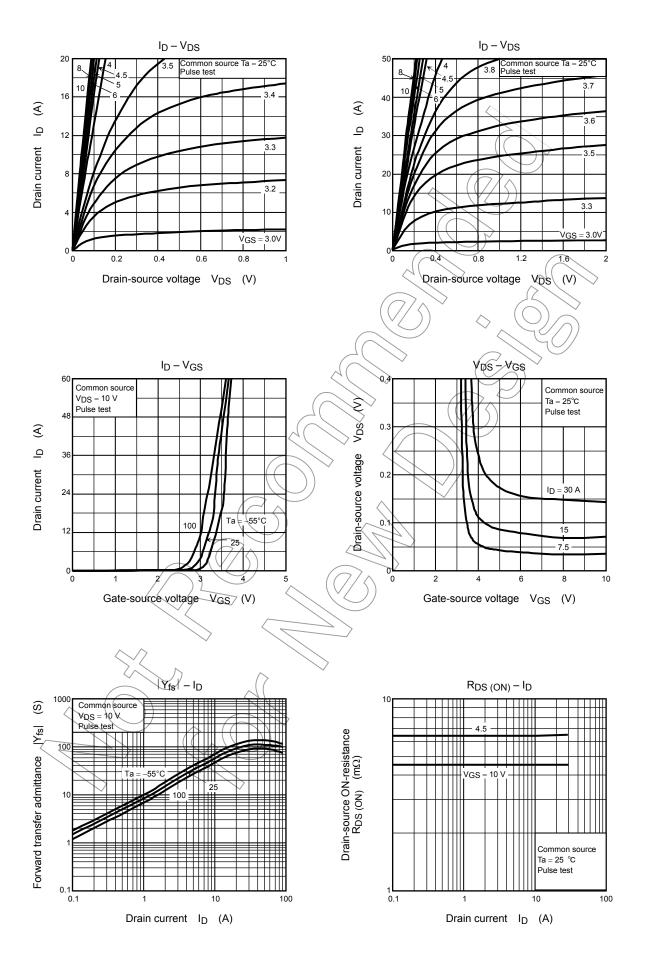
(The last digit of the calendar year)

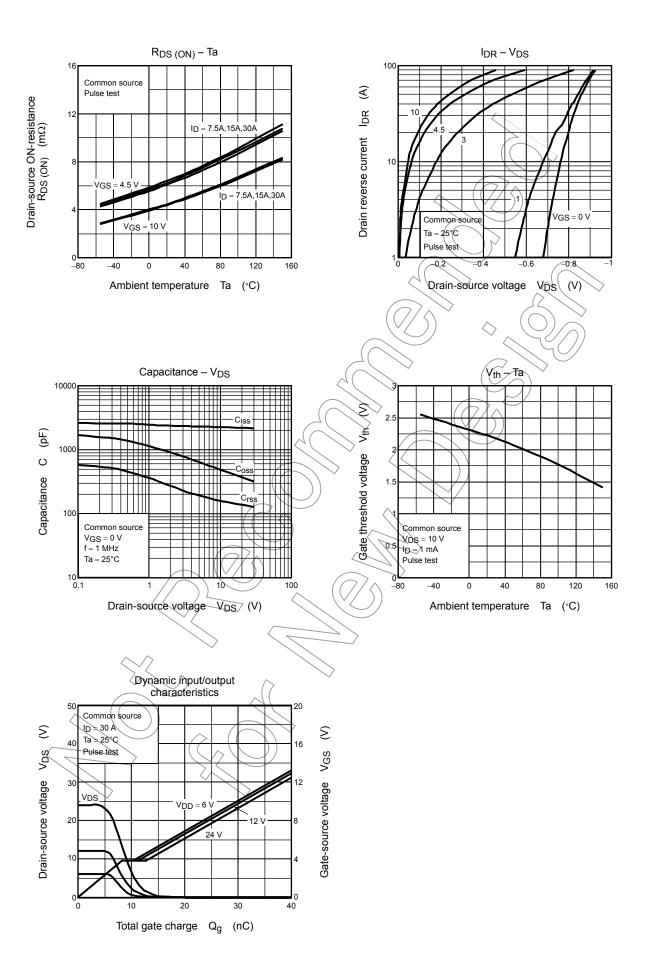
Electrical Characteristics (Ta = 25°C)

Cha	aracteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage curi	rent	I _{GSS}	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±100	nA
Drain cutoff curre	nt	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V	_	_	10	μА
Drain-source breakdown voltage		V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	30	_	_	V
		V (BR) DSX	$I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$	15	_	_	v
Gate threshold vo	ltage	V _{th}	$V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$	1.5) >	2.5	٧
Drain-source ON-	Dania davina ON registera		V _{GS} = 4.5 V, I _D = 15 A		6.3	8.2	mΩ
Drain-source ON-resistance		R _{DS} (ON)	V _{GS} = 10 V, I _D = 15 A)	4.7	6.2	
Forward transfer a	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 15 A	38	76	_	S
Input capacitance		C _{iss}		^ —	2270	2846	
Reverse transfer capacitance		C _{rss}	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	135	205	pF
Output capacitano	се	Coss			505	\downarrow	
Gate resistance		Rg	V _{DS} = 10 V, V _{GS} = 0 V, f = 5 MHz	-	1.0) 1.5	Ω
Switching time -	Rise time	t _r	V _{GS} 10 V I _D = 15A CG CG W	7	5) —	
	Turn-on time	t _{on}		7	13		ns
	Fall time	t _f			10		115
	Turn-off time	t _{off}	Duty ≦ 1%, t _w ≠ 10 μs	_	33		
Total gate charge (gate-source plus gate-drain)		Qg	$V_{DD} \simeq 24 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 30 \text{A}$	_	34	_	
			$V_{DD} \simeq 24 \text{ V}, V_{GS} = 5 \text{ V}, I_{D} \neq 30 \text{ A}$	_	18	_	
Gate-source char	ge 1 /	Q _{ĝs1}		_	7.9	_	nC
Gate-drain ("Miller") charge		Qgd	$V_{DD} \simeq 24 V$, $V_{GS} = 10 V$, $I_{D} = 30 A$	_	5.8	_	
Gate switch charg	ge (7)	Q _{SW}		_	9.3	_	

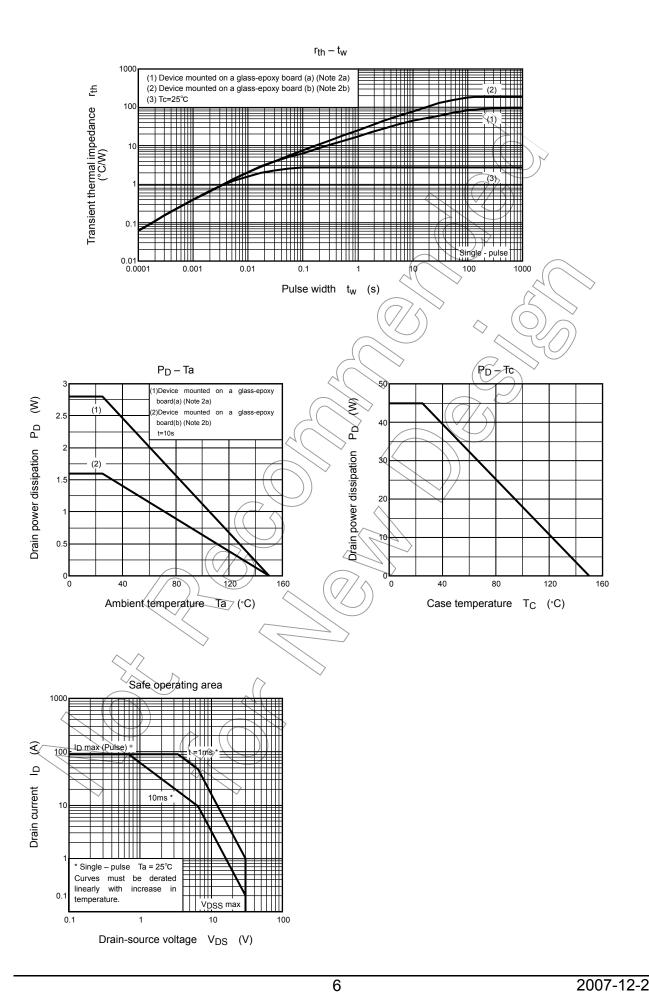
Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristic	Symbol Test Condition	Min	Тур.	Max	Unit
Drain reverse current Pulse (Note 1)	I _{DRP} —	_	_	90	Α
Forward voltage (diode)	V_{DSF} $I_{DR} = 30 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	-1.2	V





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