TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π–MOSIII)

2SK2884

Chopper Regulator, DC-DC Converter Applications

• Low drain–source ON resistance : $R_{DS\ (ON)} = 1.9\ \Omega\ (typ.)$ • High forward transfer admittance : $|Y_{fs}| = 3.8\ S\ (typ.)$

Low leakage current : $I_{DSS} = 100 \mu A \text{ (max) (V}_{DS} = 640 \text{ V)}$

• Enhancement mode : $V_{th} = 2.0 \text{ to } 4.0 \text{ V } (V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	800	(y)	
Drain-gate voltage (R _{GS} = 20 kΩ)		V_{DGR}	800	$\langle \langle v \rangle \rangle$	
Gate-source voltage		V_{GSS}	±30	/ <	
Drain current	DC (Note 1)	ΙD	5	A	
	Pulse (Note 1)	I_{DP}	15	A	
Drain power dissipation (Tc=25°C)		P_{D}	100	W	
Single pulse avalanche energy (Note 2)		Eas <	370	mJ	
Avalanche current		IAR	5	A	
Repetitive avalanche energy (Note 3)		EAR)) 10	mJ	
Channel temperature		T _{ch} <	150	∕ °C	
Storage temperature range		(T _{stg}))	-55 to 150	//c	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics Symbol	Max	Unit
Thermal resistance, channel to case Rth (ch-c)	1.25	°C/W
Thermal resistance, channel to ambient Rth (ch-a)	83.3	°C/W

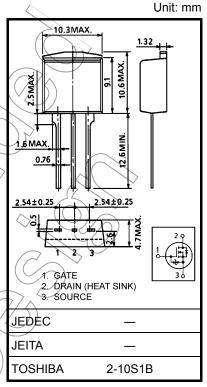
Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 27 mH, R_G = 25 Ω , I_{AR} = 5 A

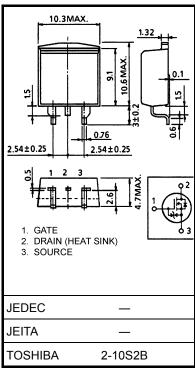
Note 3: Repetitive rating: pulse width limited by maximum channel temperature.

This transistor is an electrostatic-sensitive device.

Please handle with caution.



Weight: 1.5 g (typ.)



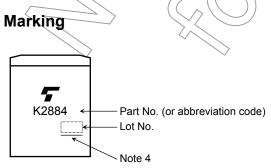
Weight: 1.5 g (typ.)

Electrical Characteristics (Ta = 25°C)

Charac	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	irrent	I _{GSS}	V _{GS} = ±30 V, V _{DS} = 0 V	_	_	±10	μΑ
Gate-source bre	eakdown voltage	V (BR) GSS	I _G = ±10 μA, V _{DS} = 0 V	±30	_	_	V
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = 640 V, V _{GS} = 0 V	\	_	100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	800	_	_	V
Gate threshold v	oltage/	V_{th}	V _{DS} = 10 V, I _D = 1 mA	2.0) >_	4.0	V
Drain-source O	N resistance	R _{DS (ON)}	V _{GS} = 10 V, I _D = 3 A	<u> </u>	1.9	2.2	Ω
Forward transfer	r admittance	Y _{fs}	V _{DS} = 15 V, I _D = 3 A	<u>)</u> ,	3.8	-	S
Input capacitano	e	C _{iss}		\	1080		
Reverse transfer capacitance		C _{rss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	<u> </u>	16	_	pF
Output capacitance		Coss		_	105		
Switching time	Rise time	t _r	V _{GS} 10V ID=3A V _{OUT}	- (40	∕2 ¹ ~	
	Turn-on time	t _{on}	$R_{L} = 66.7\Omega$		80		ns
	Fall time	t _f	V _{DD} =200V	(2)	40		115
	Turn-off time	t _{off}	Duty $\leq 1\%$, $t_{\rm W} = 10 \mu \rm s$) –	140	_	
Total gate charg plus gate-drain)		Qg		_	34	_	
Gate-source charge Q _g		Q _{gs}	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 5 \text{ A}$		16	_	nC
Gate-drain ("miller") Charge Qg		Qgd		_	18	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	ldr /	_	_	_	5	Α
Pulse drain reverse current (Note 1)	I _{DRP}	-	_	_	15	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 5 A, V _{GS} = 0 V	_	_	-1.9	V
Reverse recovery time	t _{rr}	I _{DR} = 5 A, V _{GS} = 0 V		1000		ns
Reverse recovery charge	Qrr	dl _{DR} / dt = 100 A / μs		7.5		μC

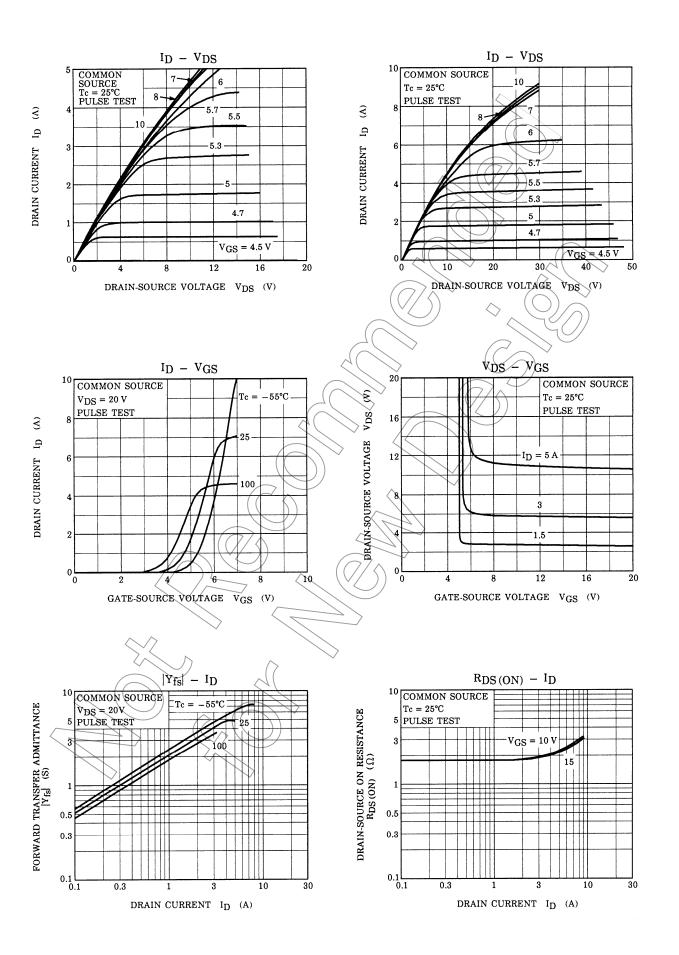


Note 4: A line under a Lot No. identifies the indication of product Labels.

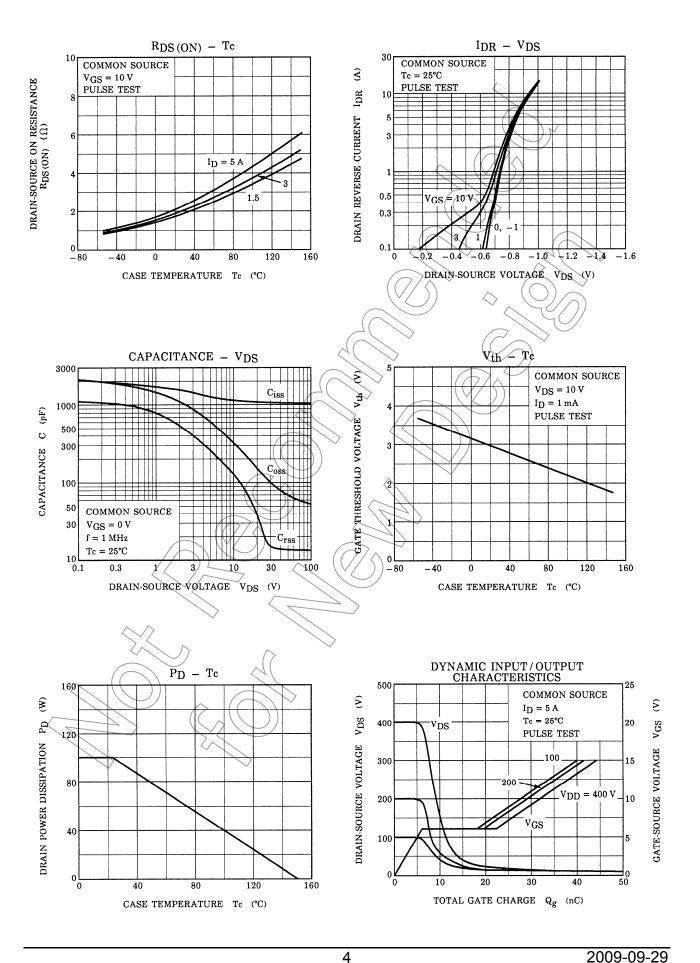
Not underlined: [[Pb]]/INCLUDES > MCV

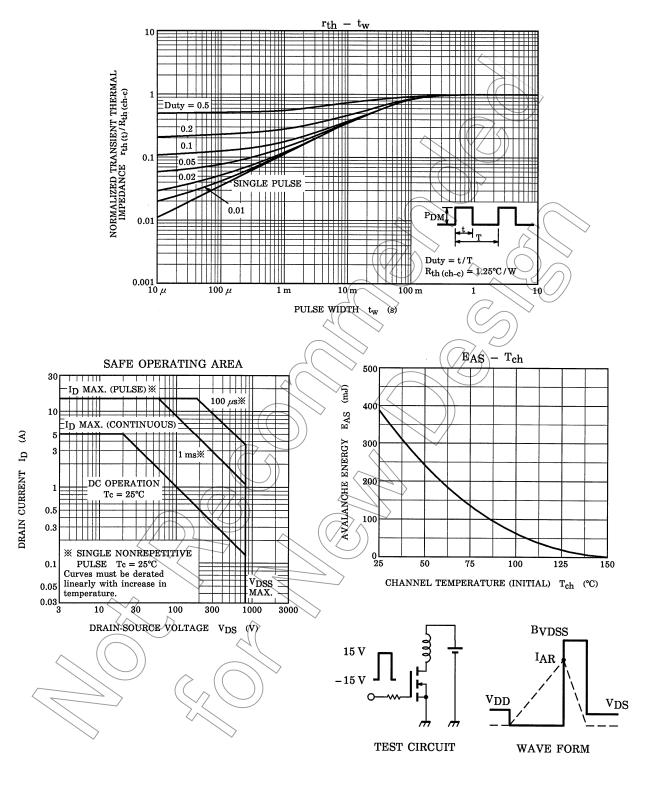
Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

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3 2009-09-29





$$R_G = 25 \Omega$$

 $V_{DD} = 90 V$, $L = 27 mH$

$$E_{AS} = \frac{1}{2} \cdot L \cdot I^{2} \cdot \left(\frac{B_{VDSS}}{B_{VDSS} - V_{DD}} \right)$$

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