TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (MACH II  $\pi$ -MOS VI)

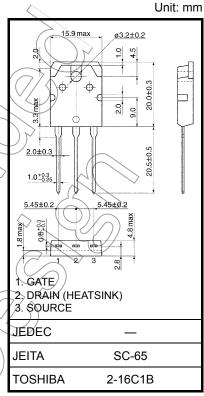
# 2SK3936

#### **Switching Regulator Applications**

- Small gate charge: Qg = 60 nC (typ.)
- Fast reverse recovery time: t<sub>rr</sub> = 380 ns (typ.)
- Low drain-source ON-resistance:  $R_{DS (ON)} = 0.2 \Omega (typ.)$
- High forward transfer admittance: |Yfs| = 16.5 S (typ.)
- Low leakage current: I<sub>DSS</sub> = 500 μA (V<sub>DS</sub> = 500 V)
- Enhancement mode:  $V_{th}$  = 2.0 to 4.0 V ( $V_{DS}$  = 10 V,  $I_D$  = 1 mA)

### Absolute Maximum Ratings (Ta = 25°C)

Characteristic			Symbol	Rating	(Unit)
Drain-source voltage			$V_{DSS}$	500	A
Drain-gate voltage (R <sub>GS</sub> = 20 kΩ)			$V_{DGR}$	500	V
Gate-source voltage			$V_{GSS}$	±30	> v
Drain current	DC	(Note 1)	I <sub>D</sub>	23	Α
	Pulse	(Note 1)	I <sub>DP</sub>	92	A .
Drain power dissipation (Tc = 25°C)			PD	150	/_w
Single-pulse avalanche energy (Note 2)			E <sub>A</sub> \$	759	, mJ
Avalanche current			TAR	23	A
Repetitive avalanche energy (Note 3)			((E <sub>AR</sub> ))	15	\/mJ
Channel temperature			Tch	150	~c
Storage temperature range			// T <sub>stg</sub>	-55 to 150	°C



Weight: 4.6 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### Thermal Characteristics

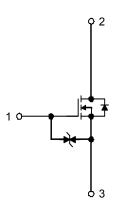
Characteristic	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	0.833	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	50	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD} = 90 \text{ V}$ ,  $T_{ch} = 25^{\circ}\text{C}$  (initial), L = 2.44 mH,  $I_{AR} = 23 \text{ A}$ ,  $R_G = 25 \Omega$ 

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.

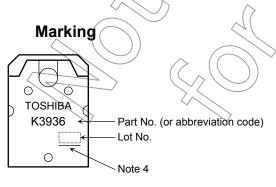


### **Electrical Characteristics (Ta = 25°C)**

Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current I <sub>G</sub>		I <sub>GSS</sub>	$V_{GS} = \pm 25 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μА
Gate-source breakdown voltage		V (BR) GSS	$I_G = \pm 10 \ \mu A, \ V_{DS} = 0 \ V$	±30	_	_	V
Drain cutoff current		I <sub>DSS</sub>	V <sub>DS</sub> = 500 V, V <sub>GS</sub> = 0 V	_	_	500	μА
Drain-source bre	akdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	500	_		V
Gate threshold voltage		V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	7(	4.0	V
Drain-source ON-resistance		R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 11.5 A	>~	0.2	0.25	Ω
Forward transfer admittance		Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 11.5 A	8	16.5		S
Input capacitance		C <sub>iss</sub>		_	4250		
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	10	_	pF
Output capacitance		C <sub>oss</sub>		_	420		
Switching time	Rise time	t <sub>r</sub>	10 V I <sub>D</sub> = 11.5 A VOUT	-	12		
	Turn-on time	t <sub>on</sub>	4.7Ω \$ R <sub>L</sub> = \ 17.4 Ω	_((	45	) —	
	Fall time	t <sub>f</sub>	V <sub>DD</sub> ≈ 200 V	7	> 10	_	ns
	Turn-off time	t <sub>off</sub>	Duty ≤ 1%, t <sub>W</sub> = 10 μs		80	_	
Total gate charge		Qg		) —	60	_	
Gate-source charge		Qgs	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 23 \text{ A}$	_	50	_	nC
Gate-drain charge		Qgd		_	10	_	

## Source-Drain Ratings and Characteristics (Ta = 25°C)

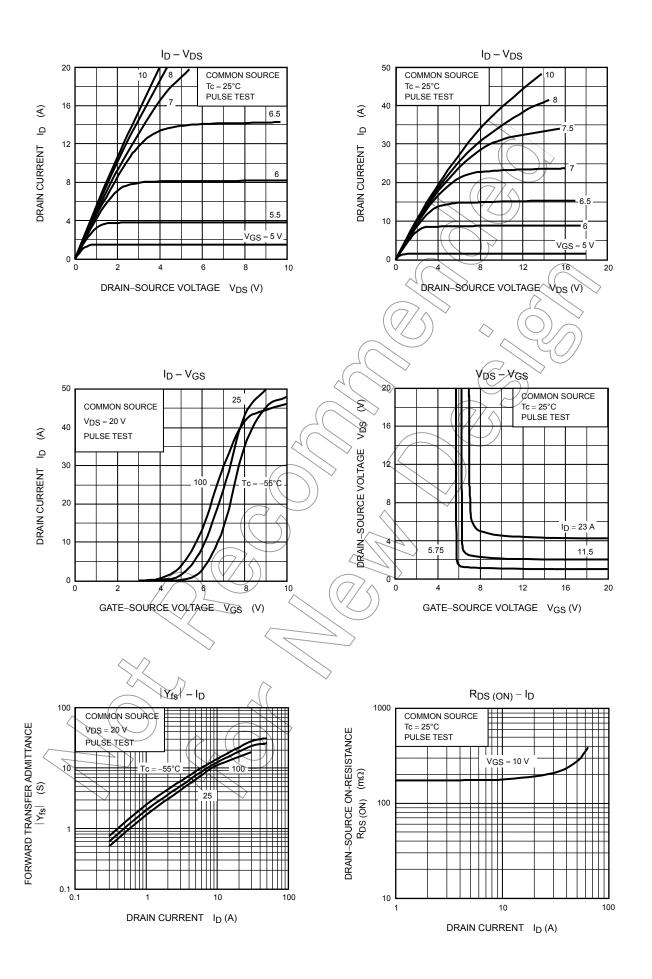
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	)) I <sub>DR</sub>		_	_	23	Α
Pulse drain reverse current (Note 1)	IDRP		_	_	92	Α
Forward voltage (diode)	V <sub>DSF</sub>	1 <sub>DR</sub> = 23 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time	t <sub>rī</sub>	I <sub>DR</sub> = 23 A, V <sub>GS</sub> = 0 V,	_	380	_	ns
Reverse recovery charge	Q <sub>rr</sub>	dl <sub>DR</sub> /dt = 100 A/μs	_	2.4	_	μС

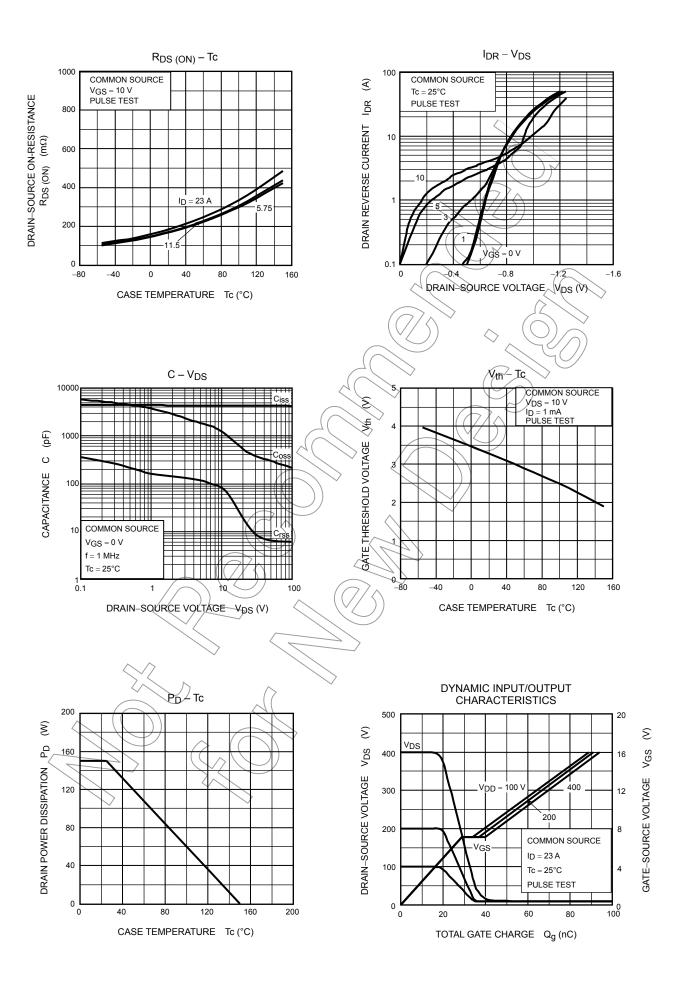


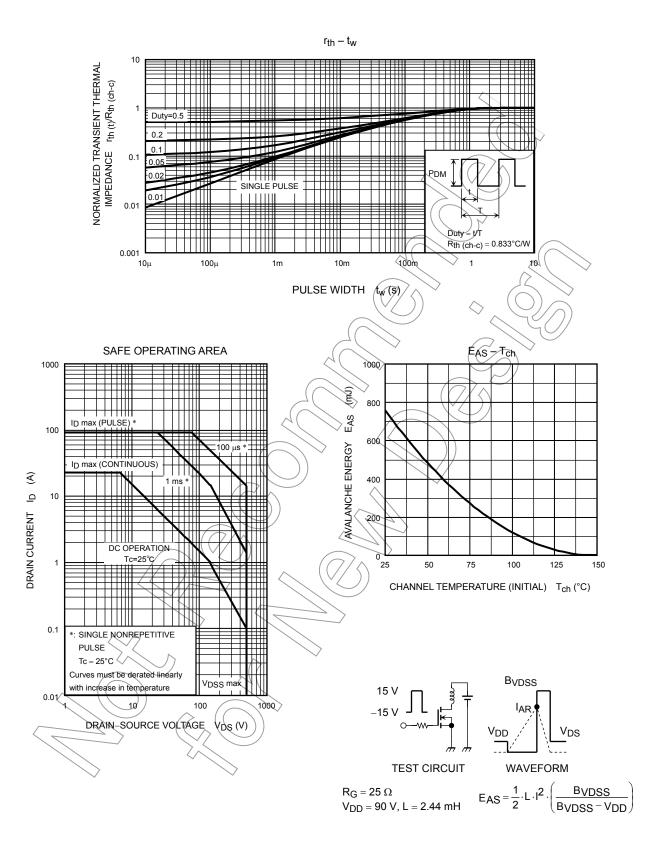
Note 4: A line under a Lot No. identifies the indication of product

Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.







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