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- Products
- Applications
- Quick Links
- Sales Contacts
- Product List
- Product Support
- Stock Check
- Take Our Web Site Survey

Featured Products 

Home > Products > Microcontrollers > ARM-Based 32-Bit Microcontrollers > TMPM320C1DFG Summary

TMPM320C1DFG

32-bit 144-pin TX03 Series

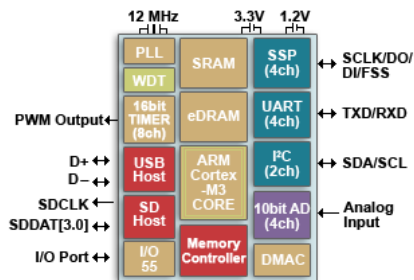
Audio control microcontroller containing high-speed USB host controller and SD host controller, enabling rapid database construction.

ARM® Cortex™-M3 CPU Core

Operating voltage: Internal= 1.1 to 1.3 V
 I/O= 3.0 to 3.6 V
 Maximum operating frequency: 144 MHz (12 MHzx12 by PLL)
 Minimum instruction execution time: 6.9 ns at 144 MHz operation

Built-chip Functions

USB (high-speed) host controller: 1 channel
 SD host controller: 1 channel
 DMA controller: 8 channels
 Memory controller: 2 channels
 10-bit AD converter: 4 channels
 16-bit timer/PWM: 8 channels
 SSP (SPI/MicroWire mode): 4 channels
 UART: 4 channels
 I²C: 2 channels
 Debug circuit (SWD): 1 channel
 Trace function (ETM): 1 channel



Flash Memory Size

Part number	SRAM	eDRAM
TMPM320C1DFG	320 Kbytes	1 Mbyte

**Under development
 * SSP: Synchronous Serial Port
 * SWD: Serial Wire Debug (ARM standard 2-pin interface)
 * ETM: Embedded Trace Macrocell

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