

FIVE OUTPUT 3.3V CLOCK BUFFER

IDT2305NZ

FEATURES:

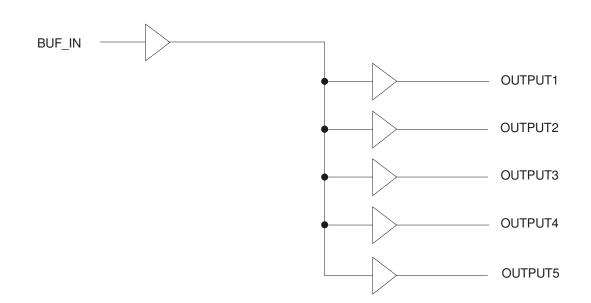
- One input to five output buffer/driver
- Low power consumption for mobile applications: less than 32mA at 66.6MHz with unloaded outputs
- 8.7ns max input-output delay
- Buffers all frequencies from DC to 133.33MHz
- Output-output skew < 250ps
- 3.3V operation
- High drive capability
- Available in SOIC package

DESCRIPTION:

The IDT2305NZ is a low-cost buffer designed to distribute high-speed clocks in mobile PC systems and desktop PC systems. The IDT2305NZ operates at 3.3V with five outputs that can run up to 133.33MHz

The IDT2305NZ is an 8-pin version of the IDT2309NZ. It is designed for low EMI and power optimization and consumes less than 32mA at 66.6MHz, making it ideal for the low power requirements of mobile systems.

FUNCTIONAL BLOCK DIAGRAM

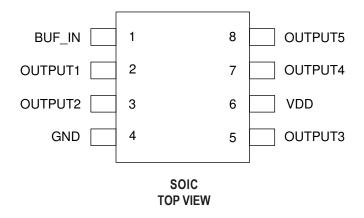


1

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COMMERCIAL AND INDUSTRIAL TEMPERATURE RANGES

PIN CONFIGURATION



ABSOLUTE MAXIMUM RATINGS⁽¹⁾

Symbol	Rating	Max.	Unit
Vdd	Supply Voltage Range	-0.5 to +4.6	V
VI ⁽²⁾	Input Voltage Range (REF)	-0.5 to +5.5	V
VI	Input Voltage Range	-0.5 to	V
	(except REF)	VDD+0.5	
Iк (VI < 0)	Input Clamp Current	50	mA
Io (Vo = 0 to VDD)	Continuous Output Current	±50	mA
VDD or GND	Continuous Current	±100	mA
TA = 55°C	Maximum Power Dissipation	0.7	W
(in still air) ⁽³⁾			
Tstg	Storage Temperature Range	-65 to +150	°C
Operating	Commercial Temperature	0 to +70	°C
Temperature	Range		
Operating	Industrial Temperature	-40 to +85	°C
Temperature	Range		

NOTES:

- Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.
- The input and output negative-voltage ratings may be exceeded if the input and output clamp-current ratings are observed.
- The maximum package power dissipation is calculated using a junction temperature of 150°C and a board trace length of 750 mils.

PIN DESCRIPTION

Pin Name	Pin Number	Functional Description
Vdd	6	3.3V Digital Voltage Supply
GND	4	Ground
BUF_IN	1	Inputclock
OUTPUT[1:5]	2, 3, 5, 7, 8	Outputs

OPERATING CONDITIONS - COMMERCIAL

Symbol Parameter		Min.	Max.	Unit
Vdd	Supply Voltage	3	3.6	V
TA	TA Operating Temperature (Ambient Temperature)		70	°C
CL	Load Capacitance, Fout < 100MHz		30	pF
	Load Capacitance 100MHz < Fout < 133.33MHz	—	15	
CIN	InputCapacitance	—	7	pF
BUF_IN, OUTPUT[1:5]	Operating Frequency	DC	133.33	MHz

OPERATING CONDITIONS - INDUSTRIAL

Symbol Parameter		Min.	Max.	Unit
Vdd	Supply Voltage	3	3.6	V
TA	TA Operating Temperature (Ambient Temperature)		+85	°C
CL	CL Load Capacitance, Fout < 100MHz		30	pF
	Load Capacitance 100MHz < Fout < 133.33MHz	—	15	
CIN Input Capacitance		—	7	pF
BUF_IN, OUTPUT[1:5]	Operating Frequency	DC	133.33	MHz

DC ELECTRICAL CHARACTERISTICS - COMMERCIAL

Symbol	Parameter	Conditions	Min.	Max.	Unit
VIL	Input LOW Voltage ⁽¹⁾		—	0.8	V
Vін	Input HIGH Voltage ⁽¹⁾		2	_	V
lıL	Input LOW Current	VIN = 0V	—	50	μA
Ін	Input HIGH Current	Vin = Vdd	—	100	μA
Vol	Output LOW Voltage ⁽²⁾	IoL = 12mA	—	0.4	V
Vон	Output HIGH Voltage ⁽²⁾	Іон = -12mA	2.4	_	V
IDD	Supply Current	Unloaded Outputs at 66.66MHz	—	32	mA

NOTES:

1. BUF_IN input has a threshold voltage of VDD/2.

2. Parameter is guaranteed by design but not production tested.

DC ELECTRICAL CHARACTERISTICS-INDUSTRIAL

Symbol	Parameter	Conditions	Min.	Max.	Unit
Vil	Input LOW Voltage ⁽¹⁾		—	0.8	V
Vih	Input HIGH Voltage ⁽¹⁾		2	—	V
lıL	InputLOW Current	Vin = 0V	—	50	μA
liн	Input HIGH Current	Vin = Vdd	—	100	μA
Vol	Output LOW Voltage ⁽²⁾	IoL = 12mA	—	0.4	V
Vон	Output HIGH Voltage ⁽²⁾	Іон = -12mA	2.4	_	V
IDD	Supply Current	Unloaded Outputs at 66.66MHz	-	35	mA

NOTES:

1. BUF_IN input has a threshold voltage of VDD/2.

2. Parameter is guaranteed by design but not production tested.

SWITCHING CHARACTERISTICS - COMMERCIAL⁽¹⁾

Symbol	Parameter ⁽²⁾	Conditions	Min.	Тур.	Max.	Unit
t3	RiseTime	Measured between 0.8V and 2V	_	_	1.5	ns
t4	FallTime	Measured between 0.8V and 2V	_	—	1.5	ns
t5	Output to Output Skew	All outputs equally loaded	_	_	250	ps
t6	Propagation Delay, BUF_IN Rising Edge to OUTPUT Rising Edge	Measured at VDD/2	1	5	8.7	ns
DC	Duty Cycle	Measured at VDD/2	45	_	55	%

NOTES:

1. All parameters specified with loaded outputs.

2. Parameter is guaranteed by design but not production tested.

SWITCHING CHARACTERISTICS - INDUSTRIAL⁽¹⁾

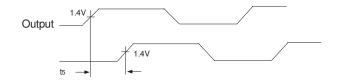
Symbol	Parameter ⁽²⁾	Conditions	Min.	Тур.	Max.	Unit
ß	RiseTime	Measured between 0.8V and 2V	_	_	1.5	ns
t4	FallTime	Measured between 0.8V and 2V	_	—	1.5	ns
15	Output to Output Skew	All outputs equally loaded	_	_	250	ps
t6	Propagation Delay, BUF_IN Rising Edge to OUTPUT Rising Edge	Measured at VDD/2	1	5	8.7	ns
DC	Duty Cycle	Measured at VDD/2	45	_	55	%

NOTES:

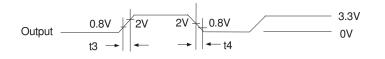
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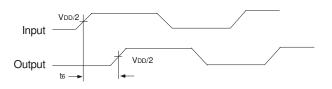
SWITCHING WAVEFORMS



Output to Output Skew

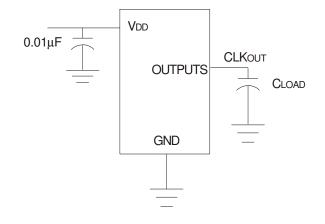




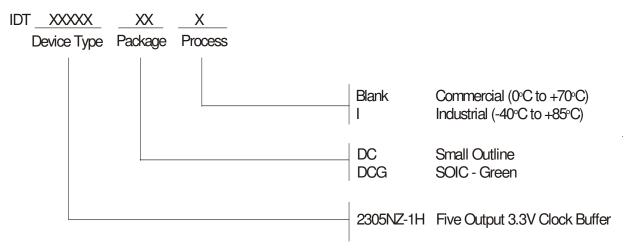


Input to Output Propagation Delay

TEST CIRCUIT



ORDERING INFORMATION





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