

SPECIFICATION

- Supplier : Samsung electro-mechanics
- Product : Multi-layer Ceramic Capacitor
- Samsung P/N : **CL31B105KCHNNNE**
- Description : **CAP, 1 μ F, 100V, \pm 10%, X7R, 1206**

A. Samsung Part Number

CL 31 B 105 K C H N N N E

Series	Samsung Multi-layer Ceramic Capacitor		
Size	1206 (inch code)	L: 3.2 \pm 0.2 mm	W: 1.6 \pm 0.2 mm
Dielectric	X7R	Inner electrode	Ni
Capacitance	1 μ F	Termination	Cu
Capacitance tolerance	\pm 10 %	Plating	Sn 100% (Pb Free)
Rated Voltage	100 V	Product	Normal
Thickness	1.6 \pm 0.2 mm	Special	Reserved for future use
		Packaging	Embossed Type, 7" reel

B. Samsung Reliability Test and Judgement condition

	Performance	Test condition
Capacitance	Within specified tolerance	1kHz \pm 10% 1.0 \pm 0.2Vrms
Tan (DF)	0.025 max.	
Insulation Resistance	10,000Mohm or 500Mohm $\cdot\mu$ F Whichever is Smaller	Rated Voltage 60~120 sec.
Appearance	No abnormal exterior appearance	Microscope (\times 10)
Withstanding Voltage	No dielectric breakdown or mechanical breakdown	200% of the rated voltage
Temperature Characterisitcs	X7R (From -55 to 125 , Capacitance change should be within \pm 15%)	
Adhesive Strength of Termination	No peeling shall be occur on the terminal electrode	500g \cdot F, for 10 \pm 1 sec.
Bending Strength	Capacitance change : within \pm 12.5%	Bending to the limit (1mm) with 1.0mm/sec.
Solderability	More than 75% of terminal surface is to be soldered newly	1) Sn63Pb37 solder 235 \pm 5 , 5 \pm 0.5sec. 2) SnAg3.0Cu0.5 solder 245 \pm 5 , 3 \pm 0.3sec. (preheating : 80~120 for 10~30sec.)
Resistance to Soldering heat	Capacitance change : within \pm 7.5% Tan , IR : initial spec.	Solder pot : 270 \pm 5 , 10 \pm 1sec.

	Performance	Test condition
Vibration Test	Capacitance change : within $\pm 5\%$ Tan δ , IR : initial spec.	Amplitude : 1.5mm From 10Hz to 55Hz (return : 1min.) 2hours \times 3 direction (x, y, z)
Moisture Resistance	Capacitance change : within $\pm 12.5\%$ Tan δ : 0.05 max IR : 500Mohm or 25Mohm $\cdot \mu F$ Whichever is Smaller	With rated voltage 40 ± 2 , 90~95%RH, 500+12/-0hrs
High Temperature Resistance	Capacitance change : within $\pm 12.5\%$ Tan δ : 0.05 max IR : 1000Mohm or 50Mohm $\cdot \mu F$ Whichever is Smaller	With 200% of the rated voltage Max. operating temperature 1000+48/-0hrs
Temperature Cycling	Capacitance change : within $\pm 7.5\%$ Tan δ , IR : initial spec.	1 cycle condition Min. operating temperature 25 Max. operating temperature 25 5 cycle test

C. Recommended Soldering method :

Reflow (Reflow Peak Temperature : 260+0/-5 , 10sec. Max)

* For the more detail Specification, Please refer to the Samsung MLCC catalogue.