

SERIES: PDS1-D | **DESCRIPTION:** DC-DC CONVERTER

FEATURES

- 1 W isolated output
- smaller package
- single unregulated output
- 1,500 Vdc isolation
- continuous short circuit protection
- extended temperature range (-40~105°C)
- antistatic protection up to 8kV
- high efficiency at light load
- efficiency up to 82%



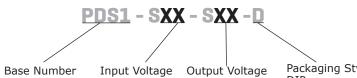
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MODEL	Ň	input voltage	output voltage		tput rent	output power	ripple and noise ¹	efficiency
	typ (Vdc)	range (Vdc)	(Vdc)	min (mA)	max (mA)	max (W)	typ (mVp-p)	typ (%)
PDS1-S3-S3-D	3.3	2.97~3.63	3.3	30	303	1	30	80
PDS1-S3-S5-D	3.3	2.97~3.63	5	20	200	1	30	80
PDS1-S5-S3-D	5	4.5~5.5	3.3	30	303	1	30	80
PDS1-S5-S5-D	5	4.5~5.5	5	20	200	1	30	80
PDS1-S5-S9-D	5	4.5~5.5	9	12	111	1	30	80
PDS1-S5-S12-D	5	4.5~5.5	12	9	83	1	30	81
PDS1-S5-S15-D	5	4.5~5.5	15	7	67	1	60	81
PDS1-S5-S24-D	5	4.5~5.5	24	4	42	1	60	81
PDS1-S12-S3-D	12	10.8~13.2	3.3	30	303	1	30	80
PDS1-S12-S5-D	12	10.8~13.2	5	20	200	1	30	80
PDS1-S12-S9-D	12	10.8~13.2	9	12	111	1	30	80
PDS1-S12-S12-D	12	10.8~13.2	12	9	83	1	30	81
PDS1-S12-S15-D	12	10.8~13.2	15	7	67	1	60	80
PDS1-S15-S5-D	15	13.5~16.5	5	20	200	1	30	80
PDS1-S24-S3-D	24	21.6~26.4	3.3	30	303	1	30	80
PDS1-S24-S5-D	24	21.6~26.4	5	20	200	1	30	80
PDS1-S24-S9-D	24	21.6~26.4	9	12	111	1	30	80
PDS1-S24-S12-D	24	21.6~26.4	12	9	83	1	30	81
PDS1-S24-S15-D	24	21.6~26.4	15	7	67	1	60	82
PDS1-S24-S24-D	24	21.6~26.4	24	4	42	1	60	82

Notes: 1. ripple and noise are measured at 20 MHz BW by "parallel cable" method

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PART NUMBER KEY



Packaging Style DIP

INPUT

parameter	conditions/description	min	typ	max	units
	3.3 V input models	2.97	3.3	3.63	Vdc
	5 V input models	4.5	5	5.5	Vdc
operating input voltage	12 V input models	10.8	12	13.2	Vdc
	15 V input models	13.5	15	16.5	Vdc
	24 V input models	21.6	24	26.4	Vdc
	for maximum of 1 second				
	3.3 V input models	-0.7		5	Vdc
	5 V input models	-0.7		9	Vdc
surge voltage	12 V input models	-0.7		18	Vdc
	15 V input models	-0.7		21	Vdc
	3.3 V input models 2.97 3.3 5 V input models 4.5 5 12 V input models 10.8 12 15 V input models 13.5 15 24 V input models 21.6 24 for maximum of 1 second -0.7 -0.7 3.3 V input models -0.7 -0.7 12 V input models -0.7 -0.7 12 V input models -0.7 -0.7 12 V input models -0.7 -0.7 15 V input models -0.7 -0.7 12 V input models -0.7 -0.7 14 V input models -0.7 -0.7 15 V input models -0.7 -0.7 14 V input models -0.7 -0.7 15 V input models -0.7 -0.7 24 V input models -0.7 -0.7 24 V input models -0.7 -0.7	30	Vdc		
filter	capacitance filter				

OUTPUT

parameter	conditions/description	min	typ	max	units
	for Vin change of 1%				
line regulation	3.3 V model			±1.5	%
	all other models			±1.2	%
	measured from 10% load to full load				
load regulation	3.3 V model			20	%
-	all other models			±1.5 ±1.2 20 15	%
voltage accuracy	see tolerance envelope curve				
switching frequency	100% load, nominal input voltage		100	300	kHz
temperature coefficient	100% load			±0.03	%/°C

PROTECTIONS

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parameter	conditions/description		typ	max	units
short circuit protection	continuous, automatic recovery				

SAFETY AND COMPLIANCE

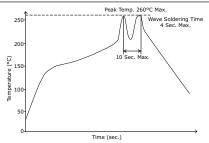
parameter	conditions/description	min	typ	max	units		
isolation voltage	for 1 minute at 1 mA max.	1,500			Vdc		
isolation resistance	at 500 Vdc	1,000		MΩ			
conducted emissions	CISPR22/EN55022, class B (external circuit re	CISPR22/EN55022, class B (external circuit required)					
ESD	IEC/EN61000-4-2, class B, contact ±8KV						
MTBF	MIL-HDBK-217F @ 25 °C	3,500,000			hours		
RoHS compliant	yes						

ENVIRONMENTAL

parameter	conditions/description	min	typ	max	units
operating temperature	see derating curve	-40		105	°C
storage temperature		-55		125	°C
storage humidity	non-condensing			95	%
temperature rise	at full load		25		°C

SOLDERABILITY

parameter	conditions/description	min	typ	max	units
hand soldering	1.5 mm from case for 10 seconds			300	°C
wave soldering	see wave soldering profile			260	°C

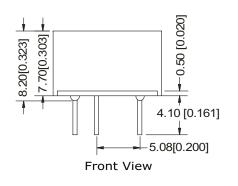


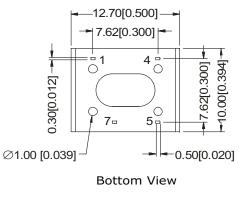
MECHANICAL

parameter	conditions/description	min	typ	max	units
dimensions	12.7 x 10.0 x 8.20 (0.50 x 0.394 x 0.323 inch)				mm
case material	plastic (UL94-V0)				
weight			1.8		g

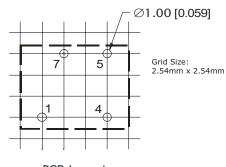
MECHANICAL DRAWING

units: mm[inch] tolerance: $\pm 0.25[\pm 0.010]$ pin section tolerance: $\pm 0.10[\pm 0.004]$





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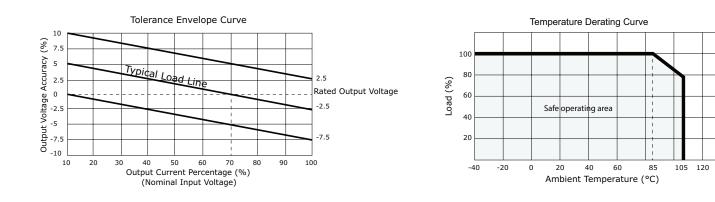


<u>PCB Layout</u> Top View

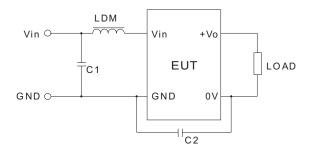
PIN CONNECTIONS				
PIN	function			
1	GND			
4	Vin			
5	+Vo			
7	0V			

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DERATING CURVES

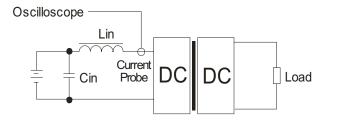


EMC RECOMMENDED CIRCUIT



Recom	Recommended external circuit components						
Vin (Vdc)	C1	LDM	C2				
3.3	475K/50V	6.8µH	NC				
5	475K/50V	6.8µH	NC				
12	475K/50V	6.8µH	NC				
15	475K/50V	6.8µH	470pF/2kV				
24	475K/50V	6.8µH	470pF/2kV				

TEST CONFIGURATION



External components					
Lin 4.7µH					
Cin	220μF, ESR < 1.0Ω at 100 KHz				

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Note: Input reflected-ripple current is measured with an inductor Lin and Capacitor Cin to simulate source impedance.

APPLICATION NOTES

1. **Output load requirement**

To ensure this module can operate efficiently and reliably, the minimum output load may not be less than 10% of the full load during operation. If the actual output power is low, connect a resistor at the output end in parallel to increase the load.

2. **Overload Protection**

Under normal operating conditions, the output circuit of this product has no protection against overload. The simplest method to add this is to add a circuit breaker to the circuit.

Recommended circuit 3.

If you want to further decrease the input/output ripple, you can increase the capacitance accordingly or choose capacitors with low ESR (see Figure 1). However, the capacitance of the output filter capacitor must be appropriate. If the capacitance is too high, a startup problem might arise. For every channel of the output, to ensure safe and reliable operation, the maximum capacitance must be less than the maximum capacitive load (see Table 1).

Figure 1	Vin o Cin ⊒ GND o	D		Cout - 0V
	Vin (Vdc)	Cin (µF)	Vo (Vdc)	Cout (µF)
	3.3	4.7	3.3	10
Table 1	5	4.7	5	10
	12	2.2	9	4.7
	15	1	12	2.2
	24	1	15	1
			24	0.47

It's not recommended to connect any external capacitors in applications with less than 0.5 watt output.

Output Voltage Regulation and Over-voltage Protection Circuit 4.

The device for output voltage regulation, over-voltage and over-current protection is a linear regulator and a capacitor filtering network with overheat protection which can be connected to the input or output end in series (see Figure 2). The recommended capacitance of its filter capacitor (see Table 1), and the linear regulator is based on the actual voltage and current required.

Figure 2

Vin O-REG		F	EG	-0 +Vo
	DC	Ť	•	0V

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Note:

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3. All specifications measured at: Ta=25°C, humidity<75%, nominal input voltage and rated output load, unless otherwise specified.

^{2.} Max, capacitive load tested at input voltage range and full load.

REVISION HISTORY

rev.	description	date
1.0	initial release	03/19/2013

The revision history provided is for informational purposes only and is believed to be accurate.



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CUI offers a two (2) year limited warranty. Complete warranty information is listed on our website.

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