late 08/30/2013

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SERIES: PDM2-D | **DESCRIPTION:** DC-DC CONVERTER

FEATURES

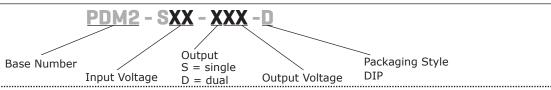
- 2 W isolated output
- smaller package
- single/dual unregulated output
- 1,500 Vdc isolation
- short circuit protection
- extended temperature range (-40~105°C)
- antistatic protection up to 8kV
- high efficiency at light load
- efficiency up to 85%



MODEL		input voltage	output voltage		itput rrent	output power	ripple and noise¹	efficiency
	typ (Vdc)	range (Vdc)	(Vdc)	min (mA)	max (mA)	max (W)	typ (mVp-p)	typ (%)
PDM2-S3-S5-D	3.3	2.97~3.63	5	40	400	2	60	80
PDM2-S5-S5-D	5	4.5~5.5	5	40	400	2	60	85
PDM2-S5-S12-D	5	4.5~5.5	12	17	167	2	60	84
PDM2-S5-S15-D	5	4.5~5.5	15	13	133	2	75	84
PDM2-S5-D5-D	5	4.5~5.5	±5	±20	±200	2	60	80
PDM2-S5-D7-D	5	4.5~5.5	±7	±15	±142	2	60	80
PDM2-S5-D9-D	5	4.5~5.5	±9	±11	±111	2	60	83
PDM2-S5-D12-D	5	4.5~5.5	±12	±8	±83	2	60	84
PDM2-S5-D15-D	5	4.5~5.5	±15	±7	±67	2	75	84
PDM2-S12-S5-D	12	10.8~13.2	5	40	400	2	60	84
PDM2-S12-S12-D	12	10.8~13.2	12	17	167	2	60	84
PDM2-S12-S24-D	12	10.8~13.2	24	8	83	2	75	84
PDM2-S12-D5-D	12	10.8~13.2	±5	±20	±200	2	60	84
PDM2-S12-D9-D	12	10.8~13.2	±9	±11	±111	2	60	84
PDM2-S12-D12-D	12	10.8~13.2	±12	±8	±83	2	60	85
PDM2-S12-D15-D	12	10.8~13.2	±15	±7	±67	2	75	84
PDM2-S24-S5-D	24	21.6~26.4	5	40	400	2	60	84
PDM2-S24-S9-D	24	21.6~26.4	9	22	222	2	60	84
PDM2-S24-S12-D	24	21.6~26.4	12	17	167	2	60	84
PDM2-S24-D5-D	24	21.6~26.4	±5	±20	±200	2	60	84
PDM2-S24-D12-D	24	21.6~26.4	±12	±8	±83	2	60	84
PDM2-S24-D15-D	24	21.6~26.4	±15	±7	±67	2	75	84

Notes: 1. ripple and noise are measured at 20 MHz BW by "parallel cable" method

PART NUMBER KEY



INPUT

parameter	conditions/description	min	typ	max	units
	3.3 V input models	2.97	3.3	3.63	Vdc
anaustina innut valtasa	5 V input models	4.5	5	5.5	Vdc
operating input voltage	12 V input models	10.8	12	13.2	Vdc
	24 V input models	21.6	24	3.63 5.5	Vdc
	for maximum of 1 second				
	3.3 V input models	-0.7		5	Vdc
surge voltage	5 V input models	-0.7		9	Vdc
	12 V input models	-0.7		5.3 3.63 5 5.5 12 13.2 24 26.4 5 9	Vdc
	24 V input models	-0.7		30	Vdc
filter	capacitance filter				

OUTPUT

parameter	conditions/description	min	typ	max	units
line regulation	for Vin change of 1%			±1.2	%
load regulation	measured from 10%~100% load 5 V output models 9 V output models 12 V output models 15 V output models 24 V output models		12 9 8 7 6		% % % % %
voltage accuracy	see tolerance envelope curve				
switching frequency	100% load, nominal input voltage		100	300	kHz
temperature coefficient	at 100% load			±0.03	%/°C

PROTECTIONS

parameter	conditions/description	min	typ	max	units
short circuit protection	supply voltage must be removed at the end of the short circuit duration			1	S

SAFETY AND COMPLIANCE

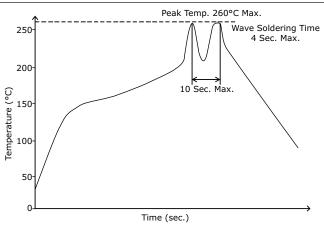
parameter	conditions/description	min	typ	max	units
isolation voltage	input to output for 1 minute, 1 mA max. leakage current	1,500			Vdc
isolation resistance	input to output at 500 Vdc	1,000			ΜΩ
conducted emissions	CISPR22/EN55022, class B, external circuit required	i			
radiated emissions	CISPR22/EN55022, class B, external circuit required	i			
ESD	IEC/EN61000-4-2, class B, contact \pm 8kV for single IEC/EN61000-4-2, class B, contact \pm 6kV for dual o				
MTBF	as per MIL-HDBK-217F @ 25°C	3,500,000			hours
RoHS compliant	yes				

ENVIRONMENTAL

parameter	conditions/description	min	typ	max	units
operating temperature	see derating curve	-40		105	°C
storage temperature		-55		125	°C
storage humidity	non-condensing			95	%
temperature rise	Ta=25°C, 100% load		25		°C

SOLDERABILITY

parameter	conditions/description	min	typ	max	units
hand soldering	1.5 mm from case for 10 seconds			300	°C
wave soldering	see wave soldering profile			260	°C



MECHANICAL

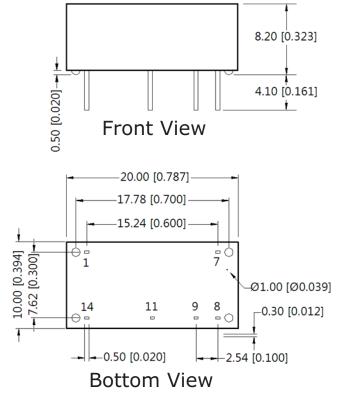
parameter	conditions/description	min	typ	max	units
dimensions	20 x 10 x 8.2 (0.787 x 0.394 x 0.323 inch)				mm
case material	plastic (UL94-V0)				
weight			2.4		g

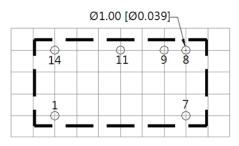
MECHANICAL DRAWING

units: mm[inch]

tolerance: $\pm 0.25[\pm 0.010]$

pin section tolerance: $\pm 0.10[\pm 0.004]$



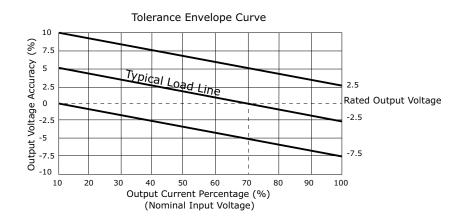


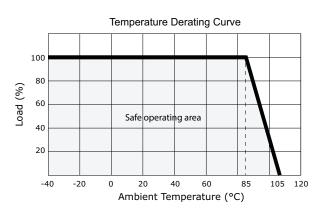
Note : grid : 2.54*2.54mm

Top View PCB Layout

PIN CONNECTIONS	
DIN Cinala Outaut Dual Outau	
PIN Single Output Dual Outpu	t
1 GND GND	
7 NC NC	
8 0V 0V	
9 +Vo +Vo	
11 NO PIN -Vo	
14 Vin Vin	

DERATING CURVES





EMC RECOMMENDED CIRCUIT

Figure 1 LDM Vin 🗁 Vin +Vo EUT LOAD _ C0 GND O GND -Vo (0V)

Table 1

Recommended external circuit components					
Vin (Vdc) C0 LDM					
5	4.7μF/50V	6.8µH			
12	4.7μF/50V	6.8µH			
24	4.7μF/50V	6.8µH			

TEST CONFIGURATION

Figure 2

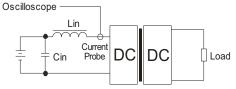


Table 2

External components				
Lin 4.7µH				
Cin	220μF, ESR $< 1.0Ω$ at 100 KHz			

Note:

1. Input reflected-ripple current is measured with an inductor Lin and Capacitor Cin to simulate source impedance.

APPLICATION NOTES

Output load requirement

To ensure this module can operate efficiently and reliably, the minimum output load may not be less than 10% of the full load during operation. If the actual output power is low, connect a resistor at the output end in parallel to increase the load.

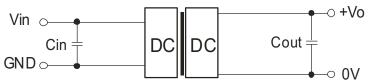
Under normal operating conditions, the output circuit of this product has no protection against overload. The simplest method to add this is to add a circuit breaker to the circuit.

3. Recommended circuit

If you want to further decrease the input/output ripple, you can increase the capacitance accordingly or choose capacitors with low ESR(see Figure 3 & Table 3). However, the capacitance of the output filter capacitor must be appropriate. If the capacitance is too high, a startup problem might arise. For every channel of the output, to ensure safe and reliable operation, the maximum capacitance must be less than the maximum capacitive load (see Table 4).

Figure 3

Single Output



Dual Output

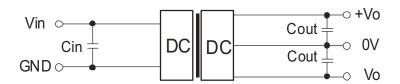


Table 3

Vin (Vdc)	Cin (µF)	Single Vo (Vdc)	Cout (µF)	Dual Vo (Vdc)	Cout (µF)
3.3	4.7	5	10	±5	4.7
5	4.7	9	4.7	±9	2.2
12	2.2	12	2.2	±12	1
24	1	15/24	1	±15	0.47

Note: It's not recommended to connect any external capacitors in applications with less than 0.5 watt output.

Table 4

Single Vout (Vdc)	Max. Capacitive Load (μF)	Dual Vout (Vdc)	Max. Capacitive Load¹ (μF)
5	220	5	100
9	220	7	100
12	220	9	100
15	220	12	100
24	220	15	100

Note: 1. For each output.

- 1. Operation under minimum load will not damage the converter; however, they may not meet all specifications listed.
- Max. capacitive load tested at input voltage range and full load.
 All specifications measured at: Ta=25°C, humidity<75%, nominal input voltage and rated output load, unless otherwise specified.

CUI Inc | SERIES: PDM2-D | DESCRIPTION: DC-DC CONVERTER

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REVISION HISTORY

rev.	description	date
1.0	initial release	08/30/2013

The revision history provided is for informational purposes only and is believed to be accurate.



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CUI offers a two (2) year limited warranty. Complete warranty information is listed on our website.

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