

3Q Hi-Com Triac Rev. 5 — 9 May 2011

**Product data sheet** 

### 1. Product profile

### 1.1 General description

Planar passivated high commutation three quadrant triac in a SOT78 (TO-220AB) plastic package. This "series F" triac balances the requirements of commutation performance and gate sensitivity and is intended for interfacing with low power drivers and logic ICs including microcontrollers in higher noise environments.

### 1.2 Features and benefits

- 3Q technology for improved noise immunity
- Direct triggering from low power drivers and logic ICs
- High blocking voltage capability
- High commutation capability

### 1.3 Applications

- AC solenoids
- General purpose motor control circuits

### 1.4 Quick reference data

#### Table 1. Quick reference data

Symbol Parameter Conditions Max Unit Min Тур 600 V repetitive peak off-state VDRM voltage non-repetitive peak full sine wave;  $T_{j(init)} = 25 \text{ °C};$ А I<sub>TSM</sub> 25  $t_p = 20 \text{ ms}; \text{ see Figure 4};$ on-state current see Figure 5 RMS on-state current full sine wave;  $T_{mb} \leq 107 \text{ °C}$ ; 4 А I<sub>T(RMS)</sub> see Figure 1; see Figure 2; see Figure 3

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- Intermediate sensitivity for maximum noise immunity and logic level triggering
- Planar passivated for voltage ruggedness and reliability
- Triggering in three quadrants only
- Home appliances

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Table 1.	Quick reference data .	continued				
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static cha	aracteristics					
I <sub>GT</sub>	gate trigger current	V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T2+ G+; T <sub>j</sub> = 25 °C; see <u>Figure 7</u>	-	-	25	mA
		$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; \text{ T2+} $ G-; T <sub>j</sub> = 25 °C; see <u>Figure 7</u>	-	-	25	mA
		$V_D = 12 \text{ V}; \text{ I}_T = 0.1 \text{ A}; \text{ T2-} \text{G-}; \text{T}_j = 25 ^\circ\text{C}; \text{ see } \frac{\text{Figure 7}}{100000000000000000000000000000000000$	-	-	25	mA

### 2. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	T1	main terminal 1		N.
2	T2	main terminal 2	mb	T2-T1
3	G	gate		Sym051
mb	Τ2	mounting base; main terminal 2		

SOT78 (TO-220AB)

### 3. Ordering information

Table 3.	Ordering information	
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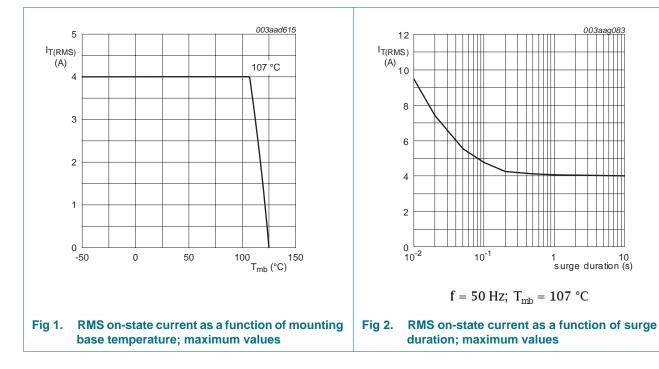
Type number	Package		
	Name	Description	Version
BTA204-600F	TO-220AB	plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB	SOT78

### 4. Limiting values

#### Table 4. Limiting values

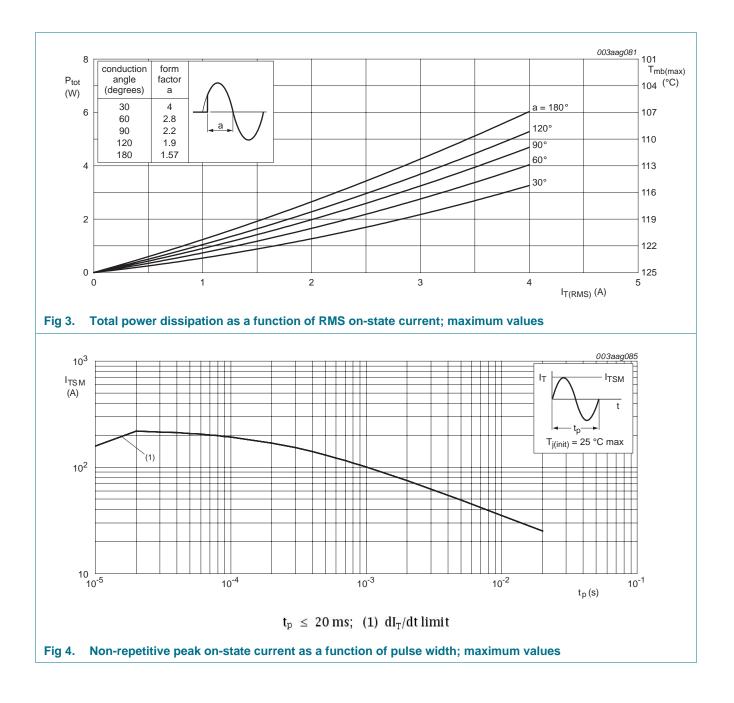
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>DRM</sub>	repetitive peak off-state voltage	9	-	600	V
I <sub>T(RMS)</sub>	RMS on-state current	full sine wave; T <sub>mb</sub> ≤ 107 °C; see <u>Figure 1</u> ; see <u>Figure 2</u> ; see <u>Figure 3</u>	-	4	А
I <sub>TSM</sub>	non-repetitive peak on-state current	full sine wave; T <sub>j(init)</sub> = 25 °C; t <sub>p</sub> = 20 ms; see <u>Figure 4</u> ; see <u>Figure 5</u>	-	25	А
		full sine wave; $T_{j(init)} = 25 \text{ °C};$ $t_p = 16.7 \text{ ms}$	-	27	А
l <sup>2</sup> t	I <sup>2</sup> t for fusing	t <sub>p</sub> = 10 ms; sine-wave pulse	-	3.1	A <sup>2</sup> s
dl <sub>T</sub> /dt	rate of rise of on-state current	$I_T = 6 \text{ A}; I_G = 0.2 \text{ A}; \text{ d}I_G/\text{d}t = 0.2 \text{ A}/\mu\text{s}$	-	100	A/µs
I <sub>GM</sub>	peak gate current		-	2	А
P <sub>GM</sub>	peak gate power		-	5	W
P <sub>G(AV)</sub>	average gate power	over any 20 ms period	-	0.5	W
T <sub>stg</sub>	storage temperature		-40	150	°C
Tj	junction temperature		-	125	°C



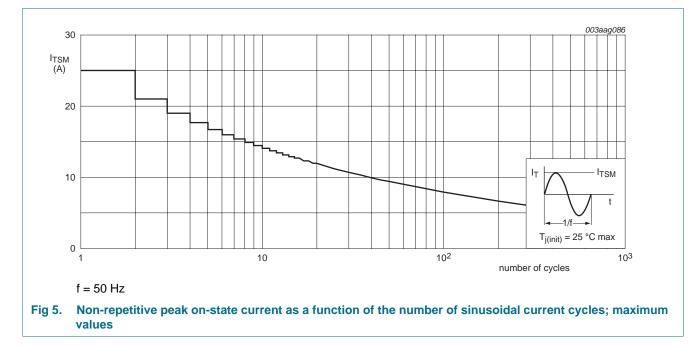
## **BTA204-600F**

#### **3Q Hi-Com Triac**



## **BTA204-600F**

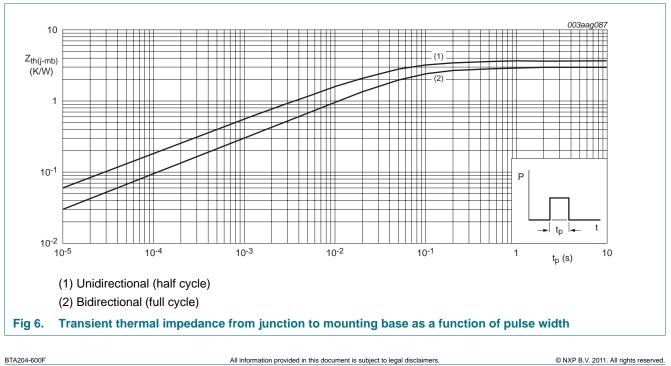
#### **3Q Hi-Com Triac**



### 5. Thermal characteristics

#### Table 5.Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R <sub>th(j-mb)</sub>	thermal resistance from junction to mounting base	full cycle; see Figure 6	-	-	3	K/W
		half cycle; see <u>Figure 6</u>	-	-	3.7	K/W
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	-	60	-	K/W



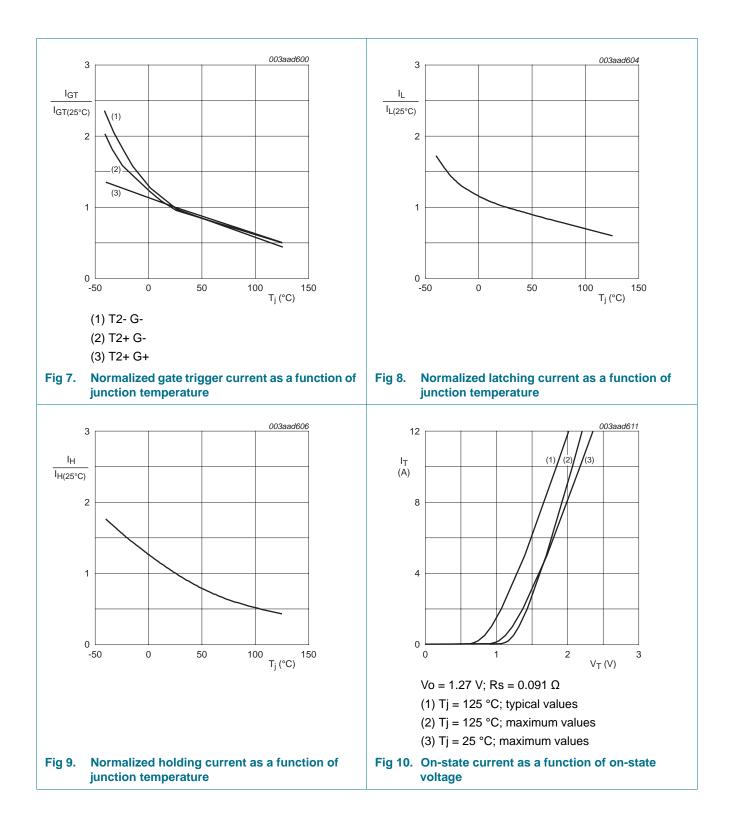
**3Q Hi-Com Triac** 

### 6. Characteristics

Table 6.	Characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static cha	aracteristics					
I <sub>GT</sub>	gate trigger current	V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T2+ G+; T <sub>j</sub> = 25 °C; see <u>Figure 7</u>	-	-	25	mA
		V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T2+ G-; T <sub>j</sub> = 25 °C; see <u>Figure 7</u>	-	-	25	mA
		V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T2- G-; T <sub>j</sub> = 25 °C; see <u>Figure 7</u>	-	-	25	mA
L	latching current	V <sub>D</sub> = 12 V; I <sub>G</sub> = 0.1 A; T2+ G+; T <sub>j</sub> = 25 °C; see <u>Figure 8</u>	-	-	20	mA
		V <sub>D</sub> = 12 V; I <sub>G</sub> = 0.1 A; T2+ G-; T <sub>j</sub> = 25 °C; see <u>Figure 8</u>	-	-	30	mA
		V <sub>D</sub> = 12 V; I <sub>G</sub> = 0.1 A; T2- G-; T <sub>j</sub> = 25 °C; see <u>Figure 8</u>	-	-	20	mA
I <sub>H</sub>	holding current	V <sub>D</sub> = 12 V; T <sub>j</sub> = 25 °C; see <u>Figure 9</u>	-	-	20	mA
V <sub>T</sub>	on-state voltage	I <sub>T</sub> = 5 A; T <sub>j</sub> = 25 °C; see <u>Figure 10</u>	-	1.4	1.7	V
V <sub>GT</sub>	gate trigger voltage	V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T <sub>j</sub> = 25 °C; see <u>Figure 11</u>	-	0.7	1.5	V
		$V_D = 400 \text{ V}; I_T = 0.1 \text{ A}; T_j = 125 \text{ °C};$ see <u>Figure 11</u>	0.25	0.4	-	V
I <sub>D</sub>	off-state current	$V_{D} = 600 \text{ V}; \text{ T}_{j} = 125 \text{ °C}$	-	0.1	0.5	mA
Dynamic	characteristics					
dV <sub>D</sub> /dt	rate of rise of off-state voltage	$V_{DM}$ = 402 V; T <sub>j</sub> = 125 °C; exponential waveform; gate open circuit	50	-	-	V/µs
dl <sub>com</sub> /dt	rate of change of commutating current	$V_D = 400 \text{ V}; \text{ T}_j = 125 \text{ °C}; \text{ I}_{T(RMS)} = 4 \text{ A};$ $dV_{com}/dt = 10 \text{ V}/\mu s;$ gate open circuit	3	-	-	A/m
		$\label{eq:VD} \begin{array}{l} V_D = 400 \text{ V};  \text{T}_j = 125 ^\circ\text{C};  \text{I}_{\text{T}(\text{RMS})} = 4 \text{ A}; \\ \text{d} V_{\text{com}}/\text{d}t = 0.1  \text{V}/\mu\text{s}; \text{ gate open circuit} \end{array}$	15	-	-	A/m
t <sub>gt</sub>	gate-controlled turn-on time	$I_{TM} = 12 \text{ A}; V_D = 600 \text{ V}; I_G = 0.1 \text{ A};$ $dI_G/dt = 5 \text{ A}/\mu \text{s}$	-	2	-	μs

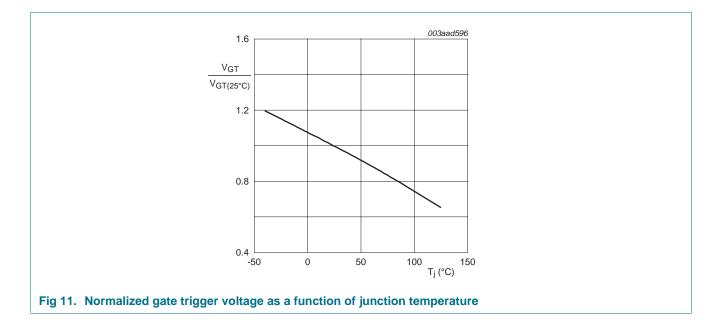
## **BTA204-600F**

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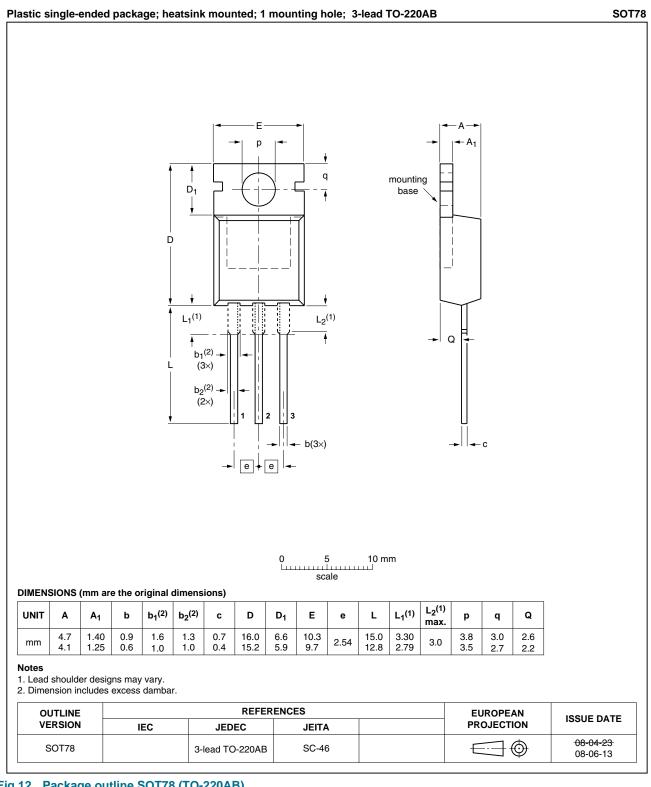
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#### **Package outline** 7.



#### Fig 12. Package outline SOT78 (TO-220AB)

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BTA204-600F

### 8. Revision history

Table 7.Revision history				
Document ID	Release date	Data sheet status	Change notice	Supersedes
BTA204-600F v.5	20110509	Product data sheet	-	BTA204_SERIES_D_E_F_4
Modifications:		t of this data sheet has be of NXP Semiconductors.	een redesigned to	comply with the new identity
	<ul> <li>Legal texts</li> </ul>	have been adapted to the	e new company na	ame where appropriate.
	<ul> <li>Type numb</li> </ul>	per BTA204-600F separa	ted from data shee	t BTA204_SERIES_D_E_F_4.
BTA204_SERIES_D_E_F_4	20030501	Product specification	-	BTA204_SERIES_D_E_F_3

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#### 9.1 Data sheet status

Document status [1] [2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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[2] The term 'short data sheet' is explained in section "Definitions".

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Date of release: 9 May 2011 Document identifier: BTA204-600F